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1

CHAPTER

Introduction to Power Quality

The subject of power quality is very broad by nature. It covers all aspects of power system engineering, from transmission and distribution level analyses to end-user problems. Therefore, electric power quality has become the concern of utilities, end users, architects, and civil engineers as well as manufacturers. These professionals must work together in developing solutions to power quality problems:

- Electric utility managers and designers must build and operate systems that take into account the interaction between customer facilities and power system. Electric utilities must understand the sensitivity of the end-use equipment to the quality of voltage.
- Customers must learn to respect the rights of their neighbors and control the quality of their nonlinear loads. Studies show that the best and the most efficient solution to power quality problems is to control them at their source. Customers can perform this by careful selection and control of their nonlinear loads and by taking appropriate actions to control and mitigate single-time disturbances and harmonics before connecting their loads to the power system.
- Architects and civil engineers must design buildings to minimize the susceptibility and vulnerability of electrical components to power quality problems.
- Manufacturers and equipment engineers must design devices that are compatible with the power system. This might mean a lower level of harmonic generation or less sensitivity to voltage distortions.
- Engineers must be able to devise ride-through capabilities of distributed generators (e.g., wind and solar generating plants).

This chapter introduces the subject of electric power quality. After a brief definition of power quality and its causes, detailed classification of the subject is presented. The formulations and measures used for power quality are explained and the impacts of poor power quality on power system and end-use devices such as appliances are mentioned. A section

is presented addressing the most important IEEE [1] and IEC [2] standards referring to power quality. The remainder of this chapter introduces issues that will be covered in the following chapters, including modeling and mitigation techniques for power quality phenomena in electric machines and power systems. This chapter contains nine application examples and ends with a summary.

1.1 DEFINITION OF POWER QUALITY

Electric power quality has become an important part of power systems and electric machines. The subject has attracted the attention of many universities and industries, and a number of books have been published in this exciting and relatively new field [3–12].

Despite important papers, articles, and books published in the area of electric power quality, its definition has not been universally agreed upon. However, nearly everybody accepts that it is a very important aspect of power systems and electric machinery with direct impacts on efficiency, security, and reliability. Various sources use the term “power quality” with different meaning. It is used synonymously with “supply reliability,” “service quality,” “voltage quality,” “current quality,” “quality of supply,” and “quality of consumption.”

Judging by the different definitions, power quality is generally meant to express the quality of voltage and/or the quality of current and can be defined as: the measure, analysis, and improvement of the bus voltage to maintain a sinusoidal waveform at rated voltage and frequency. This definition includes all momentary and steady-state phenomena.

1.2 CAUSES OF DISTURBANCES IN POWER SYSTEMS

Although a significant literature on power quality is now available, most engineers, facility managers, and consumers remain unclear as to what constitutes a power quality problem. Furthermore, due to the power system impedance, any current (or voltage) harmonic will result in the generation and propaga-

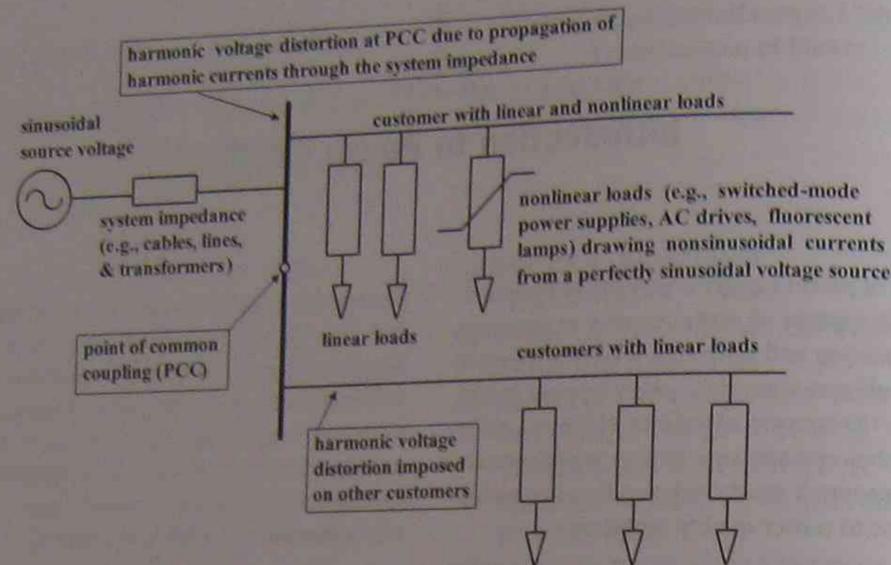


FIGURE 1.1 Propagation of harmonics (generated by a nonlinear load) in power systems.

tion of voltage (or current) harmonics and affects the entire power system. Figure 1.1 illustrates the impact of current harmonics generated by a nonlinear load on a typical power system with linear loads.

What are the origins of the power quality problem? Some references [9] divide the distortion sources into three categories: small and predictable (e.g., residential consumers generating harmonics), large and random (e.g., arc furnaces producing voltage fluctuations and flicker), and large and predictable (e.g., static converters of smelters and high-voltage DC transmission causing characteristic and uncharacteristic harmonics as well as harmonic instability). However, the likely answers to the question are these: unpredictable events, the electric utility, the customer, and the manufacturer.

Unpredictable Events. Both electric utilities and end users agree that more than 60% of power quality problems are generated by natural and unpredictable events [6]. Some of these include faults, lightning surge propagation, resonance, ferroresonance, and geomagnetically induced currents (GICs) due to solar flares [13]. These events are considered to be utility related problems.

The Electric Utility. There are three main sources of poor power quality related to utilities:

- *The point of supply generation.* Although synchronous machines generate nearly perfect sinusoidal voltages (harmonic content less than 3%), there

are power quality problems originating at generating plants which are mainly due to maintenance activity, planning, capacity and expansion constraints, scheduling, events leading to forced outages, and load transferring from one substation to another.

- *The transmission system.* Relatively few power quality problems originate in the transmission system. Typical power quality problems originating in the transmission system are galloping (under high-wind conditions resulting in supply interruptions and/or random voltage variations), lightning (resulting in a spike or transient overvoltage), insulator flashover, voltage dips (due to faults), interruptions (due to planned outages by utility), transient overvoltages (generated by capacitor and/or inductor switching, and lightning), transformer energizing (resulting in inrush currents that are rich in harmonic components), improper operation of voltage regulation devices (which can lead to long-duration voltage variations), slow voltage variations (due to a long-term variation of the load caused by the continuous switching of devices and load), flexible AC transmission system (FACTS) devices [14] and high-voltage DC (HVDC) systems [15], corona [16], power line carrier signals [17], broadband power line (BPL) communications [18], and electromagnetic fields (EMFs) [19].
- *The distribution system.* Typical power quality problems originating in the distribution system are voltage dips, spikes, and interruptions, transient

overvoltages, transformer energizing, improper operation of voltage regulation devices, slow voltage variations, power line carrier signals, BPL, and EMFs.

The Customer. Customer loads generate a considerable portion of power quality problems in today's power systems. Some end-user related problems are harmonics (generated by nonlinear loads such as power electronic devices and equipment, renewable energy sources, FACTS devices, adjustable-speed drives, uninterruptible power supplies (UPS), fax machines, laser printers, computers, and fluorescent lights), poor power factor (due to highly inductive loads such as induction motors and air-conditioning units), flicker (generated by arc furnaces [20]), transients (mostly generated inside a facility due to device switching, electrostatic discharge, and arcing), improper grounding (causing most reported customer problems), frequency variations (when secondary and backup power sources, such as diesel engine and turbine generators, are used), misapplication of technology, wiring regulations, and other relevant standards.

Manufacturing Regulations. There are two main sources of poor power quality related to manufacturing regulations:

- *Standards.* The lack of standards for testing, certification, sale, purchase, installation, and use of electronic equipment and appliances is a major cause of power quality problems.
- *Equipment sensitivity.* The proliferation of "sensitive" electronic equipment and appliances is one of the main reasons for the increase of power quality problems. The design characteristics of these devices, including computer-based equipment, have increased the incompatibility of a wide variety of these devices with the electrical environment [21].

Power quality therefore must necessarily be tackled from three fronts, namely:

- The utility must design, maintain, and operate the power system while minimizing power quality problems;
- The end user must employ proper wiring, system grounding practices, and state-of-the-art electronic devices; and
- The manufacturer must design electronic devices that keep electrical environmental disturbances to a minimum and that are immune to anomalies of the power supply line.

1.3 CLASSIFICATION OF POWER QUALITY ISSUES

To solve power quality problems it is necessary to understand and classify this relatively complicated subject. This section is based on the power quality classification and information from references [6] and [9].

There are different classifications for power quality issues, each using a specific property to categorize the problem. Some of them classify the events as "steady-state" and "non-steady-state" phenomena. In some regulations (e.g., ANSI C84.1 [22]) the most important factor is the duration of the event. Other guidelines (e.g., IEEE-519) use the wave shape (duration and magnitude) of each event to classify power quality problems. Other standards (e.g., IEC) use the frequency range of the event for the classification.

For example, IEC 61000-2-5 uses the frequency range and divides the problems into three main categories: low frequency (<9 kHz), high frequency (>9 kHz), and electrostatic discharge phenomena. In addition, each frequency range is divided into "radiated" and "conducted" disturbances. Table 1.1 shows

TABLE 1.1 Main Phenomena Causing Electromagnetic and Power Quality Disturbances [6, 9]

Conducted low-frequency phenomena
Harmonics, interharmonics
Signaling voltage
Voltage fluctuations
Voltage dips
Voltage imbalance
Power frequency variations
Induced low-frequency voltages
DC components in AC networks
Radiated low-frequency phenomena
Magnetic fields
Electric fields
Conducted high-frequency phenomena
Induced continuous wave (CW) voltages or currents
Unidirectional transients
Oscillatory transients
Radiated high-frequency phenomena
Magnetic fields
Electric fields
Electromagnetic field
Steady-state waves
Transients
Electrostatic discharge phenomena (ESD)
Nuclear electromagnetic pulse (NEMP)

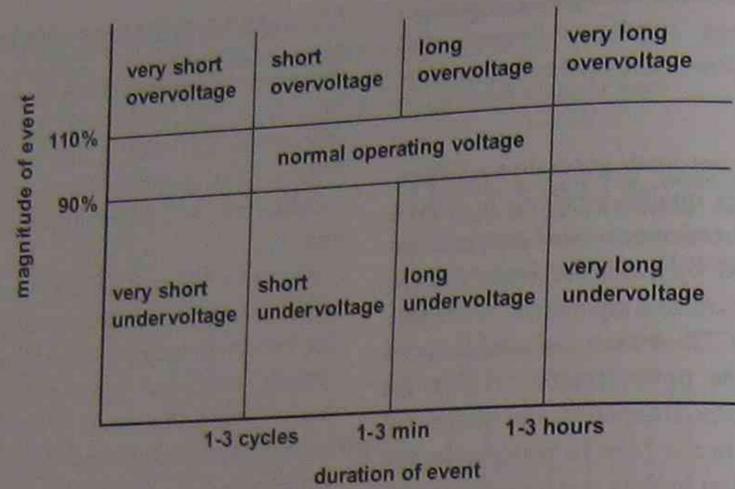


FIGURE 1.2 Magnitude-duration plot for classification of power quality events [11].

the principal phenomena causing electromagnetic disturbances according to IEC classifications [9]. All these phenomena are considered to be power quality issues; however, the two conducted categories are more frequently addressed by the industry.

The magnitude and duration of events can be used to classify power quality events, as shown in Fig. 1.2. In the magnitude-duration plot, there are nine different parts [11]. Various standards give different names to events in these parts. The voltage magnitude is split into three regions:

- interruption: voltage magnitude is zero,
- undervoltage: voltage magnitude is below its nominal value, and
- overvoltage: voltage magnitude is above its nominal value.

The duration of these events is split into four regions: very short, short, long, and very long. The borders in this plot are somewhat arbitrary and the user can set them according to the standard that is used.

IEEE standards use several additional terms (as compared with IEC terminology) to classify power quality events. Table 1.2 provides information about categories and characteristics of electromagnetic phenomena defined by IEEE-1159 [23]. These categories are briefly introduced in the remaining parts of this section.

1.3.1 Transients

Power system transients are undesirable, fast- and short-duration events that produce distortions. Their characteristics and waveforms depend on the mechanism of generation and the network parameters

(e.g., resistance, inductance, and capacitance) at the point of interest. "Surge" is often considered synonymous with transient.

Transients can be classified with their many characteristic components such as amplitude, duration, rise time, frequency of ringing polarity, energy delivery capability, amplitude spectral density, and frequency of occurrence. Transients are usually classified into two categories: impulsive and oscillatory (Table 1.2).

An impulsive transient is a sudden frequency change in the steady-state condition of voltage, current, or both that is unidirectional in polarity (Fig. 1.3). The most common cause of impulsive transients is a lightning current surge. Impulsive transients can excite the natural frequency of the system.

An oscillatory transient is a sudden frequency change in the steady-state condition of voltage, current, or both that includes both positive and negative polarity values. Oscillatory transients occur for different reasons in power systems such as appliance switching, capacitor bank switching (Fig. 1.4), fast-acting overcurrent protective devices, and ferroresonance (Fig. 1.5).

1.3.2 Short-Duration Voltage Variations

This category encompasses the IEC category of "voltage dips" and "short interruptions." According to the IEEE-1159 classification, there are three different types of short-duration events (Table 1.2): instantaneous, momentary, and temporary. Each category is divided into interruption, sag, and swell. Principal cases of short-duration voltage variations are fault conditions, large load energization, and loose connections.

TABLE 1.2 Categories and Characteristics of Electromagnetic Phenomena in Power Systems as Defined by IEEE-1159 [6, 9]

Categories	Typical spectral content	Typical duration	Typical voltage magnitude	
1. Transient	1.1. Impulsive <ul style="list-style-type: none"> • nanosecond • microsecond • millisecond 	5 ns rise 1 μ s rise 0.1 ms rise	<50 ns 50 ns–1 ms >1 ms	
	1.2. Oscillatory <ul style="list-style-type: none"> • low frequency • medium frequency • high frequency 	<5 kHz 5–500 kHz 0.5–5 MHz	0.3–50 ms 20 μ s 5 μ s	0–4 pu 0–8 pu 0–4 pu
2. Short-duration variation	2.1. Instantaneous <ul style="list-style-type: none"> • interruption • sag • swell 		0.5–30 cycles 0.5–30 cycles 0.5–30 cycles	<0.1 pu 0.1–0.9 pu 1.1–1.8 pu
	2.2. Momentary <ul style="list-style-type: none"> • interruption • sag • swell 		0.5 cycle–3 s 30 cycles–3 s 30 cycles–3 s	<0.1 pu 0.1–0.9 pu 1.1–1.4 pu
	2.3. Temporary <ul style="list-style-type: none"> • interruption • sag • swell 		3 s–1 min 3 s–1 min 3 s–1 min	<0.1 pu 0.1–0.9 pu 1.1–1.2 pu
3. Long-duration variation	3.1. Sustained interruption		>1 min	0.0 pu
	3.2. Undervoltage		>1 min	0.8–0.9 pu
	3.3. Overvoltage		>1 min	1.1–1.2 pu
4. Voltage imbalance			steady state	0.5–2%
5. Waveform distortion	5.1. DC offset		steady state	0–0.1%
	5.2. Harmonics	0–100th	steady state	0–20%
	5.3. Interharmonics	0–6 kHz	steady state	0–2%
	5.4. Notching		steady state	
	5.5. Noise	Broadband	steady state	0–1%
6. Voltage fluctuation		<25 Hz	intermittent	0.1–7%
7. Power frequency variations			<10 s	

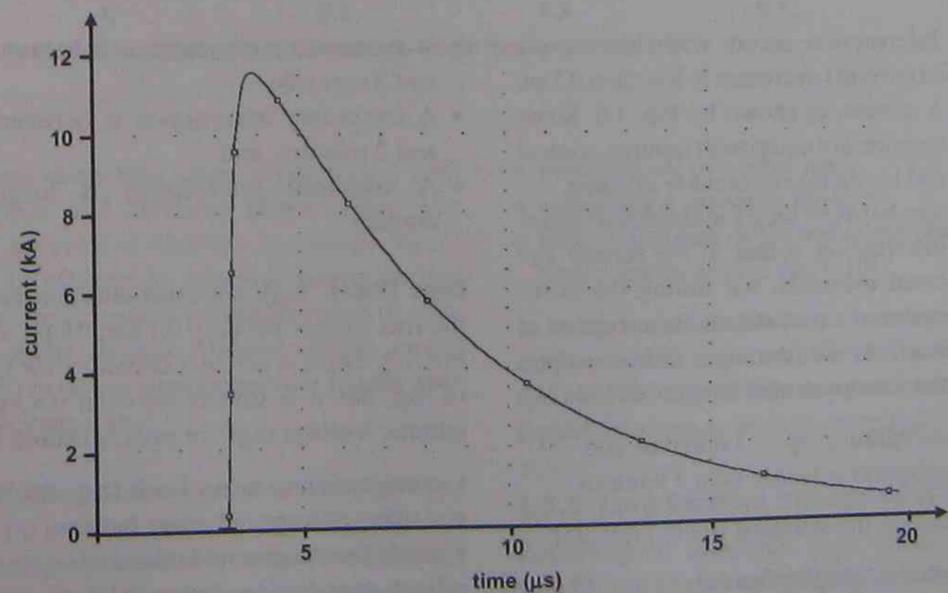


FIGURE 1.3 Impulsive transient current caused by lightning strike, result of PSpice simulation.

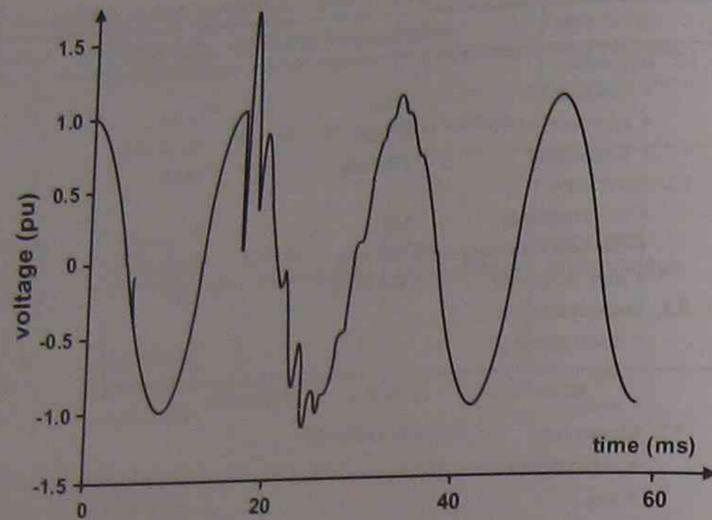


FIGURE 1.4 Low-frequency oscillatory transient caused by capacitor bank energization.

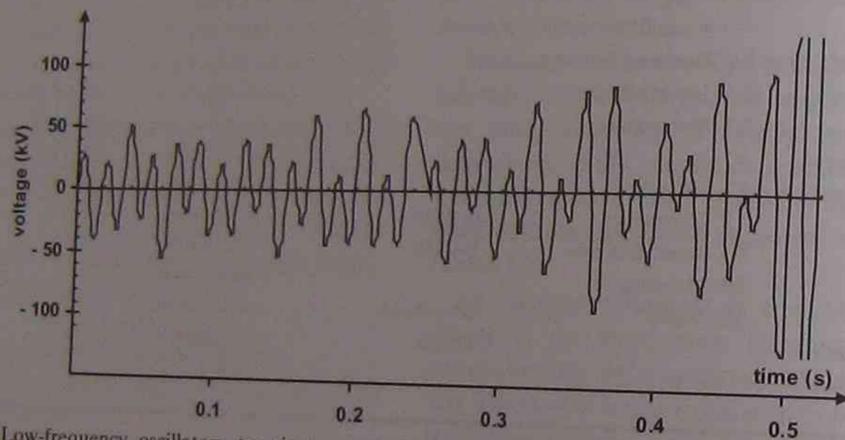


FIGURE 1.5 Low-frequency oscillatory transient caused by ferroresonance of a transformer at no load, result of Mathematica simulation.

Interruption. Interruption occurs when the supply voltage (or load current) decreases to less than 0.1 pu for less than 1 minute, as shown by Fig. 1.6. Some causes of interruption are equipment failures, control malfunction, and blown fuse or breaker opening.

The difference between long (or sustained) interruption and interruption is that in the former the supply is restored manually, but during the latter the supply is restored automatically. Interruption is usually measured by its duration. For example, according to the European standard EN-50160 [24]:

- A short interruption is up to 3 minutes; and
- A long interruption is longer than 3 minutes.

However, based on the standard IEEE-1250 [25]:

- An instantaneous interruption is between 0.5 and 30 cycles;

- A momentary interruption is between 30 cycles and 2 seconds;
- A temporary interruption is between 2 seconds and 2 minutes; and
- A sustained interruption is longer than 2 minutes.

Sags (Dips). Sags are short-duration reductions in the rms voltage between 0.1 and 0.9 pu, as shown by Fig. 1.7. There is no clear definition for the duration of sag, but it is usually between 0.5 cycles and 1 minute. Voltage sags are usually caused by

- energization of heavy loads (e.g., arc furnace),
- starting of large induction motors,
- single line-to-ground faults, and
- load transferring from one power source to another.

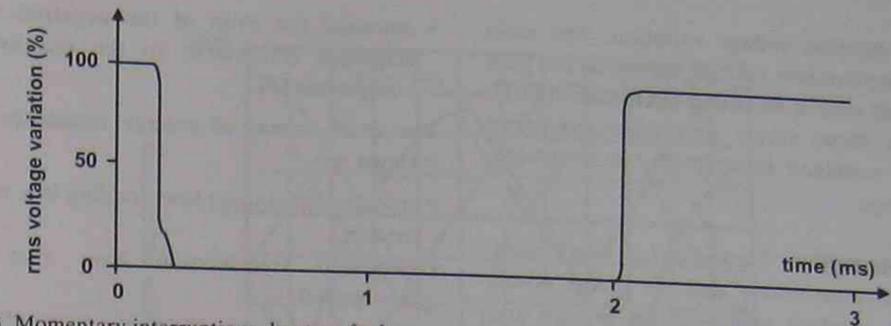


FIGURE 1.6 Momentary interruptions due to a fault.

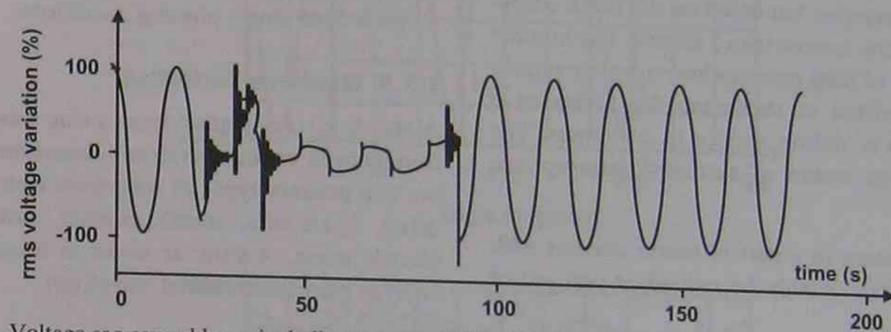


FIGURE 1.7 Voltage sag caused by a single line-to-ground (SLG) fault.

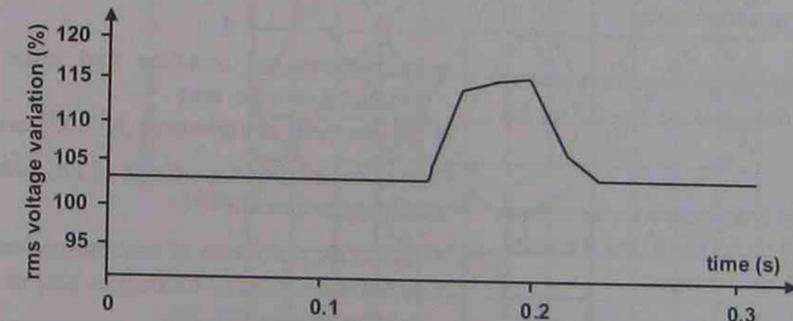


FIGURE 1.8 Instantaneous voltage swell caused by a single line-to-ground fault.

Each of these cases may cause a sag with a special (magnitude and duration) characteristic. For example, if a device is sensitive to voltage sag of 25%, it will be affected by induction motor starting [11]. Sags are main reasons for malfunctions of electrical low-voltage devices. Uninterruptible power supply (UPS) or power conditioners are mostly used to prevent voltage sags.

Swells. The increase of voltage magnitude between 1.1 and 1.8 pu is called swell, as shown by Fig. 1.8. The most accepted duration of a swell is from 0.5 cycles to 1 minute [7]. Swells are not as common as sags and their main causes are

- switching off of a large load,
- energizing a capacitor bank, or
- voltage increase of the unfaulted phases during a single line-to-ground fault [10].

In some textbooks the term “momentary overvoltage” is used as a synonym for the term swell. As in the case of sags, UPS or power conditioners are typical solutions to limit the effect of swell [10].

1.3.3 Long-Duration Voltage Variations

According to standards (e.g., IEEE-1159, ANSI-C84.1), the deviation of the rms value of voltage from the nominal value for longer than 1 minute is

called long-duration voltage variation. The main causes of long-duration voltage variations are load variations and system switching operations. IEEE-1159 divides these events into three categories (Table 1.2): sustained interruption, undervoltage, and overvoltage.

Sustained Interruption. Sustained (or long) interruption is the most severe and the oldest power quality event at which voltage drops to zero and does not return automatically. According to the IEC definition, the duration of sustained interruption is more than 3 minutes; but based on the IEEE definition the duration is more than 1 minute. The number and duration of long interruptions are very important characteristics in measuring the ability of a power system to deliver service to customers. The most important causes of sustained interruptions are

- fault occurrence in a part of power systems with no redundancy or with the redundant part out of operation,
- an incorrect intervention of a protective relay leading to a component outage, or
- scheduled (or planned) interruption in a low-voltage network with no redundancy.

Undervoltage. The undervoltage condition occurs when the rms voltage decreases to 0.8–0.9 pu for more than 1 minute.

Overvoltage. Overvoltage is defined as an increase in the rms voltage to 1.1–1.2 pu for more than 1 minute. There are three types of overvoltages:

- overvoltages generated by an insulation fault, ferroresonance, faults with the alternator regulator, tap changer transformer, or overcompensation;
- lightning overvoltages; and
- switching overvoltages produced by rapid modifications in the network structure such as opening of protective devices or the switching on of capacitive circuits.

1.3.4 Voltage Imbalance

When voltages of a three-phase system are not identical in magnitude and/or the phase differences between them are not exactly 120 degrees, voltage imbalance occurs [10]. There are two ways to calculate the degree of imbalance:

- divide the maximum deviation from the average of three-phase voltages by the average of three-phase voltages, or

- compute the ratio of the negative- (or zero-) sequence component to the positive-sequence component [7].

The main causes of voltage imbalance in power systems are

- unbalanced single-phase loading in a three-phase system,
- overhead transmission lines that are not transposed,
- blown fuses in one phase of a three-phase capacitor bank, and
- severe voltage imbalance (e.g., >5%), which can result from single phasing conditions.

1.3.5 Waveform Distortion

A steady-state deviation from a sine wave of power frequency is called waveform distortion [7]. There are five primary types of waveform distortions: DC offset, harmonics, interharmonics, notching, and electric noise. A Fourier series is usually used to analyze the nonsinusoidal waveform.

DC Offset. The presence of a DC current and/or voltage component in an AC system is called DC offset [7]. Main causes of DC offset in power systems are

- employment of rectifiers and other electronic switching devices, and
- geomagnetic disturbances [6, 7, 13] causing GICs.

The main detrimental effects of DC offset in alternating networks are

- half-cycle saturation of transformer core [26–28],
- generation of even harmonics [26] in addition to odd harmonics [29, 30],
- additional heating in appliances leading to a decrease of the lifetime of transformers [31–36], rotating machines, and electromagnetic devices, and
- electrolytic erosion of grounding electrodes and other connectors.

Figure 1.9a shows strong half-cycle saturation in a transformer due to DC magnetization and the influence of the tank, and Fig. 1.9b exhibits less half-cycle saturation due to DC magnetization and the absence of any tank. One concludes that to suppress DC currents due to rectifiers and geomagnetically induced currents, three-limb transformers with a relatively large air gap between core and tank should be used.

Harmonics. Harmonics are sinusoidal voltages or currents with frequencies that are integer multiples of the power system (fundamental) frequency (usually, $f = 50$ or 60 Hz). For example, the frequency

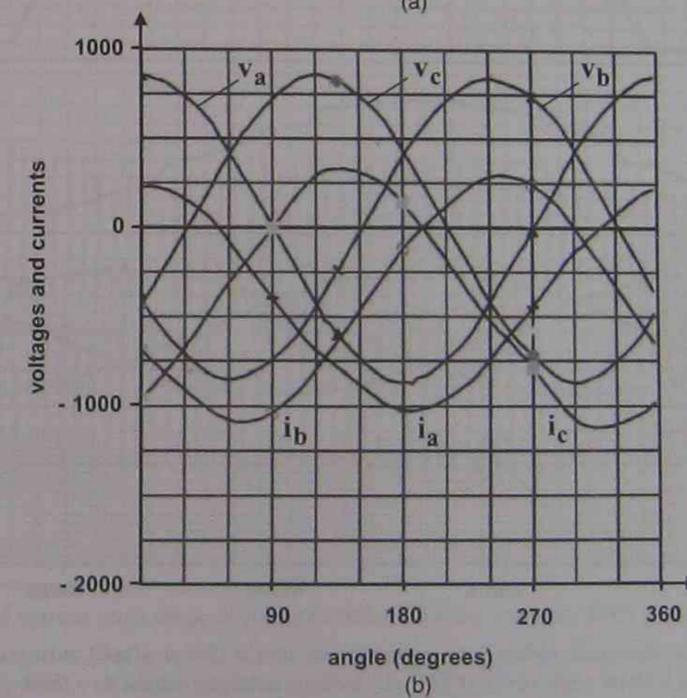
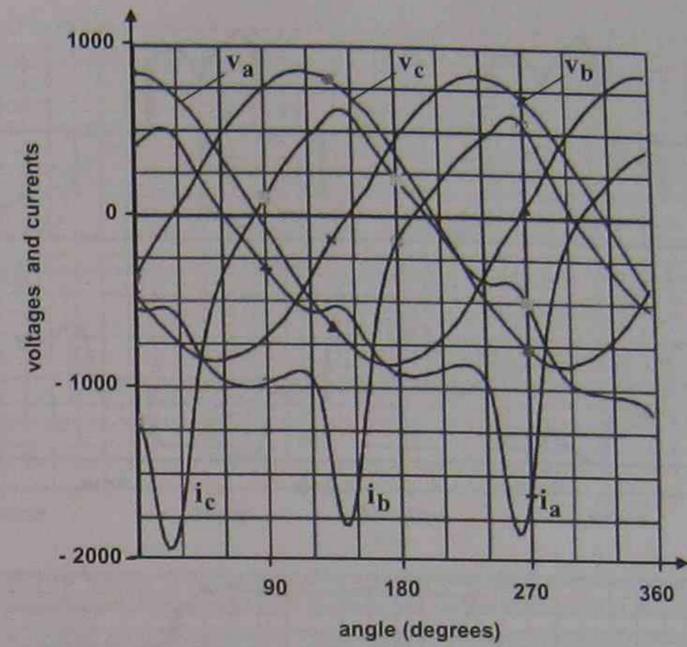


FIGURE 1.9 Measured voltages and currents at balanced DC bias current $I_{DC} = -2$ A for a 2.3 kVA three-limb transformer (a) at full load with tank (note the strong half-cycle saturation) and (b) at full load without tank (note the reduced half-cycle saturation) [27]. Dividing the ordinate values by 2.36 and 203 the voltages in volts and the currents in amperes are obtained, respectively.

of the h th harmonic is (hf). Periodic nonsinusoidal waveforms can be subjected to Fourier series and can be decomposed into the sum of fundamental component and harmonics. Main sources of harmonics in power systems are

- industrial nonlinear loads (Fig. 1.10) such as power electronic equipment, for example, drives (Fig.

1.10a), rectifiers (Fig. 1.10b,c), inverters, or loads generating electric arcs, for example, arc furnaces, welding machines, and lighting, and

- residential loads with switch-mode power supplies such as television sets, computers (Fig. 1.11), and fluorescent and energy-saving lamps.

Some detrimental effects of harmonics are

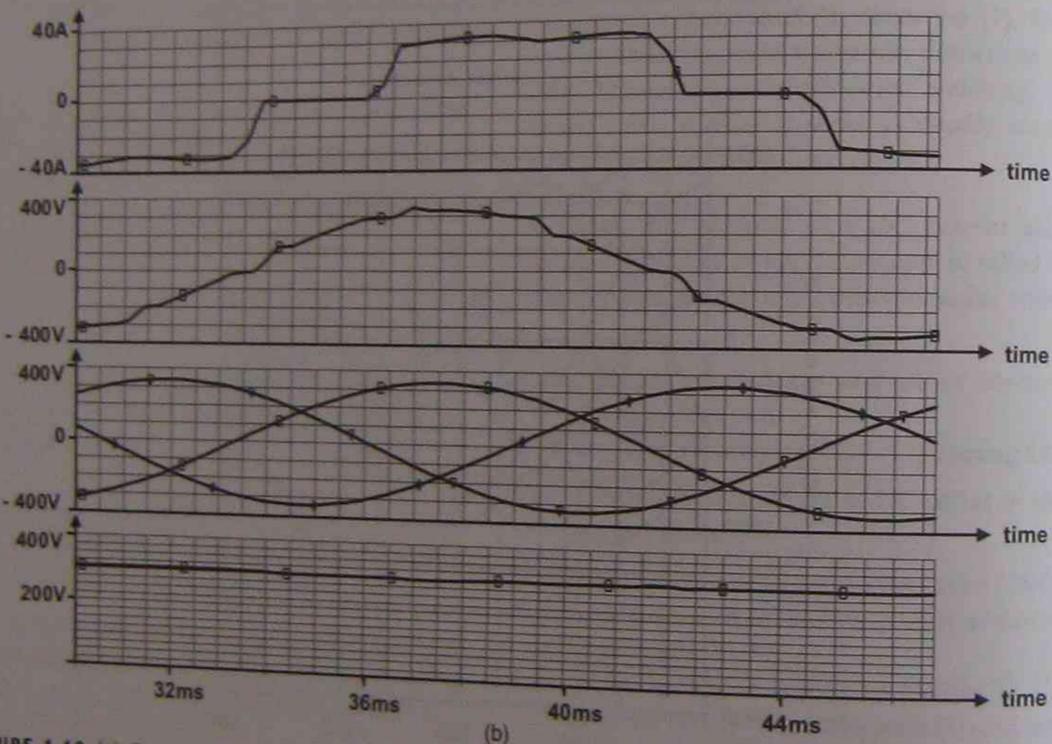
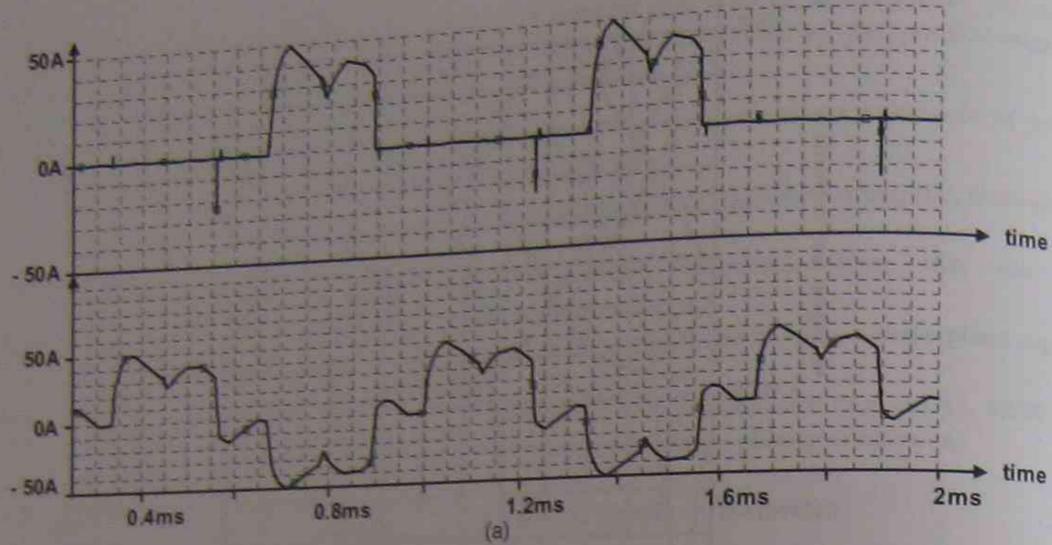


FIGURE 1.10 (a) Computed electronic switch (upper graph) and motor (lower graph) currents of an adjustable-speed brushless DC motor drive for a phase angle of $\theta = 0^\circ$ [29]. (b) Voltage notching caused by a three-phase rectifier for a firing angle of $\alpha = 50^\circ$, result of PSpice simulation. Top: phase current; second from top: line-to-line voltage of rectifier; third from top: line-to-line voltages of infinite bus; bottom: DC output voltage of rectifier.

- maloperation of control devices,
- additional losses in capacitors, transformers, and rotating machines,
- additional noise from motors and other apparatus,
- telephone interference, and
- causing parallel and series resonance frequencies (due to the power factor correction capacitor and cable capacitance), resulting in voltage amplification

tion even at a remote location from the distorting load.

Recommended solutions to reduce and control harmonics are applications of high-pulse rectification, passive, active, and hybrid filters, and custom power devices such as active-power line conditioners (APLCs) and unified power quality conditioners (UPOCs).

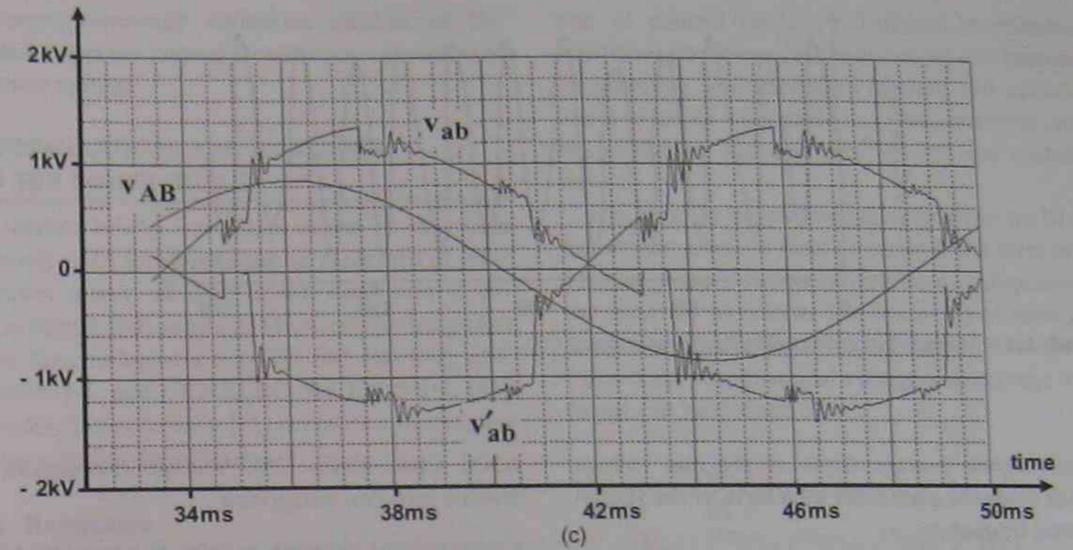


FIGURE 1.10 (continued) (c) Voltage notching caused by a three-phase rectifier with interphase reactor for a firing angle of $\alpha = 0^\circ$, result of PSpice simulation. Waveshapes with notches: line-to-line voltages of rectifier, V_{ab} and V'_{ab} being the line-to-line voltages of the two voltage systems; sinusoidal waveshape: line-to-line voltage of infinite bus.

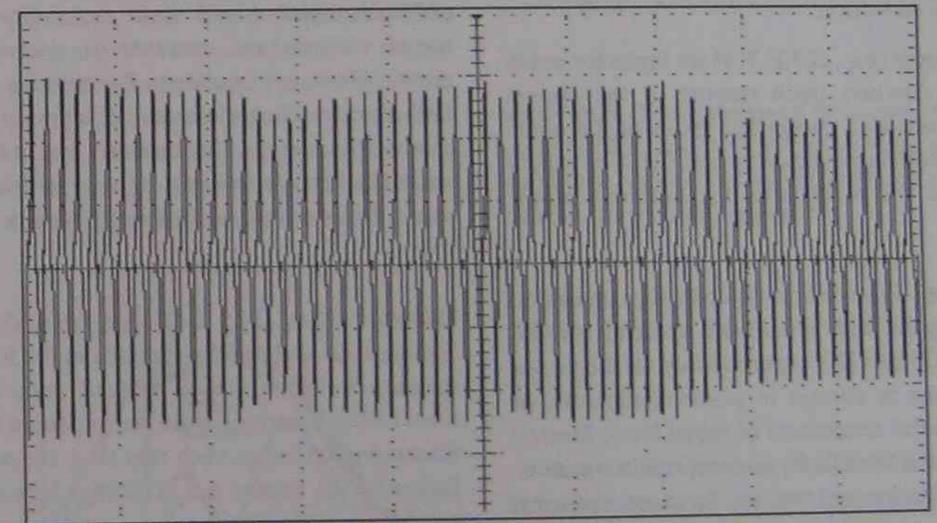


FIGURE 1.11 Measured current wave shape of state-of-the-art personal computer (PC) (many periods) [45].

Interharmonics. Interharmonics are discussed in Section 1.4.1. Their frequencies are not integer multiples of the fundamental frequency.

Notching. A periodic voltage disturbance caused by line-commutated thyristor circuits is called notching. The notching appears in the line voltage waveform during normal operation of power electronic devices when the current commutates from one phase to another. During this notching period, there exists a momentary short-circuit between the two

commutating phases, reducing the line voltage; the voltage reduction is limited only by the system impedance.

Notching is repetitive and can be characterized by its frequency spectrum (Figs. 1.10b,c). The frequency of this spectrum is quite high. Usually it is not possible to measure it with equipment normally used for harmonic analysis. Notches can impose extra stress on the insulation of transformers, generators, and sensitive measuring equipment.

Notching can be characterized by the following properties:

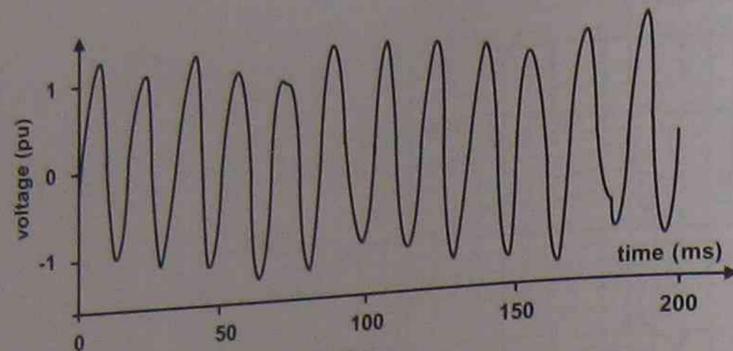


FIGURE 1.12 Voltage flicker caused by arc furnace operation.

- **Notch depth:** average depth of the line voltage notch from the sinusoidal waveform at the fundamental frequency;
- **Notch width:** the duration of the commutation process;
- **Notch area:** the product of notch depth and width; and
- **Notch position:** where the notch occurs on the sinusoidal waveform.

Some standards (e.g., IEEE-519) set limits for notch depth and duration (with respect to the system impedance and load current) in terms of the notch depth, the total harmonic distortion THD, of supply voltage, and the notch area for different supply systems.

Electric Noise. Electric noise is defined as unwanted electrical signals with broadband spectral content lower than 200 kHz [37] superimposed on the power system voltage or current in phase conductors, or found on neutral conductors or signal lines. Electric noise may result from faulty connections in transmission or distribution systems, arc furnaces, electrical furnaces, power electronic devices, control circuits, welding equipment, loads with solid-state rectifiers, improper grounding, turning off capacitor banks, adjustable-speed drives, corona, and broadband power line (BPL) communication circuits. The problem can be mitigated by using filters, line conditioners, and dedicated lines or transformers. Electric noise impacts electronic devices such as microcomputers and programmable controllers.

1.3.6 Voltage Fluctuation and Flicker

Voltage fluctuations are systemic variations of the voltage envelope or random voltage changes, the magnitude of which does not normally exceed specified voltage ranges (e.g., 0.9 to 1.1 pu as defined by

ANSI C84.1-1982) [38]. Voltage fluctuations are divided into two categories:

- step-voltage changes, regular or irregular in time, and
- cyclic or random voltage changes produced by variations in the load impedances.

Voltage fluctuations degrade the performance of the equipment and cause instability of the internal voltages and currents of electronic equipment. However, voltage fluctuations less than 10% do not affect electronic equipment. The main causes of voltage fluctuation are pulsed-power output, resistance welders, start-up of drives, arc furnaces, drives with rapidly changing loads, and rolling mills.

Flicker. Flicker (Fig. 1.12) has been described as “continuous and rapid variations in the load current magnitude which causes voltage variations.” The term flicker is derived from the impact of the voltage fluctuation on lamps such that they are perceived to flicker by the human eye. This may be caused by an arc furnace, one of the most common causes of the voltage fluctuations in utility transmission and distribution systems.

1.3.7 Power-Frequency Variations

The deviation of the power system fundamental frequency from its specified nominal value (e.g., 50 or 60 Hz) is defined as power frequency variation [39]. If the balance between generation and demand (load) is not maintained, the frequency of the power system will deviate because of changes in the rotational speed of electromechanical generators. The amount of deviation and its duration of the frequency depend on the load characteristics and response of the generation control system to load changes. Faults of the power transmission system can

also cause frequency variations outside of the accepted range for normal steady-state operation of the power system.

1.4 FORMULATIONS AND MEASURES USED FOR POWER QUALITY

This section briefly introduces some of the most commonly used formulations and measures of electric power quality as used in this book and as defined in standard documents. Main sources for power quality terminologies are IEEE Std 100 [40], IEC Std 61000-1-1, and CENELEC Std EN 50160 [41]. Appendix C of reference [11] presents a fine survey of power quality definitions.

1.4.1 Harmonics

Nonsinusoidal current and voltage waveforms (Figs. 1.13 to 1.20) occur in today's power systems due to equipment with nonlinear characteristics such as transformers, rotating electric machines, FACTS devices, power electronics components (e.g., rectifiers, triacs, thyristors, and diodes with capacitor smoothing, which are used extensively in PCs, audio, and video equipment), switch-mode power supplies, compact fluorescent lamps, induction furnaces, adjustable AC and DC drives, arc furnaces, welding tools, renewable energy sources, and HVDC networks. The main effects of harmonics are malopera-

tion of control devices, telephone interferences, additional line losses (at fundamental and harmonic frequencies), and decreased lifetime and increased losses in utility equipment (e.g., transformers, rotating machines, and capacitor banks) and customer devices.

The periodic nonsinusoidal waveforms can be formulated in terms of Fourier series. Each term in the Fourier series is called the harmonic component of the distorted waveform. The frequency of harmonics are integer multiples of the fundamental frequency. Therefore, nonsinusoidal voltage and current waveforms can be defined as

$$\begin{aligned} v(t) &= V_{DC} + \sum_{h=1}^n V_{rms}^{(h)} \cos(h\omega_o t + \alpha_h) \\ &= V_{DC} + v^{(1)}(t) + v^{(2)}(t) + v^{(3)}(t) + v^{(4)}(t) + \dots, \end{aligned} \quad (1-1a)$$

$$\begin{aligned} i(t) &= I_{DC} + \sum_{h=1}^n I_{rms}^{(h)} \cos(h\omega_o t + \beta_h) \\ &= I_{DC} + i^{(1)}(t) + i^{(2)}(t) + i^{(3)}(t) + i^{(4)}(t) + \dots, \end{aligned} \quad (1-1b)$$

where ω_o is the fundamental frequency, h is the harmonic order, and $V^{(h)}$, $I^{(h)}$, α_h , and β_h are the rms amplitude values and phase shifts of voltage and current for the h th harmonic.

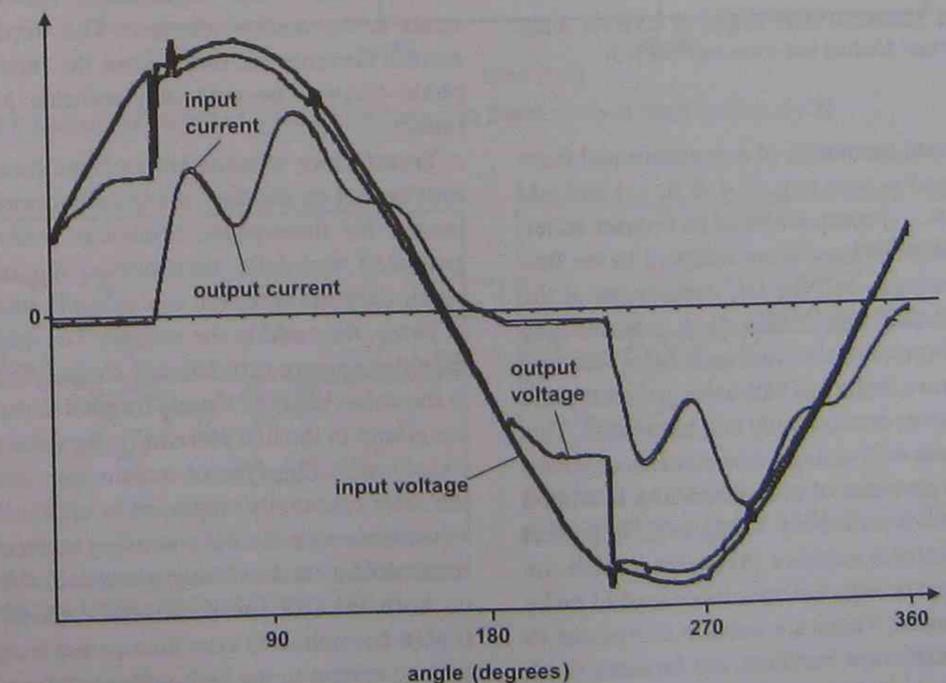


FIGURE 1.13 Measured wave shapes of single-phase induction motor fed by thyristor/triac controller at rated operation [42].

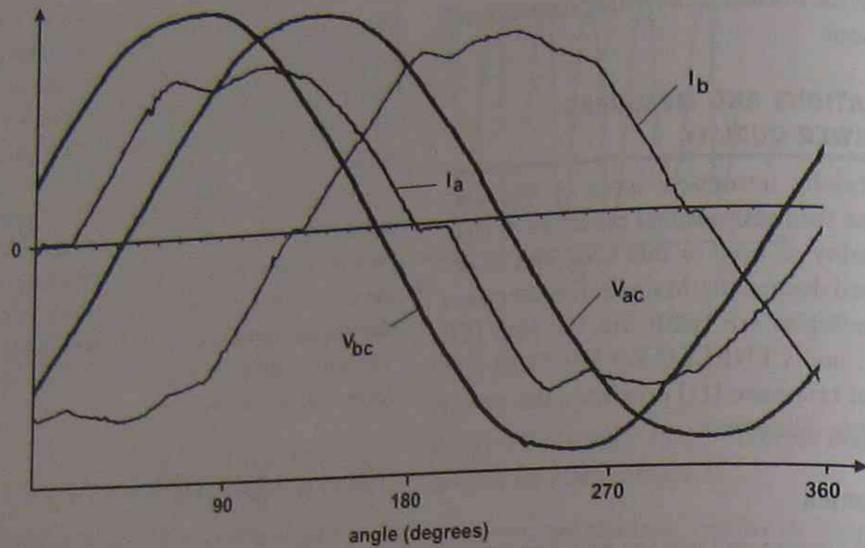


FIGURE 1.14 Measured wave shapes of three-phase induction motor fed by thyristor/triac controller at rated operation [42].

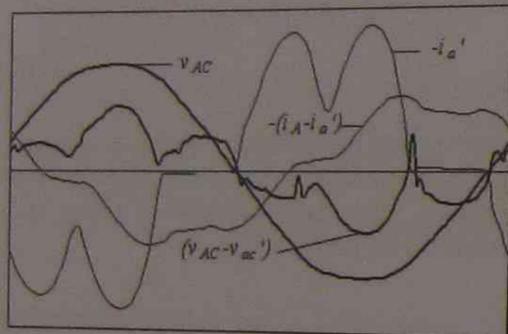


FIGURE 1.15 Measured wave shapes of 4.5 kVA three-phase transformer feeding full-wave rectifier [43].

Even and odd harmonics of a nonsinusoidal function correspond to even (e.g., 2, 4, 6, 8, ...) and odd (e.g., 3, 5, 7, 9, ...) components of its Fourier series. Harmonics of order 1 and 0 are assigned to the fundamental frequency and the DC component of the waveform, respectively. When both positive and negative half-cycles of the waveform have identical shapes, the wave shape has half-wave symmetry and the Fourier series contains only odd harmonics. This is the usual case with voltages and currents of power systems. The presence of even harmonics is often a clue that there is something wrong (e.g., imperfect gating of electronic switches [42]), either with the load equipment or with the transducer used to make the measurement. There are notable exceptions to this such as half-wave rectifiers, arc furnaces (with random arcs), and the presence of GICs in power systems [27].

Triplen Harmonics. Triplen harmonics (Fig. 1.21) are the odd multiples of the third harmonic ($h=3, 9, 15, 21, \dots$). These harmonic orders become an important issue for grounded-wye systems with current flowing in the neutral line of a wye configuration. Two typical problems are overloading of the neutral conductor and telephone interference.

For a system of perfectly balanced three-phase nonsinusoidal loads, fundamental current components in the neutral are zero. The third harmonic neutral currents are three times the third-harmonic phase currents because they coincide in phase or time.

Transformer winding connections have a significant impact on the flow of triplen harmonic currents caused by three-phase nonlinear loads. For the grounded wye-delta transformer, the triplen harmonic currents enter the wye side and since they are in phase, they add in the neutral. The delta winding provides ampere-turn balance so that they can flow in the delta, but they remain trapped in the delta and are absent in the line currents of the delta side of the transformer. This type of transformer connection is the most commonly employed in utility distribution substations with the delta winding connected to the transmission feeder. Using grounded-wye windings on both sides of the transformer allows balanced triplen harmonics to flow unimpeded from the low-voltage system to the high-voltage system. They will be present in equal proportion on both sides of a transformer.

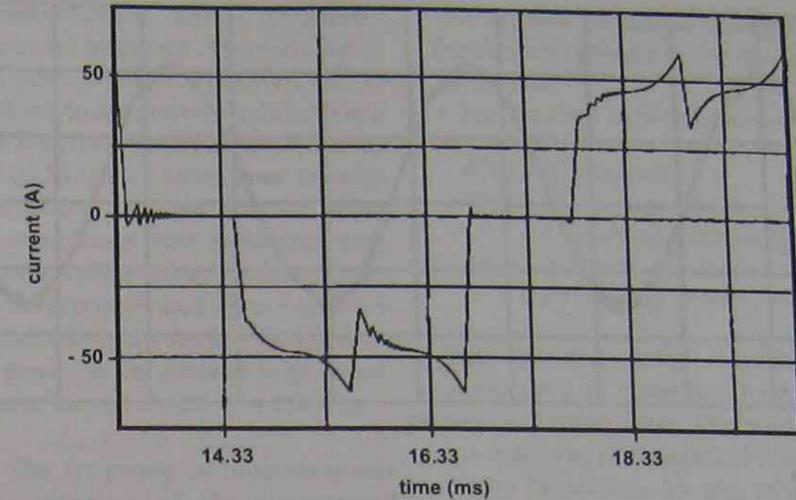


FIGURE 1.16 Calculated current of brushless DC motor in full-on mode at rated operation [29].

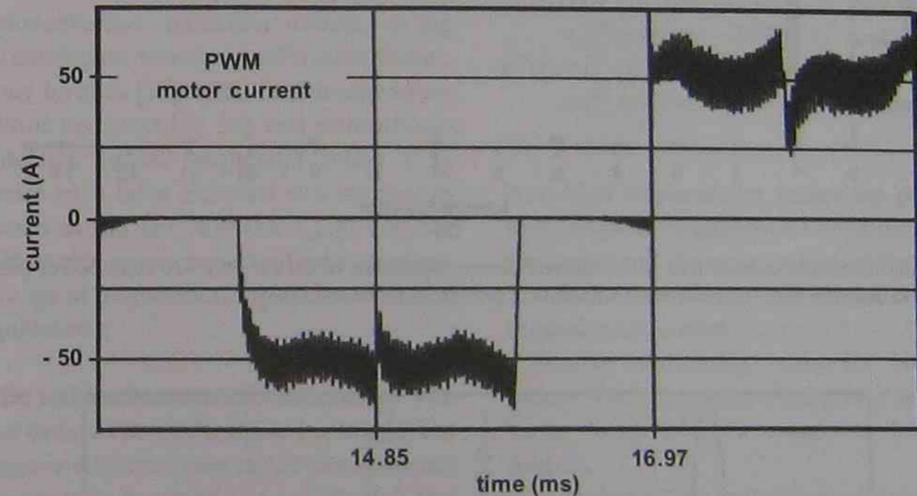


FIGURE 1.17 Calculated current of brushless DC motor in PWM mode at rated operation [29].

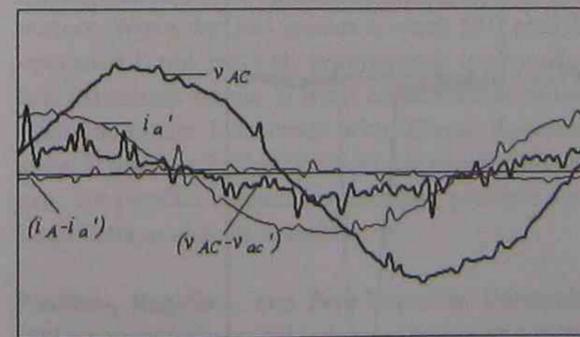


FIGURE 1.18 Measured wave shapes of 15 kVA three-phase transformer feeding resonant rectifier [43].

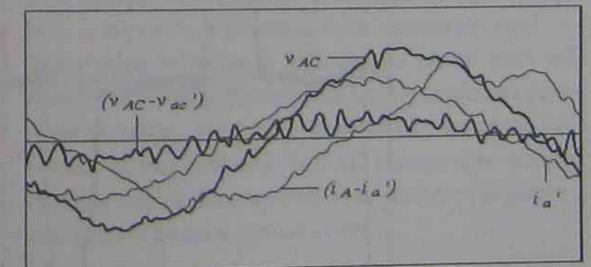


FIGURE 1.19 Measured wave shapes of 15 kVA three-phase transformer fed by PWM inverter [43].

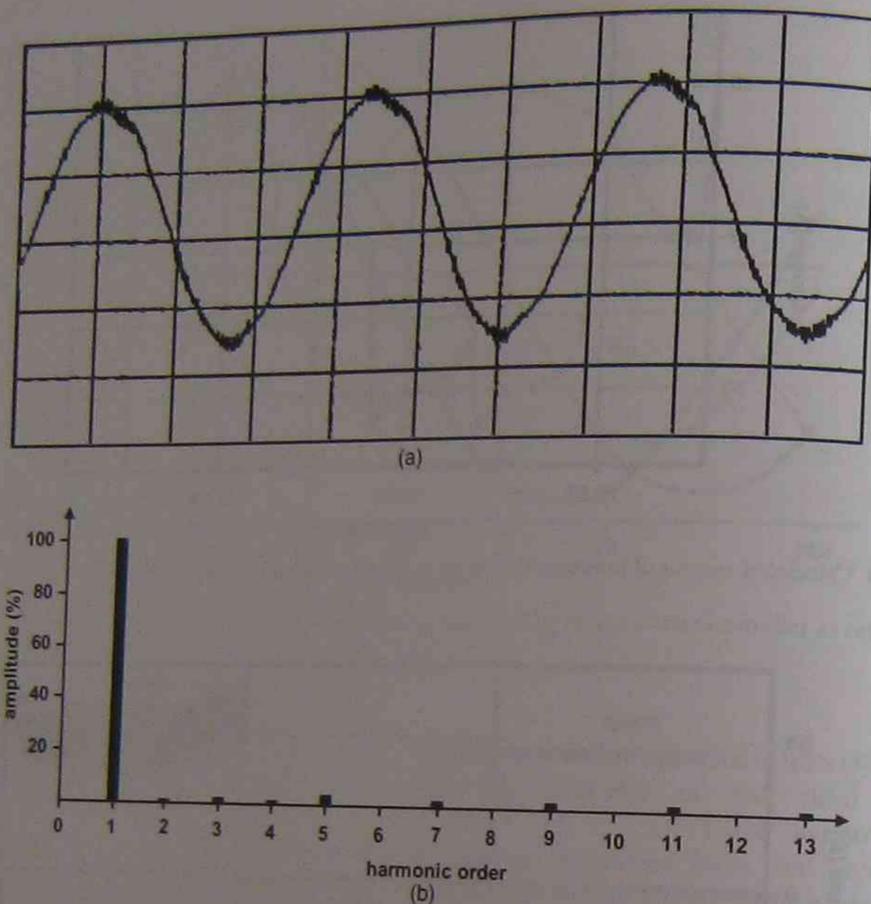


FIGURE 1.20 (a) Measured current and (b) measured current spectrum of 20 kW/25 kVA wind-power plant supplying power via inverter into the 240 V three-phase distribution system at rated load [44].

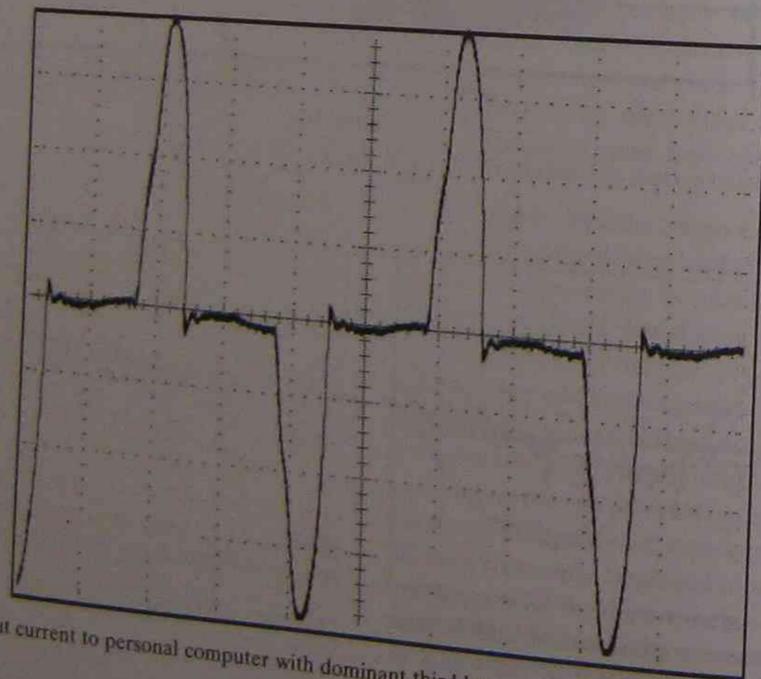


FIGURE 1.21 Input current to personal computer with dominant third harmonic [45].

Subharmonics. Subharmonics have frequencies below the fundamental frequency. There are rarely subharmonics in power systems. However, due to the fast control of electronic power supplies of computers, inter- and subharmonics are generated in the input current (Fig. 1.11) [45]. Resonance between the harmonic currents or voltages with the power system (series) capacitance and inductance may cause subharmonics, called subsynchronous resonance [46]. They may be generated when a system is highly inductive (such as an arc furnace during start-up) or when the power system contains large capacitor banks for power factor correction or filtering.

Interharmonics. The frequency of interharmonics are not integer multiples of the fundamental frequency. Interharmonics appear as discrete frequencies or as a band spectrum. Main sources of interharmonic waveforms are static frequency converters, cycloconverters, induction motors, arcing devices, and computers. Interharmonics cause flicker, low-frequency torques [32], additional temperature rise in induction machines [33, 34], and malfunctioning of protective (under-frequency) relays [35]. Interharmonics have been included in a number of guidelines such as the IEC 61000-4-7 [36] and the IEEE-519. However, many important related issues, such as the range of frequencies, should be addressed in revised guidelines.

Characteristic and Uncharacteristic Harmonics. The harmonics of orders $12k + 1$ (positive sequence) and $12k - 1$ (negative sequence) are called characteristic and uncharacteristic harmonics, respectively. The amplitudes of these harmonics are inversely proportional to the harmonic order. Filters are used to reduce characteristic harmonics of large power converters. When the AC system is weak [47] and the operation is not perfectly symmetrical, uncharacteristic harmonics appear. It is not economical to reduce uncharacteristic harmonics with filters; therefore, even a small injection of these harmonic currents can, via parallel resonant conditions, produce very large voltage distortion levels.

Positive-, Negative-, and Zero-Sequence Harmonics [48]. Assuming a positive-phase (abc) sequence balanced three-phase power system, the expressions for the fundamental currents are

$$\begin{aligned} i_a(t) &= I_a^{(1)} \cos(\omega_s t) \\ i_b(t) &= I_b^{(1)} \cos(\omega_s t - 120^\circ) \\ i_c(t) &= I_c^{(1)} \cos(\omega_s t - 240^\circ). \end{aligned} \quad (1-2)$$

The negative displacement angles indicate that the fundamental phasors rotate clockwise in the space-time plane.

For the third harmonic (zero-sequence) currents,

$$\begin{aligned} i_a^{(3)}(t) &= I_a^{(3)} \cos(3\omega_s t) \\ i_b^{(3)}(t) &= I_b^{(3)} \cos(3\omega_s t - 120^\circ) \\ &= I_b^{(3)} \cos(3\omega_s t - 360^\circ) = I_b^{(3)} \cos(3\omega_s t) \\ i_c^{(3)}(t) &= I_c^{(3)} \cos(3\omega_s t - 240^\circ) \\ &= I_c^{(3)} \cos(3\omega_s t - 720^\circ) = I_c^{(3)} \cos(3\omega_s t). \end{aligned} \quad (1-3)$$

This equation shows that the third harmonic phasors are in phase and have zero displacement angles between them. The third harmonic currents are known as zero-sequence harmonics.

The expressions for the fifth harmonic currents are

$$\begin{aligned} i_a^{(5)}(t) &= I_a^{(5)} \cos(5\omega_s t) \\ i_b^{(5)}(t) &= I_b^{(5)} \cos(5\omega_s t - 120^\circ) = I_b^{(5)} \cos(5\omega_s t - 600^\circ) \\ &= I_b^{(5)} \cos(5\omega_s t - 240^\circ) = I_b^{(5)} \cos(5\omega_s t + 120^\circ) \\ i_c^{(5)}(t) &= I_c^{(5)} \cos(5\omega_s t - 240^\circ) = I_c^{(5)} \cos(5\omega_s t - 1200^\circ) \\ &= I_c^{(5)} \cos(5\omega_s t - 120^\circ) = I_c^{(5)} \cos(5\omega_s t + 240^\circ). \end{aligned} \quad (1-4)$$

Note that displacement angles are positive; therefore, the phase sequence of this harmonic is counterclockwise and opposite to that of the fundamental. The fifth harmonic currents are known as negative-sequence harmonics.

Similar relationships exist for other harmonic orders. Table 1.3 categorizes power system harmonics in terms of their respective frequencies and sources.

Note that although the harmonic phase-shift angle has the effect of altering the shape of the composite waveform (e.g., adding a third harmonic component with 0 degree phase shift to the fundamental results in a composite waveform with maximum peak-to-peak value whereas a 180 degree phase shift will result in a composite waveform with minimum peak-to-peak value), the phase-sequence order of the harmonics is not affected. Not all voltage and current systems can be decomposed into positive-, negative-, and zero-sequence systems [49].

Time and Spatial (Space) Harmonics. Time harmonics are the harmonics in the voltage and current waveforms of electric machines and power systems due to magnetic core saturation, presence of nonlinear loads, and irregular system conditions (e.g., faults and imbalance). Spatial (space) harmonics are referred to the harmonics in the flux linkage of rotating electromagnetic devices such as induction and

TABLE 1.3 Types and Sources of Power System Harmonics

Type	Frequency	Source
DC	0	Electronic switching devices, half-wave rectifiers, arc furnaces (with random arcs), geomagnetic induced currents (GICs)
Odd harmonics	$h \cdot f$, ($h = \text{odd}$)	Nonlinear loads and devices
Even harmonics	$h \cdot f$, ($h = \text{even}$)	Half-wave rectifiers, geomagnetic induced currents (GICs)
Triplen harmonics	$3k \cdot f$, ($k = 1, 2, 3, 4, \dots$)	Unbalanced three-phase load, electronic switching devices
Positive-sequence harmonics	$h \cdot f$, ($h = 1, 4, 7, 10, \dots$)	Operation of power system with nonlinear loads
Negative-sequence harmonics	$h \cdot f$, ($h = 2, 5, 8, 11, \dots$)	Operation of power system with nonlinear loads
Zero-sequence harmonics	$h \cdot f$, ($h = 3, 6, 9, 12, \dots$) (same as triplen harmonics)	Unbalanced operation of power system
Time harmonics	$h \cdot f$, ($h = \text{an integer}$)	Voltage and current source inverters, pulse-width modulated rectifiers, switch-mode rectifiers and inverters
Spatial harmonics	$h \cdot f$, ($h = \text{an integer}$)	Induction machines
Interharmonic	$h \cdot f$, ($h = \text{not an integer multiple of } f$)	Static frequency converters, cycloconverters, induction machines, arcing devices, computers
Subharmonic	$h \cdot f$, ($h < 1$) and not an integer multiple of f , e.g., $h = 15 \text{ Hz}, 30 \text{ Hz}$	Fast control of power supplies, subsynchronous resonances, large capacitor banks in highly inductive systems, induction machines
Characteristic harmonic	$(12k + 1) \cdot f$, ($k = \text{integer}$)	Rectifiers, inverters
Uncharacteristic harmonic	$(12k - 1) \cdot f$, ($k = \text{integer}$)	Weak and unsymmetrical AC systems

synchronous machines. The main cause of spatial harmonics is the unsymmetrical physical structure of stator and rotor magnetic circuits (e.g., selection of number of slots and rotor eccentricity). Spatial harmonics of flux linkages will induce time harmonic voltages in the rotor and stator circuits that generate time harmonic currents.

1.4.2 The Average Value of a Nonsinusoidal Waveform

The average value of a sinusoidal waveform is defined as

$$I_{\text{ave}} = \frac{1}{T} \int_0^T i(t) dt \quad (1-5)$$

For the nonsinusoidal current of Eq. 1-1,

$$I_{\text{ave}} = \frac{1}{T} \int_0^T i(t) dt = \frac{1}{T} \int_0^T [I_{\text{DC}} + I_{\text{max}}^{(1)} \cos(\omega t + \beta_1) + I_{\text{max}}^{(2)} \cos(2\omega t + \beta_2) + \dots] dt \quad (1-6)$$

Since all harmonics are sinusoids, the average value of a nonsinusoidal function is equal to its DC value:

$$I_{\text{ave}} = I_{\text{DC}} \quad (1-7)$$

1.4.3 The rms Value of a Nonsinusoidal Waveform

The rms value of a sinusoidal waveform is defined as

$$I_{\text{rms}} = \sqrt{\frac{1}{T} \int_0^T i^2(t) dt} = \sqrt{\frac{1}{2\pi} \int_0^{2\pi} I_{\text{max}}^2 \cos^2(\omega t) dt} = \frac{1}{\sqrt{2}} I_{\text{max}} = \frac{I_{\text{max}}}{\sqrt{2}} \quad (1-8)$$

For the nonsinusoidal current of Eq. 1-1,

$$I_{\text{rms}} = \sqrt{\frac{1}{T} \int_0^T i^2(t) dt} = \sqrt{\frac{1}{T} \int_0^T [I_{\text{DC}} + I_{\text{max}}^{(1)} \cos(\omega t + \beta_1) + I_{\text{max}}^{(2)} \cos(2\omega t + \beta_2) + I_{\text{max}}^{(3)} \cos(3\omega t + \beta_3) + \dots]^2 dt} \quad (1-9)$$

This equation contains two parts:

- The first part is the sum of the squares of harmonics:

$$\sum_{p=1}^n (I_{\text{max}}^{(p)})^2 \cos^2(p\omega t + \beta_p) \quad (1-10)$$

- The second part is the sum of the products of harmonics:

$$\sum_{p=1}^n \sum_{q=1}^n I_{\text{max}}^{(p)} I_{\text{max}}^{(q)} \cos(p\omega t + \beta_p) \cos(q\omega t + \beta_q), \quad p \neq q \quad (1-11)$$

After some simplifications it can be shown that the average of the second part is zero, and the first part becomes

$$I_{\text{rms}} = \sqrt{\int_0^{2\pi} \left[\sum_{p=1}^n (I_{\text{max}}^{(p)})^2 \cos^2(p\omega t + \beta_p) \right] d(2\omega t)} = \sqrt{\int_0^{2\pi} [(I_{\text{max}}^{(1)})^2 \cos^2(\omega t + \beta_1)] d(\omega t) + \int_0^{2\pi} [(I_{\text{max}}^{(2)})^2 \cos^2(2\omega t + \beta_2)] d(2\omega t) + \dots} \quad (1-12)$$

Therefore, the rms value of a nonsinusoidal waveform is

$$I_{\text{rms}} = [(I_{\text{DC}})^2 + (I_{\text{max}}^{(1)})^2 + (I_{\text{max}}^{(2)})^2 + \dots + (I_{\text{max}}^{(n)})^2]^{1/2} \quad (1-13)$$

If the nonsinusoidal waveform contains DC values, then

$$I_{\text{rms}} = [I_{\text{DC}}^2 + (I_{\text{max}}^{(1)})^2 + (I_{\text{max}}^{(2)})^2 + \dots + (I_{\text{max}}^{(n)})^2]^{1/2} \quad (1-14)$$

1.4.4 Form Factor (FF)

The form factor (FF) is a measure of the shape of the waveform and is defined as

$$FF = \frac{I_{\text{rms}}}{I_{\text{ave}}} \quad (1-15)$$

Since the average value of a sinusoid is zero, its average over one half-cycle is used in the above equation. As the harmonic content of the waveform increases, its FF will also increase.

1.4.5 Ripple Factor (RF)

Ripple factor (RF) is a measure of the ripple content of the waveform and is defined as

$$RF = \frac{I_{\text{AC}}}{I_{\text{DC}}} \quad (1-16)$$

where $I_{\text{AC}} = \sqrt{(I_{\text{rms}})^2 - (I_{\text{DC}})^2}$. It is easy to show that

$$RF = \frac{\sqrt{(I_{\text{rms}})^2 - (I_{\text{DC}})^2}}{I_{\text{DC}}} = \frac{\sqrt{(I_{\text{rms}})^2 - (I_{\text{DC}})^2}}{I_{\text{DC}}} = \sqrt{FF^2 - 1} \quad (1-17)$$

1.4.6 Harmonic Factor (HF)

The harmonic factor (HF) of the h th harmonic, which is a measure of the individual harmonic contribution, is defined as

$$HF_h = \frac{I_{\text{max}}^{(h)}}{I_{\text{max}}^{(1)}} \quad (1-18)$$

Some references [8] call HF the individual harmonic distortion (IHD).

1.4.7 Lowest Order Harmonic (LOH)

The lowest order harmonic (LOH) is that harmonic component whose frequency is closest to that of the fundamental and its amplitude is greater than or equal to 3% of the fundamental component.

1.4.8 Total Harmonic Distortion (THD)

The most common harmonic index used to indicate the harmonic content of a distorted waveform with a single number is the total harmonic distortion (THD). It is a measure of the effective value of the harmonic components of a distorted waveform, which is defined as the rms of the harmonics expressed in percentage of the fundamental (e.g., current) component:

$$THD = \frac{\sqrt{\sum_{h=2}^n (I_{\text{max}}^{(h)})^2}}{I_{\text{max}}^{(1)}} \quad (1-19)$$

A commonly cited value of 5% is often used as a dividing line between a high and low distortion level. The ANSI standard recommends truncation of THD series at 5 kHz, but most practical commercially available instruments are limited to about 1.6 kHz (due to the limited bandwidth of potential and current transformers and the word length of the digital hardware [5]).

Main advantages of THD are

- It is commonly used for a quick measure of distortion; and
- It can be easily calculated.

Some disadvantages of THD are

- It does not provide amplitude information; and
- The detailed information of the spectrum is lost.

THD_i is related to the rms value of the current waveform as follows [6]:

$$I_{rms} = \sqrt{\sum_{h=2}^{\infty} (I^{(h)})^2} = I^{(1)} \sqrt{1 + THD_i^2} \quad (1-20)$$

THD can be weighted to indicate the amplitude stress on various system devices. The weighted distortion factor adapted to inductance is an approximate measure for the additional thermal stress of inductances of coils and induction motors [9, Table 2.4]:

THD adapted to inductance = THD_{ind}

$$= \frac{\sqrt{\sum_{h=2}^{50} \frac{(V^{(h)})^2}{h^{\alpha}}}}{V^{(1)}} \quad (1-21)$$

where $\alpha = 1 \dots 2$. On the other hand, the weighted THD adapted to capacitors is an approximate measure for the additional thermal stress of capacitors directly connected to the system without series inductance [9, Table 2.4]:

THD adapted to capacitor = THD_{cap}

$$= \frac{\sqrt{\sum_{h=2}^{50} (h \times (V^{(h)})^2)}}{V^{(1)}} \quad (1-22)$$

Because voltage distortions are maintained small, the voltage THD , nearly always assumes values which are not a threat to the power system. This is not the case for current; a small current may have a high THD , but may not be a significant threat to the system.

1.4.9 Total Interharmonic Distortion (TIHD)

This factor is equivalent to the (e.g., current) THD , but is defined for interharmonics as [9]

$$TIHD = \frac{\sqrt{\sum_{k=1}^n (I^{(k)})^2}}{I^{(1)}} \quad (1-23)$$

where k is the total number of interharmonics and n is the total number of frequency bins present including subharmonics (e.g., interharmonic frequencies that are less than the fundamental frequency).

1.4.10 Total Subharmonic Distortion (TSHD)

This factor is equivalent to the (e.g., current) THD , but defined for subharmonics [9]:

$$TSHD = \frac{\sqrt{\sum_{s=1}^s (I^{(s)})^2}}{I^{(1)}} \quad (1-24)$$

where s is the total number of frequency bins present below the fundamental frequency.

1.4.11 Total Demand Distortion (TDD)

Due to the mentioned disadvantages of THD , some standards (e.g., IEEE-519) have defined the total demand distortion factor. This term is similar to THD except that the distortion is expressed as a percentage of some rated or maximum value (e.g., load current magnitude), rather than as a percentage of the fundamental current:

$$TDD = \frac{\sqrt{\sum_{h=2}^{50} (I^{(h)})^2}}{I_{rated}} \quad (1-25)$$

1.4.12 Telephone Influence Factor (TIF)

The telephone influence factor (TIF), which was jointly proposed by Bell Telephone Systems (BTS) and the Edison Electric Institute (EEI) and is widely used in the United States and Canada, determines the influence of power systems harmonics on telecommunication systems. It is a variation of THD in which the root of the sum of the squares is weighted using factors (weights) that reflect the response of the human ear [5]:

$$TIF = \frac{\sqrt{\sum_{i=1}^{\infty} (w_i V^{(i)})^2}}{\sqrt{\sum_{i=1}^{\infty} (V^{(i)})^2}} \quad (1-26)$$

where w_i are the TIF weighting factors obtained by physiological and audio tests, as listed in Table 1.4. They also incorporate the way current in a power circuit induces voltage in an adjacent communication system.

1.4.13 C-Message Weights

The C-message weighted index is very similar to the TIF except that the weights c_i are used in place of w_i [5]:

TABLE 1.4 Telephone Interface (w_i) and C-Message (c_i) Weighting Factors [5]

Harmonic order ($h, f_1 = 60$ Hz)	TIF weights (w_i)	C weights (c_i)	Harmonic order ($h, f_1 = 60$ Hz)	TIF weights (w_i)	C weights (c_i)
1			29	7320	0.841
2	0.5	0.0017	30	7570	0.841
3	10.0	0.0167	31	7820	0.841
4	30.0	0.0333	32	8070	0.841
5	105	0.0875	33	8330	0.841
6	225	0.1500	34	8580	0.841
7	400	0.222	35	8830	0.841
8	650	0.310	36	9080	0.841
9	950	0.396	37	9330	0.841
10	1320	0.489	38	9590	0.841
11	1790	0.597	39	9840	0.841
12	2260	0.685	40	10090	0.841
13	2760	0.767	41	10340	0.841
14	3360	0.862	42	10480	0.841
15	3830	0.912	43	10600	0.832
16	4350	0.967	44	10610	0.822
17	4690	0.977	45	10480	0.804
18	5100	1.000	46	10350	0.776
19	5400	1.000	47	10210	0.750
20	5630	0.988	48	9960	0.692
21	5860	0.977	49	9820	0.668
22	6050	0.960	50	9670	0.645
23	6230	0.944	55	8090	0.490
24	6370	0.923	60	6460	0.359
25	6650	0.924	65	4400	0.226
26	6680	0.891	70	3000	0.143
27	6790	0.871	75	1830	0.0812
28	6970	0.860			
	7060	0.840			

$$C = \frac{\sqrt{\sum_{i=1}^{\infty} (c_i I^{(i)})^2}}{\sqrt{\sum_{i=1}^{\infty} (I^{(i)})^2}} = \frac{\sqrt{\sum_{i=1}^{\infty} (c_i I^{(i)})^2}}{I_{rms}} \quad (1-27)$$

where c_i are the C-message weighting factors (Table 1.4) that are related to the TIF weights by $w_i = 5(i)(f_0)c_i$. The C-message could also be applied to the bus voltage.

1.4.14 $V \cdot T$ and $I \cdot T$ Products

The THD index does not provide information about the amplitude of voltage (or current); therefore, BTS or the EEI use $I \cdot T$ and $V \cdot T$ products. The $I \cdot T$ and $V \cdot T$ products are alternative indices to the THD incorporating voltage or current amplitudes:

$$V \cdot T = \sqrt{\sum_{i=1}^{\infty} (w_i V^{(i)})^2} \quad (1-28)$$

$$I \cdot T = \sqrt{\sum_{i=1}^{\infty} (w_i I^{(i)})^2} \quad (1-29)$$

where the weights w_i are listed in Table 1.4.

1.4.15 Telephone Form Factor (TFF)

Two weighting systems widely used by industry for interference on telecommunication system are [9]

- the sophomoric weighting system proposed by the International Consultation Commission on Telephone and Telegraph System (CCITT) used in Europe, and
- the C-message weighting system proposed jointly by Bell Telephone Systems (BTS) and the Edison Electric Institute (EEI), used in the United States and Canada.

These concepts acknowledge that the harmonic effect is not uniform over the audio-frequency range and use measured weighting factors to account for

this nonuniformity. They take into account the type of telephone equipment and the sensitivity of the human ear to provide a reasonable indication of the interference from each harmonic.

The BTS and EEI systems describe the level of harmonic interference in terms of the telephone influence factor (Eq. 1-26) or the C-message (Eq. 1-27), whereas the CCITT system uses the telephone form factor (TFF):

$$TFF = \frac{1}{V^{(1)}} \sqrt{\sum_{h=1}^{\infty} K_h P_h (V^{(h)})^2}, \quad (1-30)$$

where $K_h = h/800$ is a coupling factor and P_h is the harmonic weight [9 (Fig. 2.5)] divided by 1000.

1.4.16 Distortion Index (DIN)

The distortion index (DIN) is commonly used in standards and specifications outside North America. It is also used in Canada and is defined as [5]

$$DIN = \frac{\sqrt{\sum_{h=2}^{\infty} (V^{(h)})^2}}{\sqrt{\sum_{h=1}^{\infty} (V^{(h)})^2}} = \frac{THD}{\sqrt{THD^2 + 1}} \quad (1-31)$$

For low levels of harmonics, a Taylor series expansion can be applied to show

$$DIN = THD(1 - \frac{1}{2}THD). \quad (1-32)$$

1.4.17 Distortion Power (D)

Harmonic distortion complicates the computation of power and power factors because voltage and current equations (and their products) contain harmonic components. Under sinusoidal conditions, there are four standard quantities associated with power:

- Fundamental apparent power (S_1) is the product of the rms fundamental voltage and current;
- Fundamental active power (P_1) is the average rate of delivery of energy;
- Fundamental reactive power (Q_1) is the portion of the apparent power that is oscillatory; and
- Power factor at fundamental frequency (or displacement factor) $\cos \theta_1 = P_1/S_1$.

The relationship between these quantities is defined by the power triangle:

$$(S_1)^2 = (P_1)^2 + (Q_1)^2. \quad (1-33)$$

If voltage and current waveforms are nonsinusoidal (Eq. 1-1), the above equation does not hold because S contains cross terms in the products of the Fourier series that correspond to voltages and currents of different frequencies, whereas P and Q correspond to voltages and currents of the same frequency. It has been suggested to account for these cross terms as follows [5, 50, 51]:

$$S^2 = P^2 + Q^2 + D^2, \quad (1-34)$$

where

$$\begin{aligned} \text{Apparent power} = S &= V_{rms} I_{rms} \\ &= \sqrt{\sum_{h=0,1,2,3,\dots}^H (V_{rms}^{(h)})^2} \sqrt{\sum_{h=0,1,2,3,\dots}^H (I_{rms}^{(h)})^2} \end{aligned} \quad (1-35)$$

$$\begin{aligned} \text{Total real power} = P &= \sum_{h=0,1,2,3,\dots}^H V_{rms}^{(h)} I_{rms}^{(h)} \cos(\theta_h), \\ \text{where } \theta_h &= \alpha_h - \beta_h, \end{aligned} \quad (1-36)$$

$$\text{Total reactive power} = Q = \sum_{h=0,1,2,3,\dots}^H V_{rms}^{(h)} I_{rms}^{(h)} \sin(\theta_h), \quad (1-37)$$

$$\begin{aligned} \text{Distortion power} = D &= \sum_{m=0}^{H-1} \sum_{n=m+1}^H [(V_{rms}^{(m)})^2 (I_{rms}^{(n)})^2 \\ &+ V_{rms}^{(n)2} (I_{rms}^{(m)})^2 - 2V_{rms}^{(m)} I_{rms}^{(n)} \cos(\theta_m - \theta_n)]. \end{aligned} \quad (1-38)$$

Also, the fundamental power factor (displacement factor) in the case of sinusoidal voltage and nonsinusoidal currents is defined as [8]

$$\cos \theta_1 = \frac{P_1}{\sqrt{(P_1)^2 + (Q_1)^2}}, \quad (1-39)$$

and the harmonic displacement factor is defined as [8]

$$\lambda = \frac{P_1}{\sqrt{(P_1)^2 + (Q_1)^2 + D^2}}. \quad (1-40)$$

The power and displacement factor quantities are shown in addition to the power quantities in Fig. 1.22. A detailed comparison of various definitions of the distortion power D is given in reference [51].

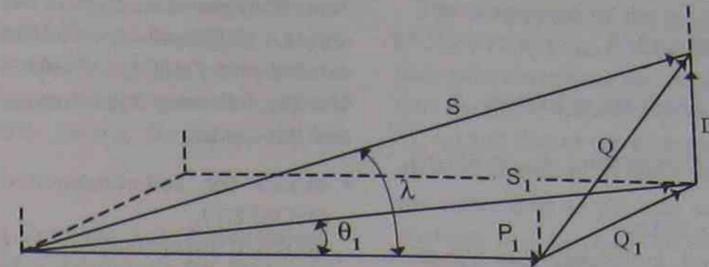


FIGURE 1.22 Phasor diagram of different parameters of electric power under nonsinusoidal conditions.

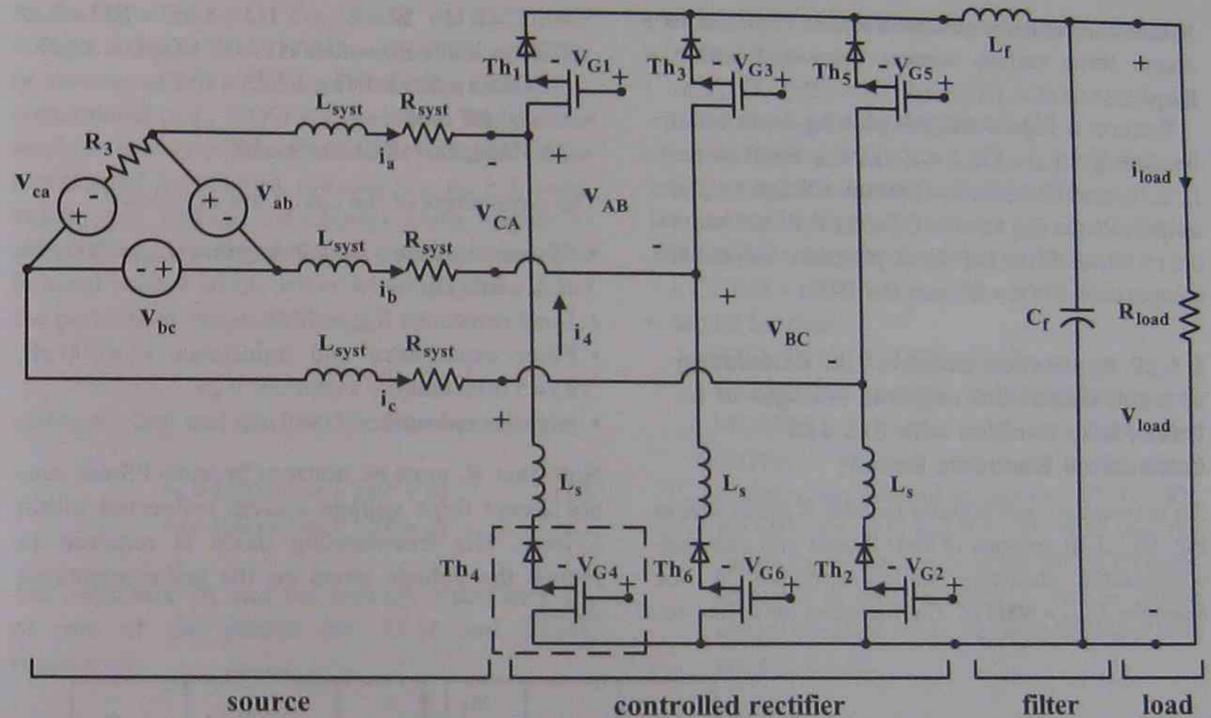


FIGURE E1.1.1 Controlled three-phase, full-wave thyristor rectifier.

1.4.18 Application Example 1.1: Calculation of Input/Output Currents and Voltages of a Three-Phase Thyristor Rectifier

The circuit of Fig. E1.1.1 represents a phase-controlled, three-phase thyristor rectifier. The balanced input line-to-line voltages are $v_{ab} = \sqrt{2} 240 \sin \omega t$, $v_{bc} = \sqrt{2} 240 \sin(\omega t - 120^\circ)$, and $v_{ca} = \sqrt{2} 240 \sin(\omega t - 240^\circ)$, where $\omega = 2\pi f$ and $f = 60$ Hz. Each of the six thyristors can be modeled by a self-commutated electronic switch and a diode in series, as is illustrated in Fig. E1.1.2. Use the following PSpice models for the MOSFET and the diode:

- Model for self-commutated electronic switch (MOSFET):
 .model SMM NMOS(Level=3 Gamma=0 Delta=0 Eta=0 Theta=0 Kappa=0 Vmax=0

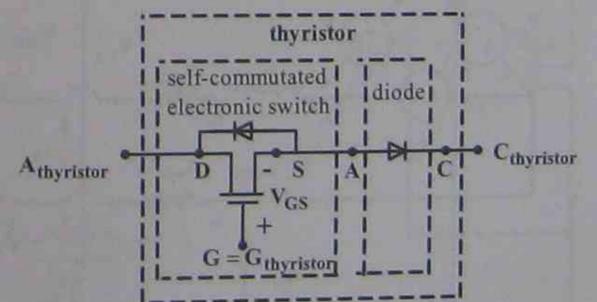


FIGURE E1.1.2 Model for thyristor consisting of self-commutated switch and diode.

- + XJ=0 TOX=100 N UO=600 PHI=0.6 RS=42.69 m KP=20.87 u L=2 u
- + W=2.9 VTO=3.487 RD=0.19 CBD=200 N PB=0.8 MJ=0.5 CGSO=3.5 n
- + CGDO=100 p RG=1.2 IS=10 F)

- Model for diode:
.model D1N4001 D(IS = 10⁻¹²)

The parameters of the circuit are as follows:

- System resistance and inductance $L_{\text{sys}} = 300 \mu\text{H}$, $R_{\text{sys}} = 0.05 \Omega$;
- Load resistance $R_{\text{load}} = 10 \Omega$;
- Filter capacitance and inductance $C_f = 500 \mu\text{F}$, $L_f = 1 \text{ mH}$;
- Snubber inductance $L_s = 5 \text{ nH}$;

Note that R_3 must be nonzero because PSpice cannot accept three voltage sources connected within a loop.

Perform a PSpice analysis plotting input line-to-line voltages $v_{ab}, v_{bc}, v_{ca}, v_{AB}, v_{BC}, v_{CA}$, input currents i_a, i_b, i_c , and the rectified output voltage v_{load} and output current i_{load} for $\alpha = 0^\circ$ during the time interval $0 \leq t \leq 60 \text{ ms}$. Print the input program. Repeat the computation for $\alpha = 50^\circ$ and $\alpha = 150^\circ$.

1.4.19 Application Example 1.2: Calculation of Input/Output Currents and Voltages of a Three-Phase Rectifier with One Self-Commutated Electronic Switch

An inexpensive and popular rectifier is illustrated in Fig. E1.2.1. It consists of four diodes and one self-commutated electronic switch operated at, for example, $f_{\text{switch}} = 600 \text{ Hz}$. The balanced input line-to-

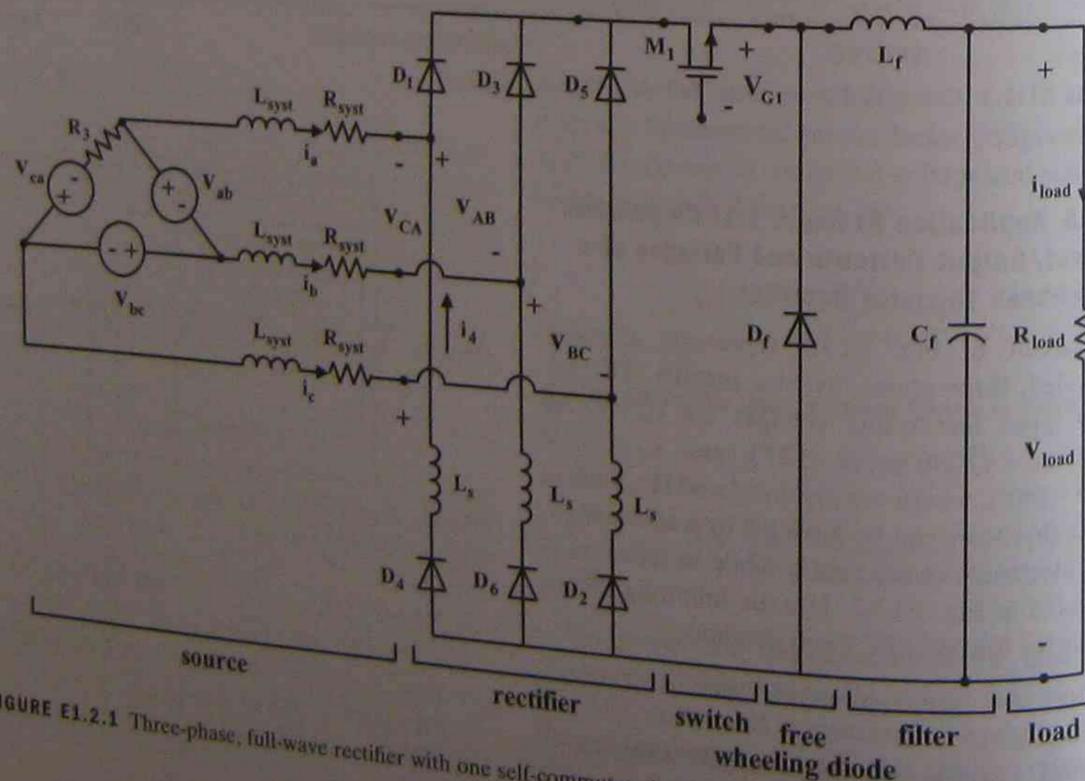


FIGURE E1.2.1 Three-phase, full-wave rectifier with one self-commutated switch.

line voltages are $v_{ab} = \sqrt{2} 240 \sin \omega t$, $v_{bc} = \sqrt{2} 240 \sin(\omega t - 120^\circ)$, and $v_{ca} = \sqrt{2} 240 \sin(\omega t - 240^\circ)$, where $\omega = 2\pi f$ and $f = 60 \text{ Hz}$. Perform a PSpice analysis. Use the following PSpice models for the MOSFET and the diodes:

- Model for self-commutated electronic switch (MOSFET):
MODEL SMM NMOS(LEVEL = 3 GAMMA = 0 DELTA = 0 ETA = 0 THETA = 0 + KAPPA = 0 VMAX = 0 XJ = 0 TOX = 100 N UO = 600 PHI = 0.6 RS = 42.69 M KP = 20.87 U + L = 2 U W = 2.9 VTO = 3.487 RD = 0.19 CBD = 200 N PB = 40.8 MJ = 0.5 CGSO = 3.5 N + CGDO = 100 P RG = 1.2 IS = 10 F)
- Model for diode:
.MODEL D1N4001 D(IS = 10⁻¹²)

The parameters of the circuit are as follows:

- System resistance and inductance $L_{\text{sys}} = 300 \mu\text{H}$, $R_{\text{sys}} = 0.05 \Omega$;
- Load resistance $R_{\text{load}} = 10 \Omega$;
- Filter capacitance and inductance $C_f = 500 \mu\text{F}$, $L_f = 5 \text{ mH}$; and
- Snubber inductance $L_s = 5 \text{ nH}$.

Note that R_3 must be nonzero because PSpice cannot accept three voltage sources connected within a loop. The freewheeling diode is required to reduce the voltage stress on the self-commutated switch.

Print the PSpice input program. Perform a PSpice analysis plotting input line-to-line voltages $v_{ab}, v_{bc}, v_{ca}, v_{AB}, v_{BC}, v_{CA}$, input currents i_a, i_b, i_c , and the rectified output voltage v_{load} and output current i_{load} for a duty ratio of $\delta = 50\%$ during the time interval $0 \leq t \leq 60 \text{ ms}$.

1.4.20 Application Example 1.3: Calculation of Input Currents of a Brushless DC Motor in Full-on Mode (Three-Phase Permanent-Magnet Motor Fed by a Six-Step Inverter)

In the drive circuit of Fig. E1.3.1 the DC input voltage is $V_{\text{DC}} = 300 \text{ V}$. The inverter is a six-pulse or six-step or full-on inverter consisting of six self-commutated (e.g., MOSFET) switches. The electric machine is a three-phase permanent-magnet motor represented by induced voltages (e_A, e_B, e_C), resistances, and leakage inductances (with respect to stator phase windings) for all three phases. The induced voltage of the stator winding (phase A) of the permanent-magnet motor is

$$e_A = 160 \sin(\omega t + \theta) \quad [\text{V}],$$

where $\omega = 2\pi f_1$ and $f_1 = 1500 \text{ Hz}$. Correspondingly,

$$e_B = 160 \sin(\omega t + 240^\circ + \theta) \quad [\text{V}],$$

$$e_C = 160 \sin(\omega t + 120^\circ + \theta) \quad [\text{V}].$$

The resistance R_1 and the leakage inductance $L_{1\ell}$ of one of the phases are 0.5Ω and $50 \mu\text{H}$, respectively.

The magnitude of the gating voltages of the six MOSFETs is $V_{\text{Gmax}} = 15 \text{ V}$. The gating signals with their phase sequence are shown in Fig. E1.3.2. Note that the phase sequence of the induced voltages (e_A, e_B, e_C) and that of the gating signals (see Fig. E1.3.2) must be the same. If these phase sequences are not the same, then no periodic solution for the machine currents (i_{MA}, i_{MB}, i_{MC}) can be obtained.

The models of the enhancement metal-oxide semiconductor field-effect transistors and those of the (external) freewheeling diodes are as follows:

- Model for self-commutated electronic switch (MOSFET):
.MODEL SMM NMOS(LEVEL = 3 GAMMA = 0 DELTA = 0 ETA = 0 THETA = 0 + KAPPA = 0 VMAX = 0 XJ = 0 TOX = 100 N UO = 600 PHI = 0.6 RS = 42.69 M KP = 20.87 U + L = 2 U W = 2.9 VTO = 3.487 RD = 0.19 CBD = 200 N PB = 0.8 MJ = 0.5 CGSO = 3.5 N + CGDO = 100 P RG = 1.2 IS = 10 F)
- Model for diode:
.MODEL D1N4001 D(IS = 10⁻¹²)

a) Using PSpice, compute and plot the current of MOSFET Q_{AU} (e.g., i_{QAU}) and the motor current of phase A (e.g., i_{MA}) for the phase angles of the induced voltages $\theta = 0^\circ, \theta = +30^\circ, \theta = +60^\circ, \theta = -30^\circ$, and $\theta = -60^\circ$. Note that the gating signal frequency of the MOSFETs corresponds to the frequency f_1 , that is, full-on mode operation exists. For switching sequence see Fig. E1.3.2.

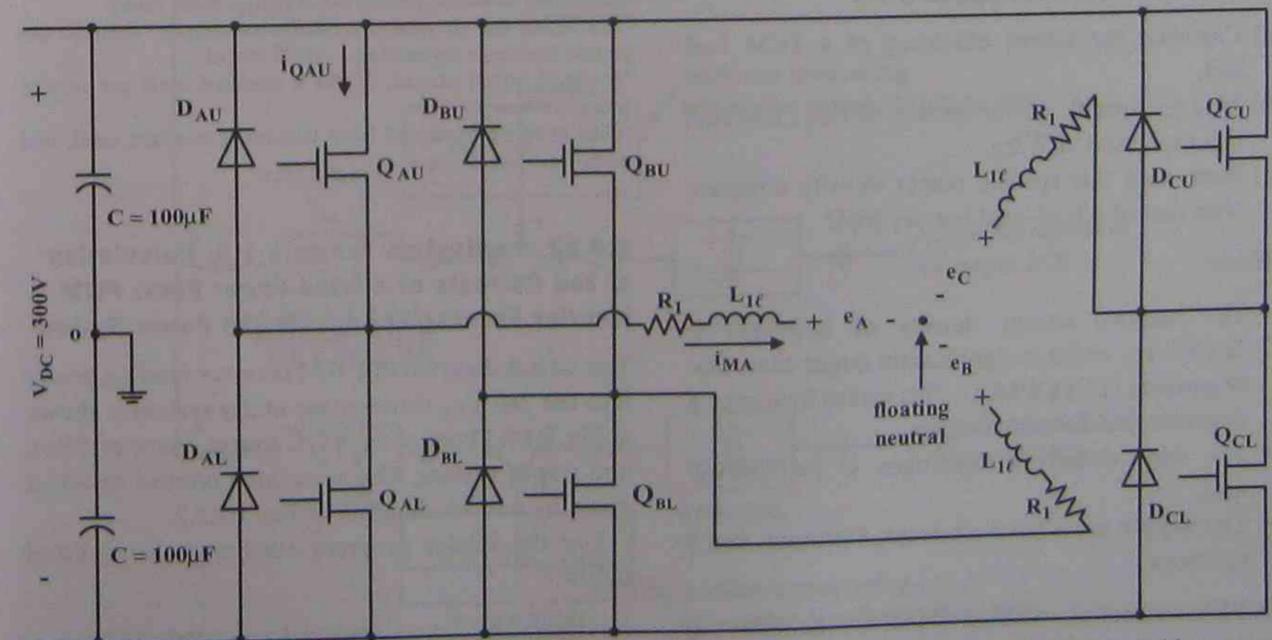


FIGURE E1.3.1 Circuit of brushless DC motor consisting of DC source, inverter, and permanent-magnet machine.

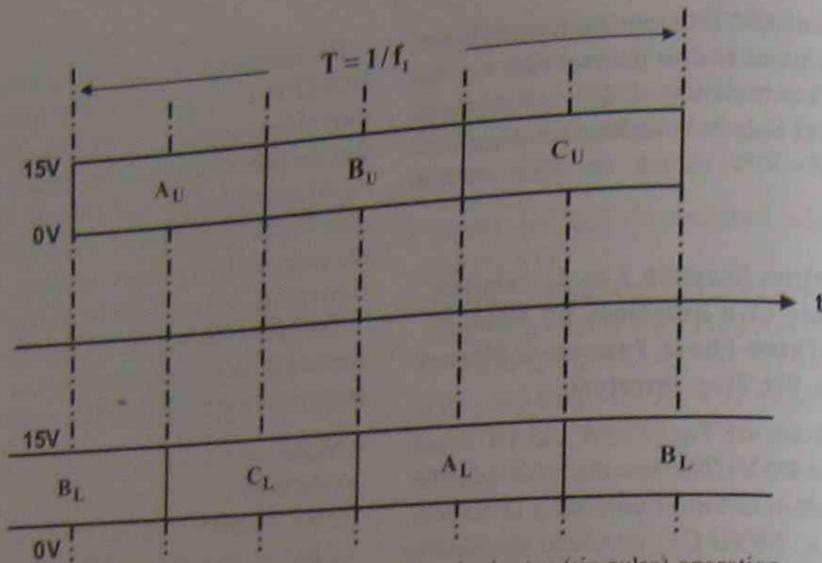


FIGURE E1.3.2 Sequence of gating signals for brushless DC motor in six-step (six-pulse) operation.

b) Repeat part a for $\theta = +30^\circ$ with reversed phase sequence.

Note the following:

- The step size for the numerical solution should be in the neighborhood of $\Delta t = 0.05 \mu s$; and
- To eliminate computational transients due to inconsistent initial conditions compute at least three periods of all quantities and plot the last (third) period of i_{QU} and i_{MA} for all five cases, where θ assumes the values given above.

1.4.21 Application Example 1.4: Calculation of the Efficiency of a Polymer Electrolyte Membrane (PEM) Fuel Cell Used as Energy Source for a Variable-Speed Drive

- Calculate the power efficiency of a PEM fuel cell.
- Find the specific power density of this PEM fuel cell expressed in W/kg.
- How does this specific power density compare with that of a lead-acid battery [66]?

Hints:

- The nominal energy density of hydrogen is 28 kWh/kg, which is significantly larger than that of gasoline (12.3 kWh/kg). This makes hydrogen a desirable fuel for automobiles.
- The mass density of hydrogen is $\gamma = 0.0899 \text{ g/liter}$.
- The oxygen atom has 8 electrons, 8 protons, and 8 neutrons.

A PEM fuel cell as specified by [65] has the following parameters:

Performance:	Output power: $P_{out} = 1200 \text{ W}^a$ Output current: $I_{out} = 46 \text{ A}^a$ DC voltage range: $V_{out} = 22 \text{ to } 50 \text{ V}$ Operating lifetime: $T_{life} = 1500 \text{ h}^b$
Fuel:	Composition: $C = 99.99\%$ dry gaseous hydrogen Supply pressure: $p = 10 \text{ to } 250 \text{ PSIG}$ Consumption: $V = 18.5 \text{ SLPM}^c$
Operating environment:	Ambient temperature: $t_{amb} = 3 \text{ to } 30^\circ\text{C}$ Relative humidity: $RH = 0 \text{ to } 95\%$ Location: Indoors and outdoors ^d
Physical:	Length \times width \times height: (56)(25)(33) cm Mass: $W = 13 \text{ kg}$
Emissions:	Liquid water: $\text{H}_2\text{O} = 0.87 \text{ liters maximum per hour}$.

^aBeginning of life, sea level, rated temperature range.
^bCO within the air (which provides the oxygen) destroys the proton exchange membrane.
^cAt rated power output, SLPM = standard liters per minute (standard flow).
^dUnit must be protected from inclement weather, sand, and dust.

1.4.22 Application Example 1.5: Calculation of the Currents of a Wind-Power Plant PWM Inverter Feeding Power into the Power System

The circuit diagram of PWM inverter feeding power into the 240 V_{LL} three-phase utility system is shown in Fig. E1.5.1 consisting of DC source, inverter, filter, and power system. The associated control circuit is given by a block diagram in Fig. E1.5.2.

Use the PSpice program *windpower.cir* as listed below:

*windpower.cir; Is=40Arms, VDC=450V, phi=30

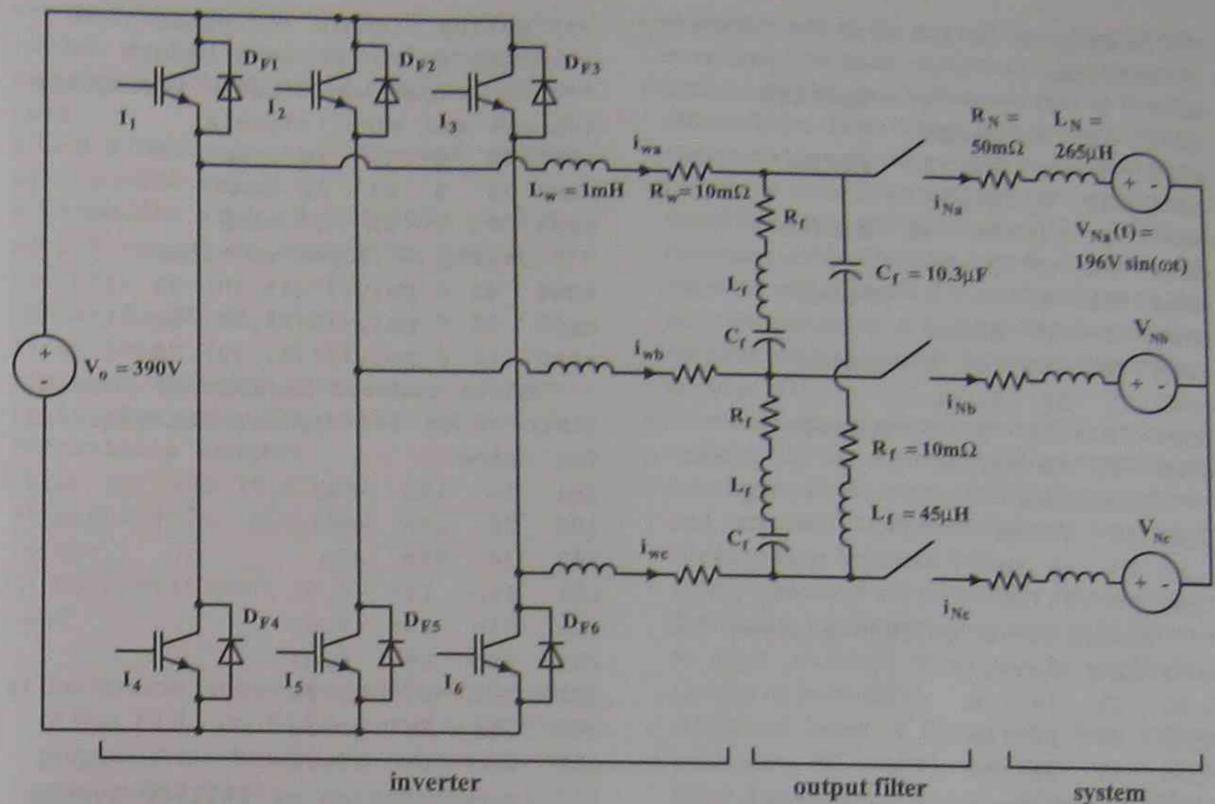


FIGURE E1.5.1 Current-controlled PWM inverter feeding into utility system.

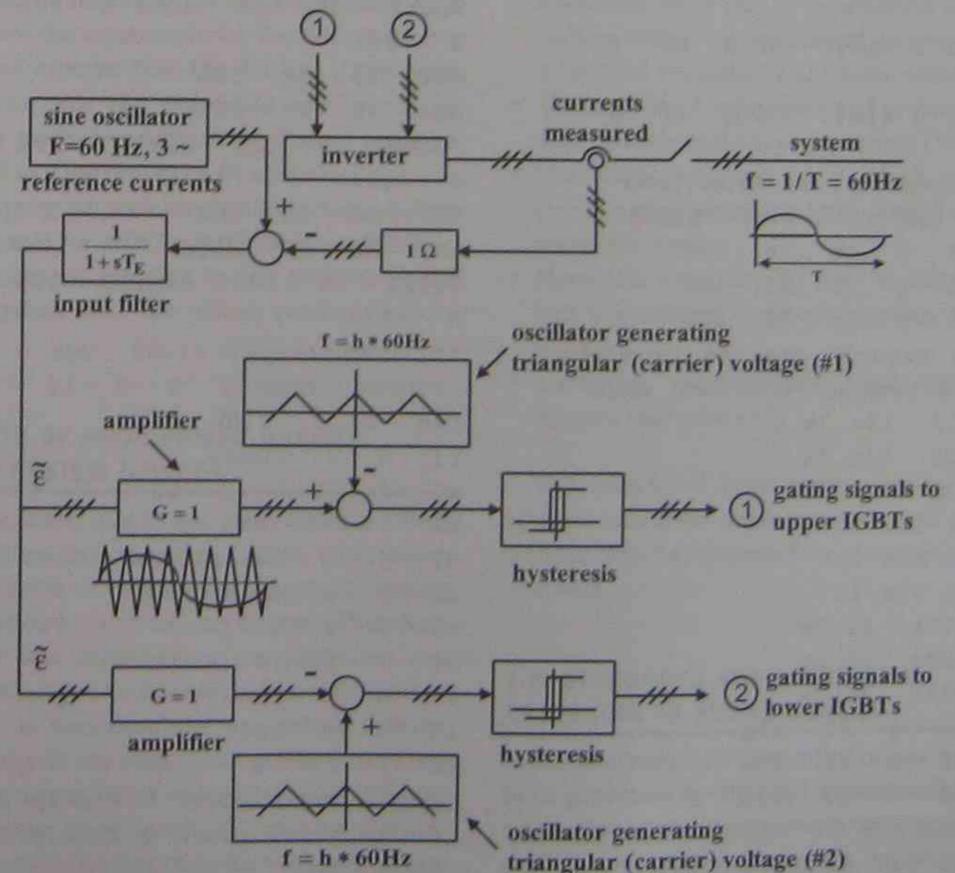


FIGURE E1.5.2 Block diagram of control circuit for current-controlled PWM inverter.

```

VDC supply 2 0 450
***switches
msw1 2 11 10 10 Mosfet
dsw1 10 2 diode
msw2 2 21 20 20 Mosfet
dsw2 20 2 diode
msw3 2 31 30 30 Mosfet
dsw3 30 2 diode
msw4 10 41 0 0 Mosfet
dsw4 0 10 diode
msw5 20 51 0 0 Mosfet
dsw5 0 20 diode
msw6 30 61 0 0 Mosfet
dsw6 0 30 diode
*** inductors
L_W1 10 15 1m
L_W2 20 25 1m
L_W3 30 35 1m
*** resistors or voltage sources for
measuring current
R_W1 15 16 10m
R_W2 25 26 10m
R_W3 35 36 10m
***voltages serve as reference
currents
vref1 12 0 sin(0 56.6 60 0 0
0)
vref2 22 0 sin(0 56.6 60 0 0
-120)
vref3 32 0 sin(0 56.6 60 0 0
-240)
*** voltages derived from load
currents (measured with shunts)
eout1 13 0 15 16 100
eout2 23 0 25 26 100
eout3 33 0 35 36 100
*** error signals derived from the
difference between vref and eout
rdiff1 12 13a 1k
rdiff2 22 23a 1k
rdiff3 32 33a 1k
cdiff1 12 13a 1u
cdiff2 22 23a 1u
cdiff3 32 33a 1u
rdiff4 13a 13 1k
rdiff5 23a 23 1k
rdiff6 33a 33 1k
ecin1 14 0 12 13a 2
ecin2 24 0 22 23a 2
ecin3 34 0 32 33a 2
vtriangular 5 0 pulse(-10 10 0
86.5u 86.5u 0.6u 173.6u)

```

```

*** gating signals for upper
switches (mosfets) as a result
*** of comparison between triangular
voltage and error signals
xgs1 14 5 11 10 comp
xgs2 24 5 21 20 comp
xgs3 34 5 31 30 comp
*** gating of lower switches
egs4 41 0 poly(1)(11,10) 50 -1
egs5 51 0 poly(1)(21,20) 50 -1
egs6 61 0 poly(1)(31,30) 50 -1
*** filter removed because of node
limit of 64 for PSpice, not required
for Spice
lf1 16 15b 45u
lf2 26 25b 45u
lf3 36 35b 45u
rf1 15b 15c 0.01
rf2 25b 25c 0.01
rf3 35b 35c 0.01
cf1 15c 26 10.3u
cf2 25c 36 10.3u
cf3 35c 16 10.3u
*** representation of utility system
RM1 16 18 50m
LM1 18 19 265u
Vout1 19 123 sin( 0 196 60 0
0 -30)
RM2 26 28 50m
LM2 28 29 265u
Vout2 29 123 sin( 0 196 60 0
0 -150)
RM3 36 38 50m
LM3 38 39 265u
Vout2 39 123 sin( 0 196 60 0
0 -270)
*** comparator: v1-v2, vgs
.subckt comp 1 2 9 10
rin 1 3 2.8k
r1 3 2 20meg
e2 4 2 3 2 50
r2 4 5 1k
d1 5 6 zenerdiode1
d2 2 6 zenerdiode2
e3 7 2 5 2 1
r3 7 8 10
c3 8 2 10n
r4 3 8 100k
e4 9 10 8 2 1
.model zenerdiode1 D (Is=1p BV=0.1)
.model zenerdiode2 D (Is=1p BV=50)
.ends comp

```

```

*** models
.model Mosfet nmos(level=3 gamma=0
kappa=0 tox=100n rs=42.69m kp=20.87u
l=2u
+ w=2.9 delta=0 eta=0 theta=0 vmax=0
xj=0 uo=600 phi=0.6
+ vto=3.487 rd=0.19 cbd=200n pb=0.8
mj=0.5 cgso=3.5n cgdo=100p rg=1.2
is=10f)
.model diode d(is=1p)
***options
.options abstol=0.01m chgtol= 0.01m
reltol=50m vntol=1m it15=0 it14=200
***analysis request
.tran 5u 350m 16.67m 5u
***prepare for plotting
.probe
***final statement
.end

```

- Use "reverse" engineering and identify the nodes of Figs. E1.5.1 and E1.5.2, as used in the PSpice program. It may be advisable that you draw your own detailed circuit.
- Study the PSpice program *wr.cir*. In particular it is important that you understand the *poly* statements and the *subcircuit* for the comparator. You may ignore the statements for the filter between switch and inverter (see Fig. E1.5.1) if the node number exceeds the maximum number of 64 (note the student version of the PSpice program is limited to a maximum of 64 nodes).
- Run this program with inverter inductance values of $L_w = 1$ mH for a DC voltage of $V_{DC} = 450$ V.
- Plot the current supplied by the inverter to the power system and the phase power system's voltage.

1.5 EFFECTS OF POOR POWER QUALITY ON POWER SYSTEM DEVICES

Poor electric power quality has many harmful effects on power system devices and end users. What makes this phenomenon so insidious is that its effects are often not known until failure occurs. Therefore, insight into how disturbances are generated and interact within a power system and how they affect components is important for preventing failures. Even if failures do not occur, poor power quality and harmonics increase losses and decrease the lifetime of power system components and end-use devices. Some of the main detrimental effects of poor power quality include the following:

- Harmonics add to the rms and peak value of the waveform. This means equipment could receive a damagingly high peak voltage and may be susceptible to failure. High voltage may also force power system components to operate in the saturation regions of their characteristics, producing additional harmonics and disturbances. The waveform distortion and its effects are very dependent on the harmonic-phase angles. The rms value can be the same but depending on the harmonic-phase angles, the peak value of a certain dependent quantity can be large [52].
- There are adverse effects from heating, noise, and reduced life on capacitors, surge suppressors, rotating machines, cables and transformers, fuses, and customers' equipment (ranging from small clocks to large industrial loads).
- Utility companies are particularly concerned that distribution transformers may need to be derated to avoid premature failure due to overheating (caused by harmonics).
- Additional losses of transmission lines, cables, generators, AC motors, and transformers may occur due to harmonics (e.g., inter- and subharmonics) [53].
- Failure of power system components and customer loads may occur due to unpredicted disturbances such as voltage and/or current magnifications due to parallel resonance and ferroresonance.
- Malfunction of controllers and protective devices such as fuses and relays is possible [35].
- Interharmonics may occur which can perturb ripple control signals and can cause flicker at subharmonic levels.
- Harmonic instability [9] may be caused by large and unpredicted harmonic sources such as arc furnaces.
- Harmonic, subharmonic, and interharmonic torques may arise [32].

The effects of poor power quality on power systems and their components as well on end-use devices will be discussed in detail in subsequent chapters.

1.6 STANDARDS AND GUIDELINES REFERRING TO POWER QUALITY

Many documents for control of power quality have been generated by different organizations and institutes. These documents come in three levels of applicability and validity: guidelines, recommendations, and standards [5]:

- **Power quality guidelines** are illustrations and exemplary procedures that contain typical parameters and representative solutions to commonly encountered power quality problems;
- **Power quality recommended practices** recognize that there are many solutions to power quality problems and recommend certain solutions over others. Any operating limits that are indicated by recommendations are not required but should be targets for designs; and
- **Power quality standards** are formal agreements between industry, users, and the government as to the proper procedure to generate, test, measure, manufacture, and consume electric power. In all jurisdictions, violation of standards could be used as evidence in courts of law for litigation purposes.

Usually the first passage of a power quality document is done in the form of the guidelines that are often based on an early document from an industry or government group. Guides are prepared and edited by different working groups. A recommended practice is usually an upgrade of guidelines, and a standard is usually an upgrade of a recommended practice.

The main reasons for setting guidelines, recommendations, and standards in power systems with nonsinusoidal voltages or currents are to keep disturbances to user equipment within permissible limits, to provide uniform terminology and test procedures for power quality problems, and to provide a common basis on which a wide range of engineering is referenced.

There are many standards and related documents that deal with power quality issues. A frequently updated list of available documents on power quality issues will simplify the search for appropriate information. Table 1.5 includes some of the commonly used guides, recommendations, and standards on electric power quality issues. The mostly adopted documents are these:

- The North American Standards adopted by many countries of North and South America:
 - a) Institute of Electrical and Electronic Engineering (IEEE).
 - b) American National Standards Institute (ANSI).
 - c) Military Specifications (MIL-Specs) published by the U.S. Department of Defense and Canadian Electric Association (CEA).
- British Standards (BS).
- European (Standards) Norms (EN).

- International Electrotechnical Commission (IEC).
- Computer Business Equipment Manufacturers Association (CBEMA) curves.
- Information Technology Industry Council (ITIC) curves [6 (Fig. 2.13), 9 (Fig. 5.9)].
- VDE (Verein Deutscher Elektrotechniker) [8, page 1] of the German association of individuals and groups concerned with electrotechnics.
- NEMA [9, page 20] of the U.S. National Electric Manufacturers Association.

1.6.1 IEC 61000 Series of Standards for Power Quality

The IEC 61000 (or EN 61000) series [54], one of the most commonly used references for power quality in Europe, contains six parts, each with standards and technical reports [9]:

- Part 1 (General). Two sections cover application and interpretation aspects of EMC (electromagnetic compatibility).
- Part 2 (Environment). Twelve sections give classification of the electromagnetic environment and compatibility levels for different environments. Some aspects of this document include harmonic compatibility levels of residential LV (low voltage) systems (IEC 61000-2-2), industrial plants (IEC 61000-2-4), and residential MV (medium voltage) systems (IEC 61000-2-12).
- Part 3 (Limits). Eleven sections cover emission limits for harmonics and other disturbances. Some aspects of this document include harmonic current emission limits for equipment connected at LV with low (less than 16 A per phase) current (IEC 61000-3-2), flicker (IEC 61000-3-3), harmonic current emission limits for equipment connected at LV with high (more than 16 A per phase) current (IEC 61000-3-4), and assessment of emission limits for distorting loads in MV and HV (high voltage) power systems (IEC 61000-3-6).
- Part 4 (Testing and Measurement Techniques). Thirty-one sections describe standard methods for testing equipment of emission and immunity to different disturbances. Some aspects of this document include harmonic and interharmonic measurements and instrumentation (IEC 61000-4-7), dips and interruptions (EN 61000-4-11), interharmonics (EN 61000-4-13), and power quality measurement methods (IEC 61000-4-30).
- Part 5 (Installation and Mitigation Guidelines). Seven sections cover earthing (grounding), cabling,

TABLE 1.5 Some Guides, Recommendations, and Standards on Electric Power Quality

Source	Coverage
IEEE and ANSI Documents	
IEEE 4: 1995	Standard techniques for high-voltage testing.
IEEE 100: 1992	Standard dictionary of electrical and electronic terms.
IEEE 120: 1989	Master test guide for electrical measurements in power circuits.
IEEE 141: 1993	Recommended practice for electric power distribution for industrial plants. Effect of voltage disturbances on equipment within an industrial area.
IEEE 142: 1993 (The Green Book)	Recommended practice for grounding of industrial and commercial power systems.
IEEE 213: 1993	Standard procedure for measuring conducted emissions in the range of 300 kHz to 25 MHz from television and FM broadcast receivers to power lines.
IEEE 241: 1990 (The Gray Book)	Recommended practice for electric power systems in commercial buildings.
IEEE 281: 1994	Standard service conditions for power system communication equipment.
IEEE 299: 1991	Standard methods of measuring the effectiveness of electromagnetic shielding enclosures.
IEEE 367: 1996	Recommended practice for determining the electric power station ground potential rise and induced voltage from a power fault.
IEEE 376: 1993	Standard for the measurement of impulse strength and impulse bandwidth.
IEEE 430: 1991	Standard procedures for the measurement of radio noise from overhead power lines and substations.
IEEE 446: 1987 (The Orange Book)	Recommended practice for emergency and standby systems for industrial and commercial applications (e.g., power acceptability curve [5, Fig. 2-26], CBEMA curve).
IEEE 449: 1990	Standard for ferroresonance voltage regulators.
IEEE 465	Test specifications for surge protective devices.
IEEE 472	Event recorders.
IEEE 473: 1991	Recommended practice for an electromagnetic site survey (10 kHz to 10 GHz).
IEEE 493: 1997 (The Gold Book)	Recommended practice for the design of reliable industrial and commercial power systems.
IEEE 519: 1993	Recommended practice for harmonic control and reactive compensation of static power converters.
IEEE 539: 1990	Standard definitions of terms relating to corona and field effects of overhead power lines.
IEEE 859: 1987	Standard terms for reporting and analyzing outage occurrences and outage states of electrical transmission facilities.
IEEE 944: 1986	Application and testing of uninterruptible power supplies for power generating stations.
IEEE 998: 1996	Guides for direct lightning strike shielding of substations.
IEEE 1048: 1990	Guides for protective grounding of power lines.
IEEE 1057: 1994	Standards for digitizing waveform recorders.
IEEE P1100: 1992 (The Emerald Book)	Recommended practice for powering and grounding sensitive electronic equipment in commercial and industrial power systems.
IEEE 1159: 1995	Recommended practice on monitoring electric power quality. Categories of power system electromagnetic phenomena.
IEEE 1250: 1995	Guides for service to equipment sensitive to momentary voltage disturbances.
IEEE 1346: 1998	Recommended practice for evaluating electric power system compatibility with electronics process equipment.
IEEE P-1453	Flicker.
IEEE/ANSI 18: 1980	Standards for shunt power capacitors.
IEEE/ANSI C37	Guides for surge withstand capability (SWC) tests.
IEEE/ANSI C50: 1982	Harmonics and noise from synchronous machines.
IEEE/ANSI C57.110: 1986	Recommended practice for establishing transformer capability when supplying nonsinusoidal load currents.
IEEE/ANSI C57.117: 1986	Guides for reporting failure data for power transformers and shunt reactors on electric utility power systems.
IEEE/ANSI C62.45: 1992 (IEEE 587)	Recommended practice on surge voltage in low-voltage AC power circuits, including guides for lightning arresters applications.
IEEE/ANSI C62.48: 1995	Guides on interactions between power system disturbances and surge protective devices.
ANSI C84.1: 1982	American national standard for electric power systems and equipment voltage ratings (60 Hz).

TABLE 1.5 Some Guides, Recommendations, and Standards on Electric Power Quality (continued)

Source	Coverage
ANSI 70	National electric code.
ANSI 368	Telephone influence factor.
ANSI 377	Spurious radio frequency emission from mobile communication equipment.
International Electrotechnical Commission (IEC) Documents	
IEC 38: 1983	Standard voltages.
IEC 816: 1984	Guides on methods of measurement of short-duration transients on low-voltage power and signal lines. Equipment susceptible to transients.
IEC 868: 1986	Flicker meter. Functional and design specifications.
IEC 868-0: 1991	Flicker meter. Evaluation of flicker severity. Evaluates the severity of voltage fluctuation on the light flicker.
IEC 1000-3-2:1994	Electromagnetic compatibility Part 3: Limits Section 2: Limits for harmonic current emissions (equipment absorbed current ≤ 16 A per phase).
IEC 1000-3-6: 1996	Electromagnetic compatibility Part 3: Limits Section 6: Emission limits evaluation for perturbing loads connected to MV and HV networks.
IEC 1000-4: 1991	Electromagnetic compatibility Part 4: Sampling and metering techniques.
EN 50160: 1994	Voltage characteristics of electricity supplied by public distribution systems.
IEC/EN 60868-0	Flicker meter implementation.
IEC 61000 standards on EMC	Electromagnetic compatibility (EMC).
British Standards (BS) and European Norm Documents	
BSS406 (based on IEC 555 part 2)	Control harmonic emissions from small domestic equipment.
Other Documents	
ER G5/3	Basis of standards in some other (mostly commonwealth) countries, but it does not include notching and burst harmonics.
G5/4: 2001	Limiting harmonic voltage distortion levels on public networks at the time of connection of new nonlinear loads to ensure compatibility of all connected equipment.
UIE-DWG-2-92-D	Produced by the Distribution Working Group (DWG) of Union Internationale Electrotechnique (UIE). Includes guides for measurements of voltage dips and short-circuit interruptions occurring in industrial installations.
UIE-DWG-3-92-G	UIE guides for quality of electrical supply for industrial installations, including types of disturbances and relevant standards.
CBEMA Curves: 1983	Produced by the Computer Business Equipment Manufacturers Association for the design of the power supply for computers and electronic equipment.
ITI Curves (new CBEMA curves)	Information Technology Industry Council (the new name for CBEMA) application.

mitigation, and degrees of protection against EM (electromagnetic) disturbances.

- Part 6 (Generic Standards). Five sections cover immunity and emission standards for residential, commercial, industrial, and power station environments.

EN 61000-3-2 [2] introduces power quality limits (Table 1.6) for four classes of equipment:

- Class A: Balanced three-phase equipment and all other equipment, except those listed in other classes.
- Class B: Portable tools,

- Class C: Lighting equipment, including dimming devices.
- Class D: Equipment with a "special wave shape" and an input power of 75 to 600 W.

1.6.2 IEEE-519 Standard

The United States (ANSI and IEEE) do not have such a comprehensive and complete set of power quality standards as the IEC. However, their standards are more practical and provide theoretical background on the phenomena. This has made them very useful reference documents, even outside of the United States. IEEE-Std 519 [1] is the IEEE recommended practices and requirements for harmonic

TABLE 1.6 Harmonic Limits Defined by the EN 61000 Standards for Different Classes of Equipment

Harmonic order (h)	Class A (A)	Class B (A)	Class C (% of fundamental)	Class D (% of fundamental)
2	1.08	1.62	2	
3	2.30	3.45	$30 \times \lambda^*$	3.4
4	0.43	0.65		
5	1.44	2.16	10	1.9
6	0.30	0.45		
7	0.77	1.12	7	1
8	0.23	0.35		
9	0.40	0.60	5	0.5
10	0.18	0.28		
11	0.33	0.50	3	0.35
12	0.15	0.23		
13	0.21	0.32		
14-40 (even)	1.84/h	2.76/h	3	0.296
15-39 (odd)	2.25/h	3.338/h	3	3.85/h

* λ is the circuit power factor.

control in electric power systems. It is one of the well-known documents for power quality limits. IEEE-519 is more comprehensive than IEC 61000-3-2 [2], but it is not a product standard. The first official version of this document was published in 1981. Product testing standards for the United States are now considered within TC77A/WG1 (TF5b) but are also discussed in IEEE. The current direction of the TC-77 working group is toward a global IEC standard for both 50/60 Hz and 115/230 V.

IEEE-519 contains thirteen sections, each with standards and technical reports [11]:

- Section 1 (Introduction and Scope). Includes application of the standards.
- Section 2 (Definition and Letter Symbols).
- Section 3 (References). Includes standard references.
- Section 4 (Converter Theory and Harmonic Generation). Includes documents for converters, arc furnaces, static VAR compensators, inverters for dispersed generation, electronic control, transformers, and generators.
- Section 5 (System Response Characteristics). Includes resonance conditions, effect of system loading, and typical characteristics of industrial, distribution, and transmission systems.
- Section 6 (Effect of Harmonics). Detrimental effects of harmonics on motors, generators, transformers, capacitors, electronic equipments, meters, relaying, communication systems, and converters.
- Section 7 (Reactive Power Compensation and Harmonic Control). Discusses converter power factor, reactive power compensation, and control of harmonics.

- Section 8 (Calculation Methods). Includes calculations of harmonic currents, telephone interference, line notching, distortion factor, and power factor.
- Section 9 (Measurements). For line notching, harmonic voltage and current, telephone interface, flicker, power factor improvement, instrumentation, and statistical characteristics of harmonics.
- Section 10 (Recommended Practices for Individual Consumers). Addresses standard impedance, customer voltage distortion limits, customer application of capacitors and filters, effect of multiple sources at a single customer, and line notching calculations.
- Section 11 (Recommended Harmonic Limits on the System). Recommends voltage distortion limits on various voltage levels, TIF limits versus voltage level, and IT products.
- Section 12 (Recommended Methodology for Evaluation of New Harmonic Sources).
- Section 13 (Bibliography). Includes books and general discussions.

IEEE-519 sets limits on the voltage and current harmonics distortion at the point of common coupling (PCC, usually the secondary of the supply transformer). The total harmonic distortion at the PCC is dependent on the percentage of harmonic distortion from each nonlinear device with respect to the total capacity of the transformer and the relative load of the system. There are two criteria that are used in IEEE-519 to evaluate harmonics distortion:

- limitation of the harmonic current that a user can transmit/inject into utility system (THD_i), and

TABLE 1.7 IEEE-519 Harmonic Current Limits [1, 64] for Nonlinear Loads at the Point of Common Coupling (PCC) with Other Loads at Voltages of 2.4 to 69 kV

I_h/I_L	Maximum harmonic current distortion at PCC (% of fundamental)					THD _v
	Harmonic order (odd harmonics) ^a					
	$h < 11$	$11 \leq h < 17$	$17 \leq h < 23$	$23 \leq h < 35$	$h \geq 35$	
<20 ^b	4.0	2.0	1.5	0.6	0.3	5.0
20–50	7.0	3.5	2.5	1.0	0.5	8.0
50–100	10.0	4.5	4.0	1.5	0.7	12.0
100–1000	12.0	5.5	5.0	2.0	1.0	15.0
>1000	15.0	7.0	6.0	2.5	1.4	20.0

^aEven harmonics are limited to 25% of the odd harmonic limits above.

^bAll power generation equipment is limited to these values of current distortion, regardless of the actual I_{sc}/I_L .

Here I_{sc} = maximum short circuit current at PCC.

I_L = maximum load current (fundamental frequency) at PCC.

For PCCs from 69 to 138 kV, the limits are 50% of the limits above. A case-by-case evaluation is required for PCCs of 138 kV and above.

TABLE 1.8 IEEE-519 Harmonic Voltage Limits [1, 64] for Power Producers (Public Utilities or Cogenerators)

	Harmonic voltage distortion (% at PCC)		
	2.3 to 69 kV	69 to 138 kV	>138 kV
Maximum for individual harmonics	3.0	1.5	1.0
Total harmonic distortion (THD _v)	5.0	2.5	1.5

- limitation of the voltage distortion that the utility must furnish the user (THD_v).

The interrelationship of these two criteria shows that the harmonic problem is a system problem and not tied just to the individual load that generates the harmonic current.

Tables 1.7 and 1.8 list the harmonic current and voltage limits based on the size of the user with respect to the size of the power system to which the user is connected [1, 64].

The short-circuit current ratio (R_{sc}) is defined as the ratio of the short-circuit current (available at the point of common coupling) to the nominal fundamental load current (Fig. 1.23):

$$R_{sc} = \frac{|\tilde{I}_{sc}|}{I_L} \quad (1-41)$$

Thus the size of the permissible nonlinear user load increases with the size of the system; that is, the stronger the system, the larger the percentage of harmonic current the user is allowed to inject into the utility system.

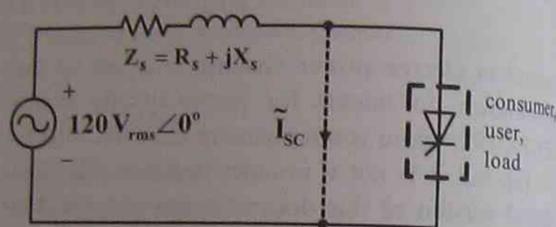


FIGURE 1.23 Equivalent circuit of power system and nonlinear load. Z_s is small (or \tilde{I}_{sc} is large) for strong systems, and Z_s is large (or \tilde{I}_{sc} is small) for weak systems.

Table 1.8 lists the amount of voltage distortion [1, 64] specified by IEEE-519 that is acceptable for a user as provided by a utility. To meet the power quality values of Tables 1.7 and 1.8, cooperation among all users and the utility is needed to ensure that no one user deteriorates the power quality beyond these limits. The values in Table 1.8 are low enough to ensure that equipment will operate correctly.

1.7 HARMONIC MODELING PHILOSOPHIES

For the simulation and modeling of power systems, the dynamic operation is normally subdivided into well-defined quasi steady-state regions [5]. Differential equations representing system dynamics in each region are transformed into algebraic relations, and the circuit is solved at the fundamental frequency (50 or 60 Hz) in terms of voltage and current phasors.

Modern power systems have many nonlinear components and loads that produce voltage and current harmonics. By definition, harmonics result from periodic steady-state conditions, and therefore their simulation should also be formulated in terms of harmonic phasors. Considering the complicated

nature of many nonlinear loads (sources) and their couplings with the harmonic power flow, sophisticated modeling techniques are required for accurate simulation. Three techniques are usually used for harmonic analysis of power systems in the presence of nonlinear loads and/or components: time-domain simulation, frequency (harmonic)-domain modeling, and iterative procedures. The more recent approaches may use time-domain, frequency-domain or some combination of time- and frequency-domain techniques to achieve a more accurate solution (e.g., the main structure of many harmonic power flow algorithms are based on a frequency-domain technique, while nonlinear loads are modeled in a time-domain simulation).

1.7.1 Time-Domain Simulation

Dynamic characteristics of power systems are represented in terms of nonlinear sets of differential equations that are normally solved by numerical integration [5]. There are two commonly used time-domain techniques:

- state-variable approach, which is extensively used for the simulation of electronic circuits (SPICE [55]), and
- nodal analysis, which is commonly used for electromagnetic transient simulation of power system (EMTP [56]).

Two main limitations attached to the time-domain methods for harmonic studies are

- They usually require considerable computing time (even for small systems) for the calculation of harmonic information. This involves solving for the steady-state condition and then applying a fast Fourier transform (FFT); and
- There are some difficulties in time-domain modeling of power system components with distributed or frequency-dependent parameters.

The Electromagnetic Transient Program (EMTP) and PSpice are two of the well-known time-domain programs that are widely used for transient and harmonic analyses. Most examples of this book are solved using the PSpice software package.

1.7.2 Harmonic-Domain Simulation

The most commonly used model in the frequency domain assumes a balanced three-phase system (at fundamental and harmonic frequencies) and uses single-phase analysis, a single harmonic source, and a direct solution [5]. The injected harmonic currents

by nonlinear power sources are modeled as constant-current sources to make a direct solution possible. In the absence of any other nonlinear loads, the effect of a given harmonic source is often assessed with the help of equivalent harmonic impedances. The single-source concept is still used for harmonic filter design. Power systems are usually asymmetric. This justifies the need for multiphase harmonic models and power flow that considerably complicates the simulation procedures.

For more realistic cases, if more than one harmonic source is present in the power system, the single-source concept can still be used, provided that the interaction between them can be ignored. In these cases, the principle of superposition is relied on to compute the total harmonic distortion throughout the network.

1.7.3 Iterative Simulation Techniques

In many modern networks, due to the increased power ratings of nonlinear elements (e.g., HVDC systems, FACTS devices, renewable energy sources, and industrial and residential nonlinear loads) as compared to the system short-circuit power, application of superposition (as applied by harmonic-domain techniques) is not justified and will provide inaccurate results. In addition, due to the propagation of harmonic voltages and currents, the injected harmonics of each nonlinear load is a function of those of other sources. For such systems, accurate results can be obtained by iteratively solving nonlinear equations describing system steady-state conditions. At each iteration, the harmonic-domain simulation techniques can be applied, with all nonlinear interactions included. Two important aspects of the iterative harmonic-domain simulation techniques are:

- Derivation of system nonlinear equations [5]. The system is partitioned into linear regions and nonlinear devices (described by isolated equations). The system solution then consists predominantly of the solution for given boundary conditions as applied to each nonlinear device. Many techniques have been proposed for device modeling including time-domain simulation, steady-state analysis, analytical time-domain expressions [references 11, 13 of [5]], waveform sampling and FFT [reference 14 of [5]], and harmonic phasor analytical expressions [reference 15 of [5]].
- Solution of nonlinear equations [5]. Early methods used the fixed point iteration procedure of Gauss-Seidel that frequently diverges. Some techniques

replace the nonlinear devices at each iteration by a linear Norton equivalent (which might be updated at the next iteration). More recent methods make use of Newton-type solutions and completely decouple device modeling and system solution. They use a variety of numerical analysis improvement techniques to accelerate the solution procedure.

Detailed analyses of iterative simulation techniques for harmonic power (load) flow are presented in Chapter 7.

1.7.4 Modeling Harmonic Sources

As mentioned above, an iterative harmonic power flow algorithm is used for the simulation of the power system with nonlinear elements. At each iteration, harmonic sources need to be accurately included and their model must be updated at the next iteration.

For most harmonic power flow studies it is suitable to treat harmonic sources as (variable) harmonic currents. At each iteration of the power flow algorithm, the magnitudes and phase angles of these harmonic currents need to be updated. This is performed based on the harmonic couplings of the nonlinear load. Different techniques have been proposed to compute and update the values of injected harmonic currents, including:

- Thevenin or Norton harmonic equivalent circuits,
- simple decoupled harmonic models for the estimation of nonlinear loads (e.g., $I_h = 1/h$, where h is the harmonic order),
- approximate modeling of nonlinear loads (e.g., using decoupled constant voltage or current harmonic sources) based on measured voltage and current characteristics or published data, and
- iterative nonlinear (time- and/or frequency-based) models for detailed and accurate simulation of harmonic-producing loads.

1.8 POWER QUALITY IMPROVEMENT TECHNIQUES

Nonlinear loads produce harmonic currents that can propagate to other locations in the power system and eventually return back to the source. Therefore, harmonic current propagation produces harmonic voltages throughout the power systems. Many mitigation techniques have been proposed and implemented to maintain the harmonic voltages and currents within recommended levels:

- high power quality equipment design,
- harmonic cancellation,
- dedicated line or transformer,
- optimal placement and sizing of capacitor banks,
- derating of power system devices, and
- harmonic filters (passive, active, hybrid) and custom power devices such as active power line conditioners (APLCs) and unified or universal power quality conditioners (UPQCs).

The practice is that if at PCC harmonic currents are not within the permissible limits, the consumer with the nonlinear load must take some measures to comply with standards. However, if harmonic voltages are above recommended levels – and the harmonic currents injected comply with standards – the utility will have to take appropriate actions to improve the power quality.

Detailed analyses of improvement techniques for power quality are presented in Chapters 8 to 10.

1.8.1 High Power Quality Equipment Design

The use of nonlinear and electronic-based devices is steadily increasing and it is estimated that they will constitute more than 70% of power system loading by year 2010 [10]. Therefore, demand is increasing for the designers and product manufacturers to produce devices that generate lower current distortion, and for end users to select and purchase high power quality devices. These actions have already been started in many countries, as reflected by improvements in fluorescent lamp ballasts, inclusion of filters with energy saving lamps, improved PWM adjustable-speed drive controls, high power quality battery chargers, switch-mode power supplies, and uninterruptible power sources.

1.8.2 Harmonic Cancellation

There are some relatively simple techniques that use transformer connections to employ phase-shifting for the purpose of harmonic cancellation, including [10]:

- delta-delta and delta-wye transformers (or multiple phase-shifting transformers) for supplying harmonic producing loads in parallel (resulting in twelve-pulse rectifiers) to eliminate the 5th and 7th harmonic components,
- transformers with delta connections to trap and prevent triplen (zero-sequence) harmonics from entering power systems,
- transformers with zigzag connections for cancellation of certain harmonics and to compensate load imbalances,

- other phase-shifting techniques to cancel higher harmonic orders, if required, and
- canceling effects due to diversity [57–59] have been discovered.

1.8.3 Dedicated Line or Transformer

Dedicated (isolated) lines or transformers are used to attenuate both low- and high-frequency electrical noise and transients as they attempt to pass from one bus to another. Therefore, disturbances are prevented from reaching sensitive loads and any load-generated noise and transients are kept from reaching the remainder of the power system. However, some common-mode and differential noise can still reach the load. Dedicated transformers with (single or multiple) electrostatic shields are effective in eliminating common-mode noise.

Interharmonics (e.g., caused by induction motor drives) and voltage notching (e.g., due to power electronic switching) are two examples of problems that can be reduced at the terminals of a sensitive load by a dedicated transformer. They can also attenuate capacitor switching and lightning transients coming from the utility system and prevent nuisance tripping of adjustable-speed drives and other equipment. Isolated transformers do not totally eliminate voltage sags or swells. However, due to the inherent large impedance, their presence between PCC and the

source of disturbance (e.g., system fault) will lead to relatively shallow sags.

An additional advantage of dedicated transformers is that they allow the user to define a new ground reference that will limit neutral-to-ground voltages at sensitive equipment.

1.8.3.1 Application Example 1.6: Interharmonic Reduction by Dedicated Transformer

Figure E1.6.1 shows a typical distribution system with linear and nonlinear loads. The nonlinear load (labeled as “distorting nonlinear load”) consists of two squirrel-cage induction motors used as prime movers for chiller-compressors for a building’s air-conditioning system. This load produces interharmonic currents that generate interharmonic voltage drops across the system’s impedances resulting in the interharmonic content of the line-to-line voltage of the induction motors as given by Table E1.6.1. Some of the loads are very sensitive to interharmonics and these must be reduced at the terminals of sensitive loads. These loads are labeled as “sensitive loads.”

Three case studies are considered:

- Case #1: Distorting nonlinear load and sensitive loads are fed from the same pole transformer (Fig. E1.6.2).

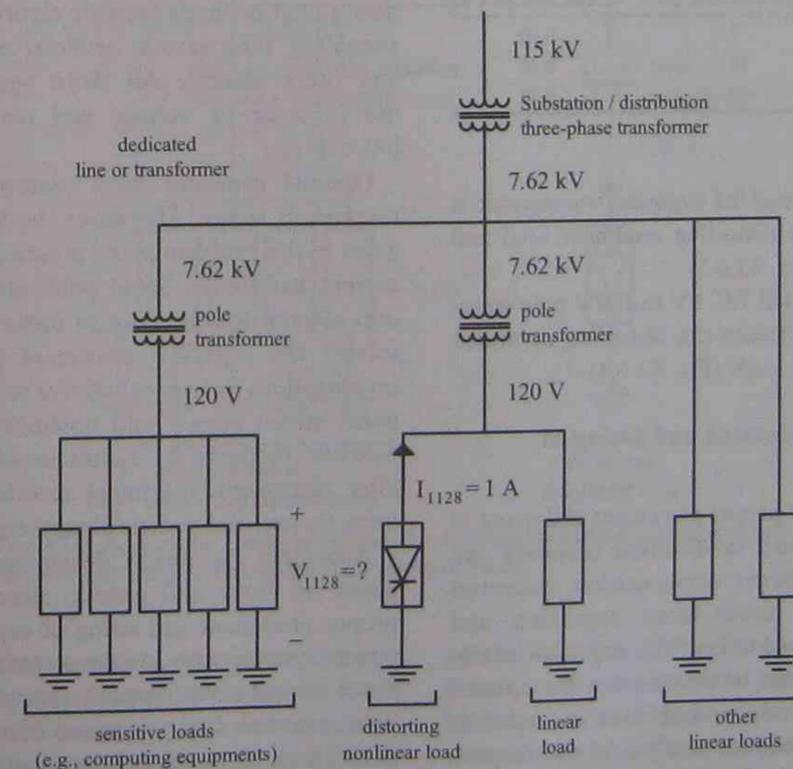


FIGURE E1.6.1 Overall (per phase) one-line diagram of the distribution system used in Application Example 1.6.

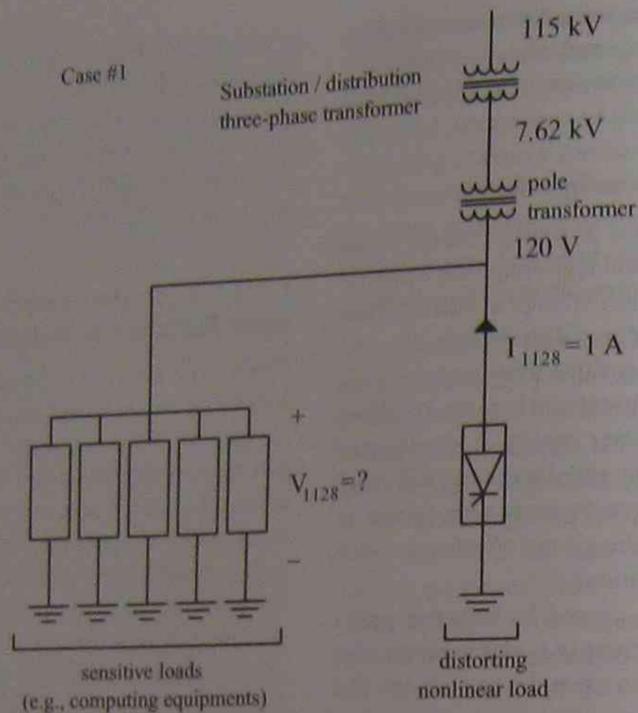


FIGURE E1.6.2 Case #1 of Application Example 1.6: distorting nonlinear load and sensitive loads are fed from same pole transformer.

TABLE E1.6.1 Interharmonics of Phase Current and Line-to-Line Voltage Generated by a Three-Phase Induction Motor [34]

Interharmonic f_h (Hz)	Interharmonic amplitude of phase current (%)	Interharmonic amplitude of line-to-line voltage (%)
1128	7	0.40
1607	10	0.40
1730	10	0.55

- Case #2: A dedicated 1:1 isolation transformer is used between the distorting nonlinear load and sensitive loads (Fig. E1.6.3).
- Case #3: A dedicated 7.62 kV to 120 V pole transformer is used between the distorting nonlinear load and sensitive loads (Fig. E1.6.4).

1.8.4 Optimal Placement and Sizing of Capacitor Banks

It is well known that proper placement and sizing of shunt capacitor banks in distorted networks can result in reactive power compensation, improved voltage regulation, power factor correction, and power/energy loss reduction. The capacitor placement problem consists of determining the optimal numbers, types, locations, and sizes of capacitor banks such that minimum yearly cost due to peak power/energy losses and cost of capacitors is

achieved, while the operational constraints are maintained within required limits.

Most of the reported techniques for capacitor placement assume sinusoidal operating conditions. These methods include nonlinear programming, and near global methods (genetic algorithms, simulated annealing, tabu search, artificial neural networks, and fuzzy theory). All these approaches ignore the presence of voltage and current harmonics [60, 61].

Optimal capacitor bank placement is a well-researched subject. However, limited attention is given to this problem in the presence of voltage and current harmonics. Some publications have taken into account the presence of distorted voltages for solving the capacitor placement problem. These investigations include exhaustive search, local variations, mixed integer and nonlinear programming, heuristic methods for simultaneous capacitor and filter placement, maximum sensitivities selection, fuzzy theory, and genetic algorithms.

According to newly developed investigations based on fuzzy and genetic algorithms [60, 61], proper placement and sizing of capacitor banks in power systems with nonlinear loads can result in lower system losses, greater yearly benefits, better voltage profiles, and prevention of harmonic parallel resonances, as well as improved power quality. Simulation results for the standard 18-bus IEEE

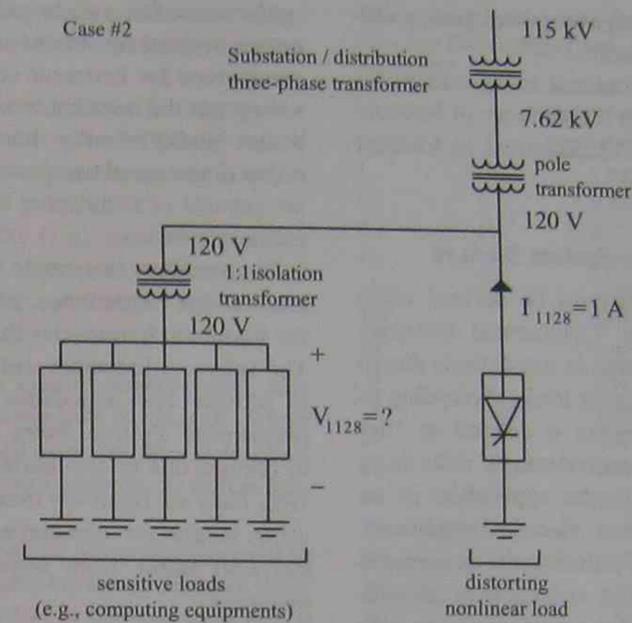


FIGURE E1.6.3 Case #2 of Application Example 1.6: use of an isolation transformer with a turns ratio 1 : 1 between distorting (nonlinear) load and sensitive loads.

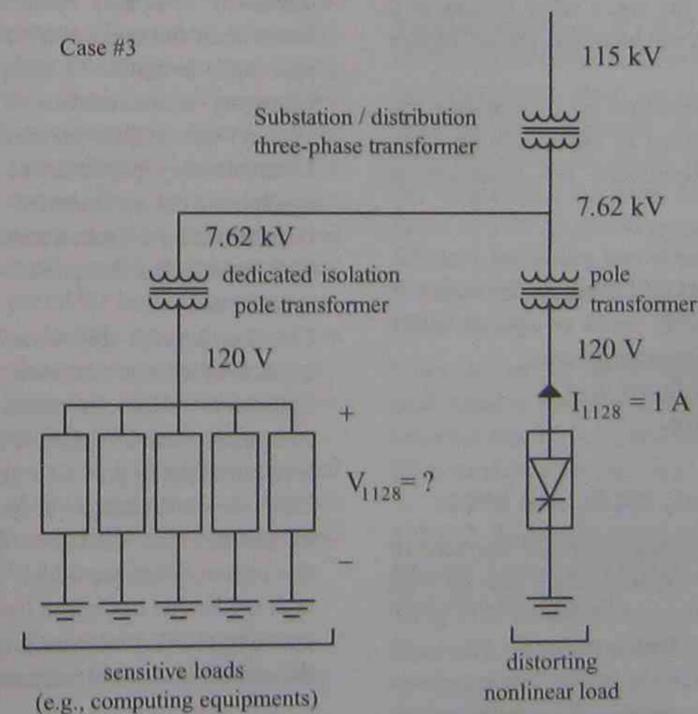


FIGURE E1.6.4 Case #3 of Application Example 1.6: use of a dedicated (isolation transformer with turns ratio 7620 : 120) pole transformer between distorting nonlinear load and sensitive loads.

distorted distribution system show that proper placement and sizing of capacitor banks can limit voltage and current harmonics and decrease their THDs to the recommended levels of IEEE-519, without application of any dedicated high-order

passive or active filters. For cases where the construction of new capacitor bank locations is not feasible, it is possible to perform the optimization process without defining any new locations. Therefore, reexamining capacitor bank sizes and locations

before taking any major steps for power quality mitigation is highly recommended.

Detailed analyses for optimal sizing and placement of capacitor banks in the presence of harmonics and nonlinear loads are presented in Chapter 10.

1.8.5 Derating of Power System Devices

Power system components must be derated when supplying harmonic loads. Commercial buildings have drawn the most attention in recent years due to the increasing use of nonlinear loads. According to the IEEE dictionary, derating is defined as "the intentional reduction of stress/strength ratio (e.g., real or apparent power) in the application of an item (e.g., cables, transformer, electrical machines), usually for the purpose of reducing the occurrence of stress-related failure (e.g., reduction of lifetime due to increased temperature beyond rated temperature)." As discussed in Section 1.5, harmonic currents and voltages result in harmonic losses of magnetic devices, increasing their temperature rise [62]. This rise beyond the rated value results in a reduction of lifetime, as will be discussed in Chapter 6.

There are several techniques for determining the derating factors (functions) of appliances for non-sinusoidal operating conditions (as discussed in Chapter 2), including:

- from tables in standards and published research (e.g., ANSI/IEEE Std C57.110 [63] for transformer derating),
- from measured (or computed) losses,
- by determining the K-factor, and
- based on the F_{HL} -factor.

1.8.6 Harmonic Filters, APLCs, and UPQCs

One means of ensuring that harmonic currents of nonlinear components will not unduly interact with the remaining part of the power system is to place filters near or close to nonlinear loads. The main function of a filter is either to bypass harmonic currents, block them from entering the power system, or compensate them by locally supplying harmonic currents. Due to the lower impedance of the filter in comparison to the impedance of the system, harmonic currents will circulate between the load and the filter and do not affect the entire system; this is called series resonance. If other frequencies are to be controlled (e.g., that of arc furnaces), additional tuned filters are required.

Harmonic filters are broadly classified into passive, active, and hybrid structures. These filters can only compensate for harmonic currents and/or harmonic voltages at the installed bus and do not consider the power quality of other buses. New generations of active filters are active-power line conditioners that are capable of minimizing the power quality of the entire system.

Passive filters are made of passive components (inductance, capacitance, and resistance) tuned to the harmonic frequencies that are to be attenuated. The values of inductors and capacitors are selected to provide low impedance paths at the selected frequencies. Passive filters are generally designed to remove one or two harmonics (e.g., the 5th and 7th). They are relatively inexpensive compared with other means for eliminating harmonic distortion, but also suffer from some inherent limitations, including:

- Interactions with the power system;
- Forming parallel resonance circuits with system impedance (at fundamental and/or harmonic frequencies). This may result in a situation that is worse than the condition being corrected. It may also result in system or equipment failure;
- Changing characteristics (e.g., their notch frequency) due to filter parameter variations;
- Unsatisfactory performance under variations of nonlinear load parameters;
- Compensating a limited number of harmonics;
- Not considering the power quality of the entire system; and
- Creating parallel resonance. This resonance frequency must not necessarily coincide with any significant system harmonic. Passive filters are commonly tuned slightly lower than the attenuated harmonic to provide a margin of safety in case there are some changes in system parameters (due to temperature variations and/or failures). For this reason filters are added to the system starting with the lowest undesired harmonic. For example, installing a seventh-harmonic filter usually requires that a fifth-harmonic filter also be installed.

Designing passive filters is a relatively simple but tedious matter. For the proper tuning of passive filters, the following steps should be followed:

- Model the power system (including nonlinear loads) to indicate the location of harmonic sources and the orders of the injected harmonics. A harmonic power (load) flow algorithm (Chapter 7) should be used; however, for most applications

with a single dominating harmonic source, a simplified equivalent model and hand calculations are adequate;

- Place the hypothetical harmonic filter(s) in the model and reexamine the system. Filter(s) should be properly tuned to dominant harmonic frequencies; and
- If unacceptable results (e.g., parallel resonance within system) are obtained, change filter location(s) and modify parameter values until results are satisfactory.

In addition to power quality improvement, harmonic filters can be configured to provide power factor correction. For such cases, the filter is designed to carry resonance harmonic currents, as well as fundamental current.

Active filters rely on active power conditioning to compensate for undesirable harmonic currents. They actually replace the portion of the sine wave that is missing in the nonlinear load current by detecting the distorted current and using power electronic switching devices to inject harmonic currents with complimentary magnitudes, frequencies, and phase shifts into the power system. Their main advantage over passive filters is their fine response to changing loads and harmonic variations. Active filters can be used in very difficult circumstances where passive filters cannot operate successfully because of parallel resonance within the system. They can also take care of more than one harmonic at a time and improve or mitigate other power quality problems such as flicker. They are particularly useful for large, distorting nonlinear loads fed from relatively weak points of the power system where the system impedance is relatively large. Active filters are relatively expensive and not feasible for small facilities.

Power quality improvement using filters, optimal placement and sizing of shunt capacitors, and unified power quality conditioners (UPQCs) are discussed in Chapters 9, 10, and 11, respectively.

1.8.6.1 Application Example 1.7: Hand Calculation of Harmonics Produced by Twelve-Pulse Converters

Figure E1.7.1 shows a large industrial plant such as an oil refinery or chemical plant [64] being serviced from a utility with transmission line-to-line voltage of 115 kV. The demand on the utility system is 50 MVA and 50% of its load is a twelve-pulse static power converter load.

Table E1.7.1 lists the harmonic currents (I_h) given in pu of the fundamental current based on the com-

TABLE E1.7.1 Harmonic Current (I_h) Generated by Six-Pulse and Twelve-Pulse Converters [64] Based on $X_c^h = 0.12$ pu and $\alpha = 30^\circ$

Harmonic order (h)	I_h for 6-pulse converter (pu)	I_h for 12-pulse converter (pu)
1	1.000	1.000
5	0.192	0.0192
7	0.132	0.0132
11	0.073	0.073
13	0.057	0.057
17	0.035	0.0035
19	0.027	0.0027
23	0.020	0.020
25	0.016	0.016
29	0.014	0.0014
31	0.012	0.0012
35	0.011	0.011
37	0.010	0.010
41	0.009	0.0009
43	0.008	0.0008
47	0.008	0.008
49	0.007	0.007

mutating reactance $X_c^h = 0.12$ pu and the firing angle $\alpha = 30^\circ$ of six-pulse and twelve-pulse converters. In an ideal twelve-pulse converter, the magnitude of some current harmonics (bold in Table E1.7.1) is zero. However, for actual twelve-pulse converters, the magnitudes of these harmonics are normally taken as 10% of the six-pulse values [64].

1.8.6.2 Application Example 1.8: Filter Design to Meet IEEE-519 Requirements

Filter design for Application Example 1.7 will be performed to meet the IEEE-519 requirements. The circuit of Fig. E1.7.1 is now augmented with a passive filter, as shown by Fig. E1.8.1.

1.8.6.3 Application Example 1.9: Several Users on a Single Distribution Feeder

Figure E1.9.1 shows a utility distribution feeder that has four users along a radial feeder [64]. Each user sees a different value of short-circuit impedance or system size. Note that

$$S_{SC} = MVA_{SC} = \frac{10MVA}{Z_{sys} [\text{pu at } 10MVA \text{ base}]}$$

There is one type of transformer ($\Delta - Y$); therefore, only six-pulse static power converters are used.

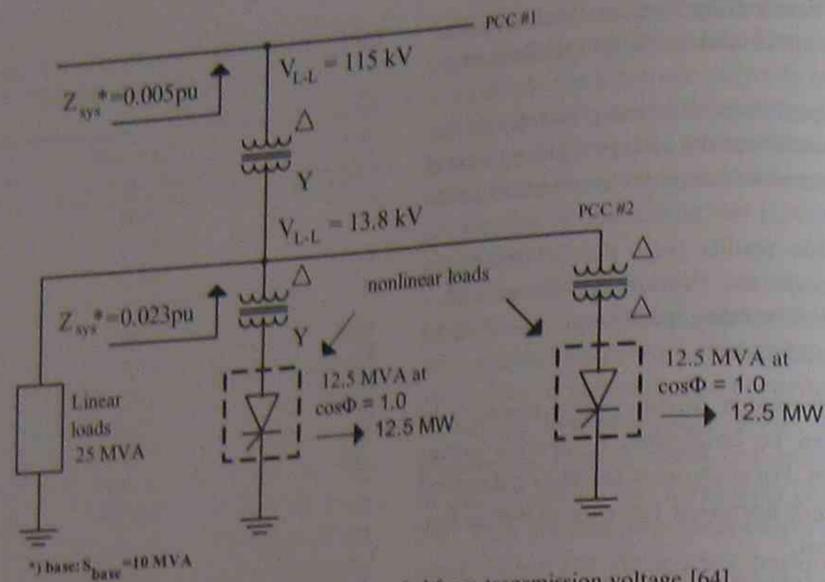


FIGURE E1.7.1 One-line diagram of a large industrial plant fed from transmission voltage [64].

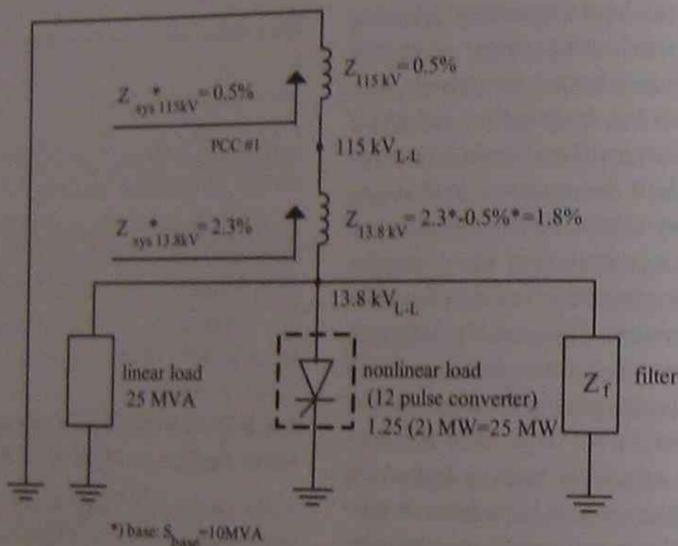


FIGURE E1.8.1 One-line diagram of a large industrial plant fed from transmission voltage (Fig. E1.7.1) with a passive filter placed at PCC #2.

1.9 SUMMARY

The focus of this chapter has been on definition, measures, and classification of electric power quality as well as related issues that will be covered in the following chapters. Power quality can be defined as "the measure, analysis, and improvement of the bus voltage to maintain a sinusoidal waveform at rated voltage and frequency." Main causes of disturbances and power quality problems are unpredictable events, the electric utility, the customer, and the manufacturer.

The magnitude-duration plot can be used to classify power quality events, where the voltage magni-

tude is split into three regions (e.g., interruption, undervoltage, and overvoltage) and the duration of these events is split into four regions (e.g., very short, short, long, and very long). However, IEEE standards use several additional terms to classify power quality events into seven categories including: transient, short-duration voltage variation, long-duration voltage variation, voltage imbalance, waveform distortion, voltage fluctuation (and flicker), and power-frequency variation. Main sources for the formulations and measures of power quality are IEEE Std 100, IEC Std 61000-1-1, and CENELEC Std EN 50160. Some of the main detrimental effects of poor power

$$S_{sc\ PCC\ #1} = \frac{10\text{MVA}}{(0.02857)} = 350\text{ MVA}$$

$$S_{sc\ PCC\ #2} = \frac{10\text{MVA}}{(0.02857+0.00476)} = 300\text{ MVA}$$

$$S_{sc\ PCC\ #3} = \frac{10\text{MVA}}{(0.02857+0.00476+0.0238)} = 175\text{ MVA}$$

$$S_{sc\ PCC\ #4} = \frac{10\text{MVA}}{(0.02857+0.00476+0.0238+0.02286)} = 125\text{ MVA}$$

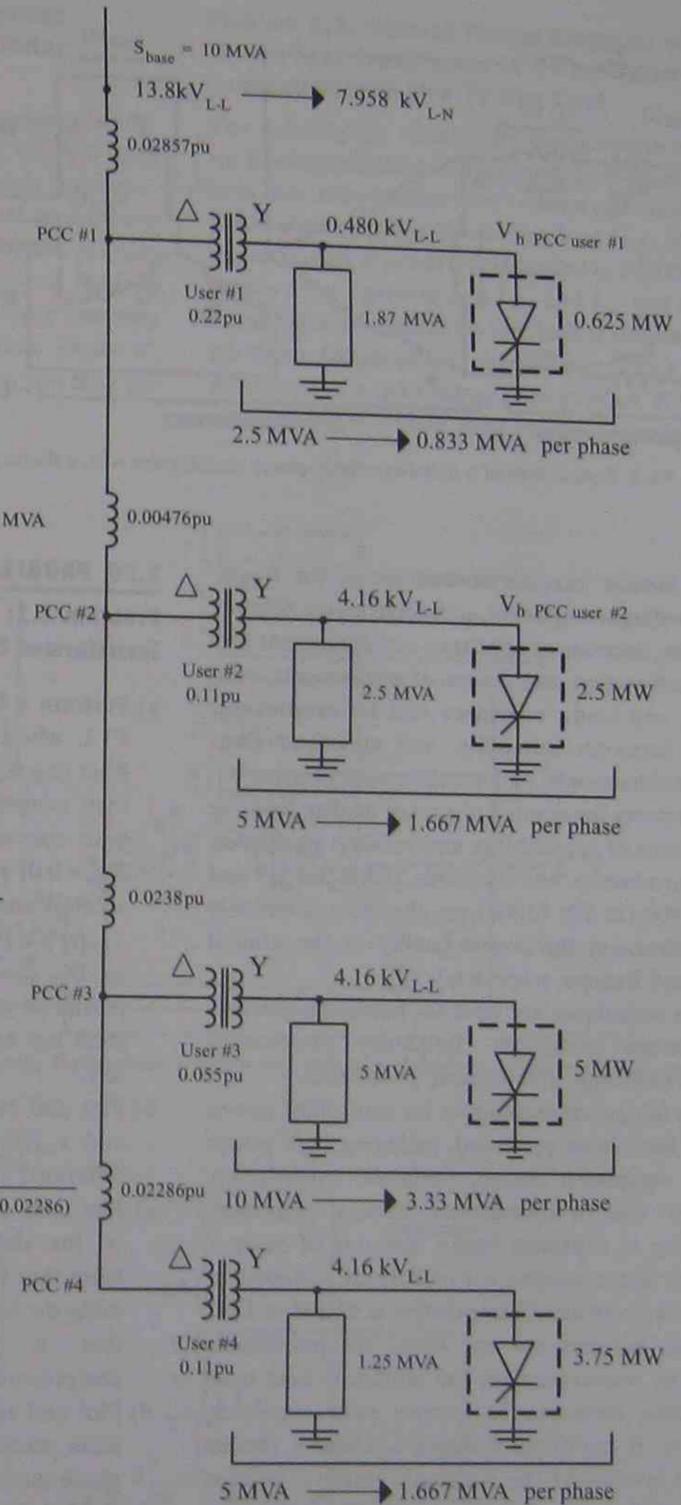


FIGURE E1.9.1 Overall one-line diagram of the distribution system feeder containing four users with six-pulse converters [64].

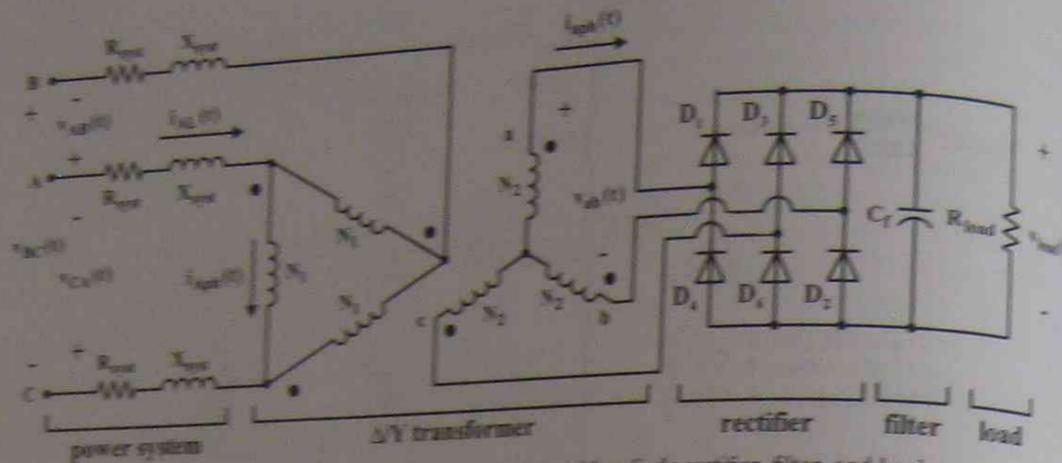


FIGURE P1.1 Connection of a delta/ye three-phase transformer with a diode rectifier, filter, and load.

quality include increase or decrease of the fundamental voltage component, additional losses, heating, and noise, decrease of appliance and equipment lifetime, malfunction and failure of components, controllers, and loads, resonance and ferroresonance, flicker, harmonic instability, and undesired (harmonic, subharmonic, and interharmonic) torques.

Documents for control of power quality come in three levels of applicability and validity: guidelines, recommendations, and standards. IEEE-Std 519 and IEC 61000 (or EN 61000) are the most commonly used references for power quality in the United States and Europe, respectively.

Three techniques are used for harmonic analysis: time-domain simulation, frequency (harmonic)-domain modeling, and iterative procedures.

Many mitigation techniques for controlling power quality have been proposed, including high power quality equipment design, harmonic cancellation, dedicated line or transformer, optimal placement and sizing of capacitor banks, derating of devices, harmonic filters (passive, active, hybrid), and custom-build power devices. The practice is that if at PCC harmonic currents are not within the permissible limits, the consumer with the nonlinear load must take some measures to comply with standards. However, if harmonic voltages are above recommended levels and the harmonic currents injected comply with standards - the utility will have to take appropriate actions to improve the power quality.

Nine application examples with solutions are provided for further clarifications of the presented materials. The reader is encouraged to read the overview of the text given in the preface before delving further into the book.

1.10 PROBLEMS

Problem 1.1: Delta/ye Three-Phase Transformer Configuration

- Perform a PSpice analysis for the circuit of Fig. P1.1, where a three-phase diode rectifier with filter (e.g., capacitor C_1) serves a load (R_{load}). You may assume ideal transformer conditions. For your convenience you may assume $(N_1/N_2)=1$, $R_{wye}=0.01 \Omega$, $X_{wye}=0.05 \Omega$ @ $f=60$ Hz, $v_{ab}(t)=\sqrt{2}600V \cos \omega t$, $v_{bc}(t)=\sqrt{2}600V \cos(\omega t - 120^\circ)$, $v_{ca}(t)=\sqrt{2}600V \cos(\omega t - 240^\circ)$, ideal diodes D_1 to D_6 , $C_1=500 \mu\text{F}$, and $R_{load}=10 \Omega$. Plot one period of either voltage or current after steady state has been reached as requested in parts b to e.
- Plot and subject the line-to-line voltages $v_{ab}(t)$ and $v_{bc}(t)$ to Fourier analysis. Why are they different?
- Plot and subject the input line current $i_{in}(t)$ of the delta primary to a Fourier analysis. Note that the input line currents of the primary delta do not contain the 3rd, 6th, 9th, 12th, ... that is, harmonic zero-sequence current components.
- Plot and subject the phase current $i_{\Delta}(t)$ of the delta primary to a Fourier analysis. Why do the phase currents of the primary delta not contain the 3rd, 6th, 9th, 12th, ... that is, harmonic zero-sequence current components?
- Plot and subject the output current $i_{out}(t)$ of the wye secondary to a Fourier analysis. Why do the output currents of the secondary wye not contain the 3rd, 6th, 9th, 12th, ... that is, zero-sequence current components?

Problem 1.2: Voltage Phasor Diagrams of a Three-Phase Transformer in Delta/Zigzag Connection

Figure P1.2 depicts the so-called delta/zigzag configuration of a three-phase transformer, which is used for supplying power to unbalanced loads and three-phase rectifiers. You may assume ideal transformer conditions. Draw a phasor diagram of the primary and secondary voltages when there is no load on the secondary side. For your convenience you may assume $(N_1/N_2)=1$. For balanced phase angles 0° , 120° , and 240° of voltages and currents you may use hexagonal paper.

Problem 1.3: Current Phasor Diagrams of a Three-Phase Transformer in Delta/Zigzag Connection With Line-To-Line Load

The delta/zigzag, three-phase configuration is used for feeding unbalanced loads and three-phase rectifiers. You may assume ideal transformer conditions. Even when only one line-to-line load (e.g., R_{load}) of the secondary is present as indicated in Fig. P1.3, the primary line currents \tilde{I}_{LA} , \tilde{I}_{LB} , and \tilde{I}_{LC} will be balanced because the line-to-line load is distributed to all three (single-phase) transformers. This is the advantage of a delta/zigzag configuration. If there is a resistive line-to-line load on the secondary side

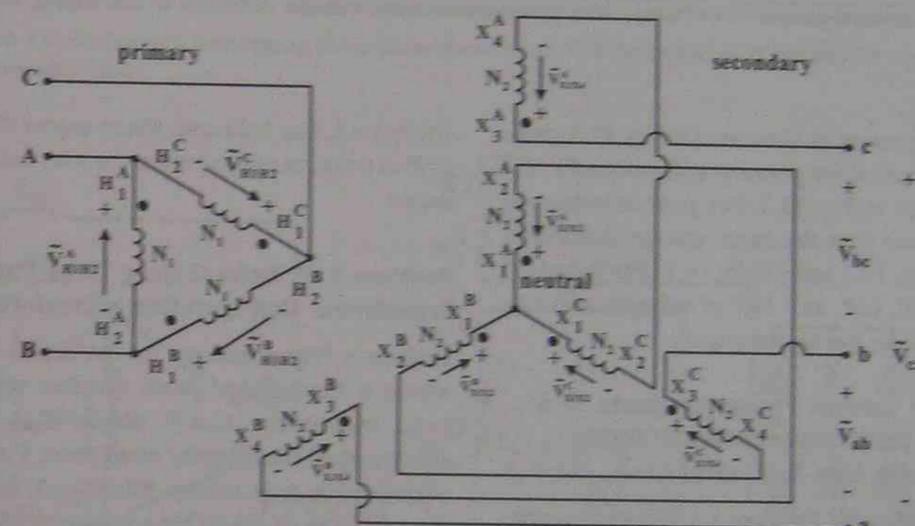


FIGURE P1.2 Connection of a delta/zigzag, three-phase transformer with the definition of primary and secondary voltages.

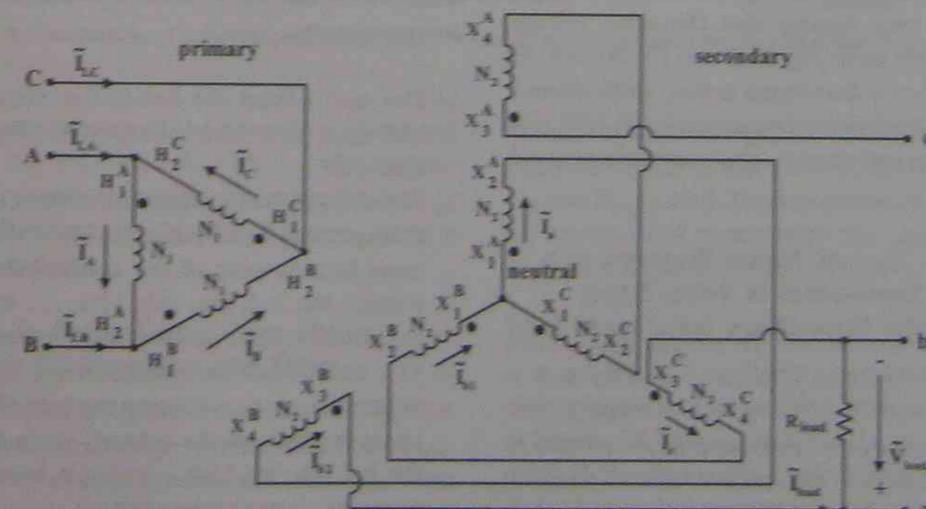


FIGURE P1.3 Connection of a delta/zigzag, three-phase transformer with the definition of current for line-to-line load.

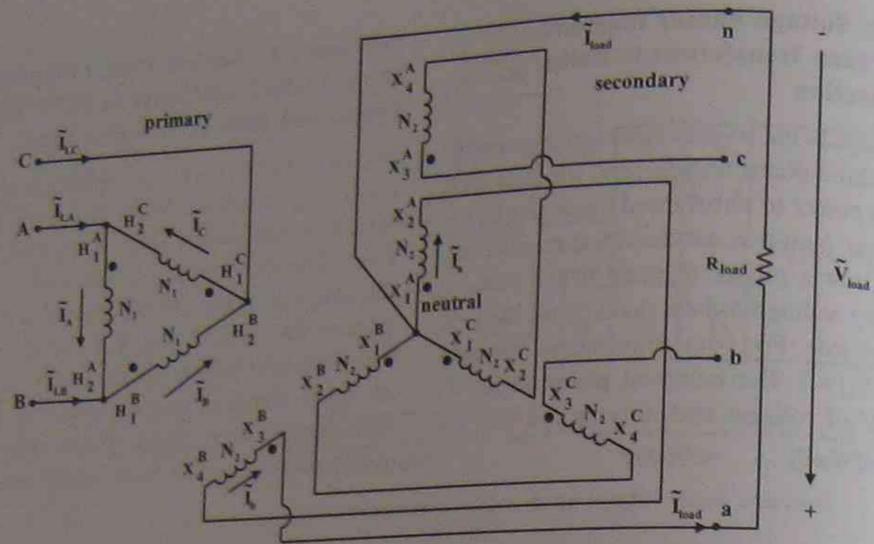


FIGURE P1.4 Connection of a delta/zigzag, three-phase transformer with the definition of current for line-to-neutral load.

($I_{load} = 10$ A) present as illustrated in Fig. P1.3, draw a phasor diagram of the primary and secondary currents as defined in Fig. P1.3. For your convenience you may assume that the same voltage definitions apply as in Fig. P1.2 and $(N_1/N_2) = 1$. For balanced phase angles 0° , 120° , and 240° of voltages and currents you may use hexagonal paper.

Problem 1.4: Current Phasor Diagrams of a Three-Phase Transformer in Delta/Zigzag Connection with Line-To-Neutral Load

Repeat the analysis of Problem 1.3 if there is a resistive line-to-neutral load on the secondary side ($I_{load} = 10$ A) present, as illustrated in Fig. P1.4; that is, draw a phasor diagram of the primary and secondary currents as defined in Fig. P1.4. For your convenience you may assume that the same voltage definitions apply as in Fig. P1.2 and $(N_1/N_2) = 1$. In this case the load is distributed to two (single-phase) transformers. For balanced phase angles 0° , 120° , and 240° of voltages and currents you may use hexagonal paper.

Problem 1.5: Current Phasor Diagrams of a Three-Phase Transformer in Delta/Zigzag Connection with Three-Phase Unbalanced Load

Repeat the analysis of Problem 1.3 if there is a resistive unbalanced load on the secondary side ($I_{loada} = 30$ A, $I_{loadb} = 20$ A, $I_{loadc} = 10$ A) present as illustrated in Fig. P1.5; that is, draw a phasor diagram of the primary and secondary currents as defined in Fig. P1.5. For your convenience you may assume that the same voltage definitions apply as in Fig. P1.2 and

$(N_1/N_2) = 1$. For balanced phase angles 0° , 120° , and 240° of voltages and currents you may use hexagonal paper.

Problem 1.6: Delta/Zigzag Three-Phase Transformer Configuration without Filter

Perform a PSpice analysis for the circuit of Fig. P1.6 where a three-phase diode rectifier without filter (e.g., capacitance $C_f = 0$) serves R_{load} . You may assume ideal transformer conditions. For your convenience you may assume $(N_1/N_2) = 1$, $R_{syst} = 0.01 \Omega$, $X_{syst} = 0.05 \Omega @ f = 60$ Hz, $v_{AB}(t) = \sqrt{2}600V \cos \omega t$, $v_{BC}(t) = \sqrt{2}600V \cos(\omega t - 120^\circ)$, $v_{CA}(t) = \sqrt{2}600V \cos(\omega t - 240^\circ)$, ideal diodes D_1 to D_6 , and $R_{load} = 10 \Omega$. Plot one period of either voltage or current after steady state has been reached as requested in the following parts.

- a) Plot and subject the line-to-line voltages $v_{ab}(t)$ and $v_{ab}(t)$ to a Fourier analysis. Why are they different?
- b) Plot and subject the input line current $i_{AL}(t)$ of the delta primary to a Fourier analysis. Note that the input line currents of the primary delta do not contain the 3rd, 6th, 9th, 12th, ..., that is, harmonic zero-sequence current components.
- c) Plot and subject the phase current $i_{Aph}(t)$ of the delta primary to a Fourier analysis. Why do the phase currents of the primary delta not contain the 3rd, 6th, 9th, 12th, ..., that is, harmonic zero-sequence current components?
- d) Plot and subject the output current $i_{aph}(t)$ of the zigzag secondary to a Fourier analysis. Why do

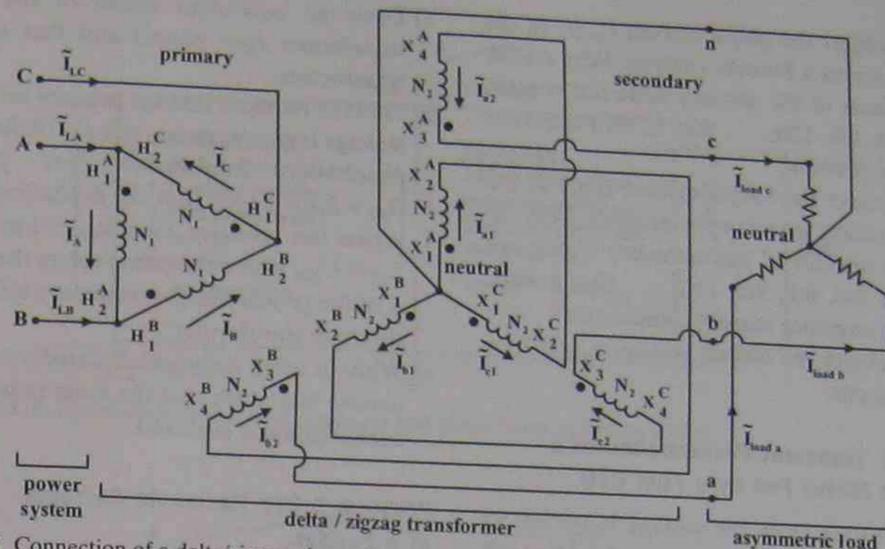


FIGURE P1.5 Connection of a delta/zigzag, three-phase transformer with the definition of currents for three-phase unbalanced line-to-neutral loads.

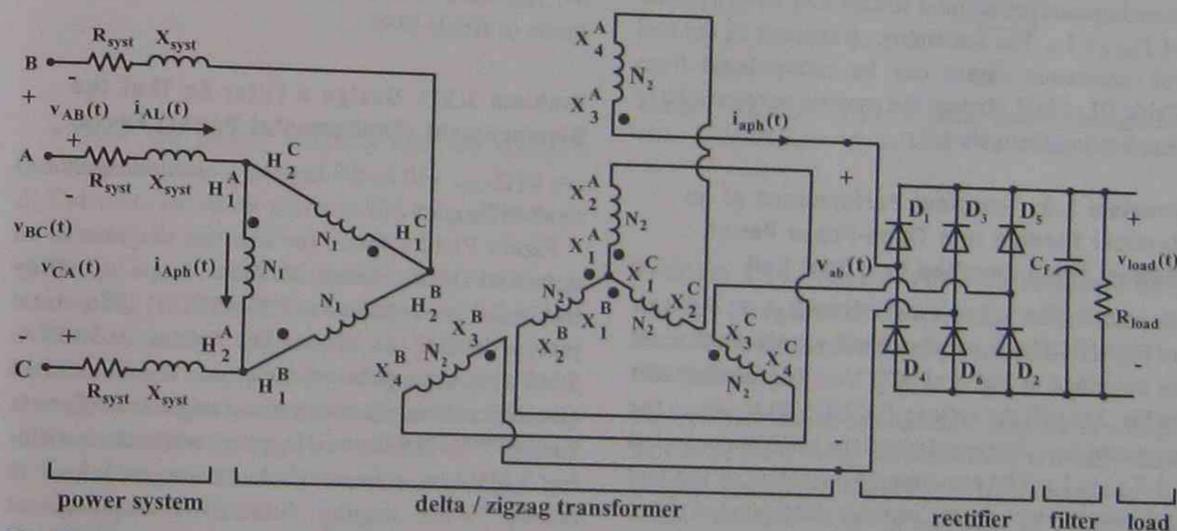


FIGURE P1.6 Connection of a delta/zigzag, three-phase transformer with a diode rectifier and load R_{load} .

the output currents of the secondary zigzag not contain the 3rd, 6th, 9th, 12th, ..., that is, harmonic zero-sequence current components?
 e) Plot and subject the output voltage $v_{load}(t)$ to a Fourier analysis.

$\sqrt{2}600V \cos \omega t$, $v_{BC}(t) = \sqrt{2}600V \cos(\omega t - 120^\circ)$, $v_{CA}(t) = \sqrt{2}600V \cos(\omega t - 240^\circ)$, ideal diodes D_1 to D_6 , and $R_{load} = 10 \Omega$. Plot one period of either voltage or current after steady state has been reached as requested in the following parts.

Problem 1.7: Delta/Zigzag Three-Phase Transformer Configuration with Filter

Perform a PSpice analysis for the circuit of Fig. P1.6 where a three-phase diode rectifier with filter (e.g., capacitance $C_f = 500 \mu F$) serves the load R_{load} . You may assume ideal transformer conditions. For your convenience you may assume $(N_1/N_2) = 1$, $R_{syst} = 0.01 \Omega$, $X_{syst} = 0.05 \Omega @ f = 60$ Hz, $v_{AB}(t) =$

- a) Plot and subject the line-to-line voltages $v_{AB}(t)$ and $v_{ab}(t)$ to a Fourier analysis. Why are they different?
- b) Plot and subject the input line current $i_{AL}(t)$ of the delta primary to a Fourier analysis. Note that the input line currents of the primary delta do not contain the 3rd, 6th, 9th, 12th, ..., that is, harmonic zero-sequence current components.

- c) Plot and subject the phase current $i_{Aph}(t)$ of the delta primary to a Fourier analysis. Why do the phase currents of the primary delta not contain the 3rd, 6th, 9th, 12th, ... that is, zero-sequence current components?
- d) Plot and subject the output current $i_{aph}(t)$ of the zigzag secondary to a Fourier analysis. Why do the output currents of the secondary zigzag not contain the 3rd, 6th, 9th, 12th, ... that is, harmonic zero-sequence current components?
- e) Plot and subject the output voltage $v_{load}(t)$ to a Fourier analysis.

Problem 1.8: Transient Performance of a Brushless Dc Motor Fed by a Fuel Cell

Replace the battery (with the voltage $V_{DC} = 300$ V) of Fig. E1.3.1 by the equivalent circuit of the fuel cell as described in Fig. 2 of [67]. You may assume that in Fig. 2 of [67] the voltage $E = 300 \pm 30$ V, where the superimposed rectangular voltage ± 30 V has a period of $T_{\pm 30V} = 1$ s. The remaining parameters of the fuel cell equivalent circuit can be extrapolated from Table III of [67]. Repeat the analysis as requested in Application Example 1.3.

Problem 1.9: Transient Performance of an Inverter Feeding into Three-Phase Power System When Supplied by a Fuel Cell

Replace the DC source (with the voltage $V_Q = 390$ V) of Fig. E1.5.1 by the equivalent circuit of the fuel cell as described in Fig. 2 of [67]. You may assume that in Fig. 2 of [67] the voltage $E = 390 \pm 30$ V, where the superimposed rectangular voltage ± 30 V has a period of $T_{\pm 30V} = 1$ s. The remaining parameters of the fuel cell equivalent circuit can be extrapolated from Table III of [67]. Repeat the analysis as requested in Application Example 1.5.

Problem 1.10: Suppression of Subharmonic of 30 Hz with a Dedicated Transformer

The air-conditioning drive (compressor motor) generates a subharmonic current of $I_{30Hz} = 1$ A due to spatial harmonics (e.g., selection of number of slots, rotor eccentricity). A sensitive load fed from the same pole transformer is exposed to a terminal voltage with the low beat frequency of 30 Hz. A dedicated transformer can be used to suppress the 30 Hz component from the power supply of the sensitive load (see Fig. E1.6.1 and Figs. E1.6.4 to E1.6.6).

The parameters of the single-phase pole transformer at 60 Hz are $X_{sp} = X'_{sp} = 0.07 \Omega$, $R_{sp} = R'_{sp} = 0$.

- a) Draw an equivalent circuit of the substation transformer (per phase) and that of the pole transformer.
- b) Find the required leakage primary and secondary leakage inductances L_{pD} and L_{sD} of the substation distribution transformer (per phase) for $R_{pD} = R_{sD} = 0$ such that the subharmonic voltage across the sensitive load $v_{30Hz} \leq 1$ mV provided $L_{sD} = L'_{pD}$, where the prime refers the inductance L of the primary to the secondary side of the distribution transformer.
- c) Without using a dedicated transformer, design a passive filter such that the same reduction of the subharmonic is achieved.

Problem 1.11: Harmonic Currents of a Feeder

For Application Example 1.9 (Fig. E1.9.1), calculate the harmonic currents associated with users #3 and #4. Are they within the permissible power quality limits of IEEE-519?

Problem 1.12: Design a Filter So That the Displacement (Fundamental Power) Factor

$\cos \Phi_1^{\text{total}}$ will be 0.9 lagging (consumer notation) $\leq \cos \Phi_1^{\text{total}}$ with filter ≤ 1.0 .

Figure P1.12.1 shows the one-line diagram of an industrial plant being serviced from a utility transmission voltage at 13.8 kV_{L-L}. The total power demand on the utility system is 5 MVA: 3 MVA is a six-pulse static power converter load (three-phase rectifier with firing angle $\alpha = 30^\circ$, note $\cos \Phi_1^{\text{nonlinear}} = 0.955 \cos \alpha$ lagging), while the remaining 2 MVA is a linear (induction motor) load at $\cos \Phi_1^{\text{linear}} = 0.8$ lagging (inductive) displacement (fundamental power) factor. The system impedance is $Z_{\text{sys}} = 10\%$ referred to a 10 MVA base.

- a) Calculate the short-circuit apparent power S_{SC} at PCC.
- b) Find the short-circuit current $I_{sc\text{phase}}$.
- c) Before filter installation, calculate the displacement (fundamental power) factor $\cos \Phi_1^{\text{total}}$ without filter, where Φ_1^{total} without filter is the angle between the fundamental voltage $\tilde{V}_{\text{phase}} = V_{\text{phase}} \angle 0^\circ$ and the total fundamental phase current. Hint: For calculation of $\tilde{I}_{\text{totalphase}}$ you may:
- use a (per-phase) phasor diagram and perform calculations using the cosine law (see Fig. P1.12.2): $a^2 = b^2 + c^2 - 2 \cdot b \cdot c \cdot \cos(\alpha)$
 - draw the phasor diagram to scale and find $\tilde{I}_{\text{totalphase}}$ by graphical means, or

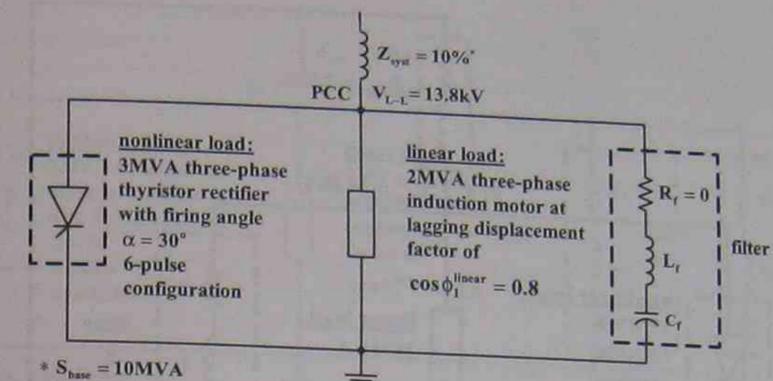


FIGURE P1.12.1 Distribution feeder with nonlinear and linear loads as well as a filter.

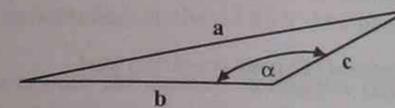


FIGURE P1.12.2 The application of cosine law.

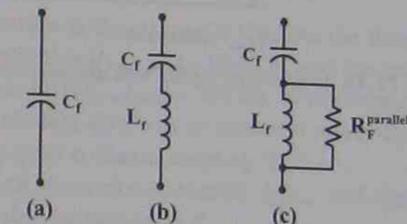


FIGURE P1.12.3 Types of filters under consideration ($C_f = 32.36 \mu\text{F}$, $L_f = 6.04$ mH, $R_{\text{parallel}} = 500 \Omega$).

- draw the phasor diagram and use complex calculations.
- d) Calculate harmonic currents and voltages without filter.
- e) Design a passive LC filter at the point of common coupling (PCC) such that the fundamental (60 Hz) current through the filter is $\tilde{I}_F = j100$ A. In your filter design calculation you may neglect the influence of the ohmic resistance of the filter ($R_f = 0$). Tune the filter to the 6th harmonic: this will lead to two equations and two unknowns (L_f and C_f).
- f) Calculate the displacement (fundamental power) factor $\cos \Phi_1^{\text{total}}$ after the filter has been installed. Is this filter design acceptable from a displacement (fundamental power) factor point of view?
- g) After the filter has been installed ($L_f = 6.04$ mH, $C_f = 32.36 \mu\text{F}$, $R_f = 0$), compute $\rho_{\text{system}}^{5\text{th}}$ and $\rho_{\text{system}}^{7\text{th}}$. These two values provide information about resonance conditions within the feeder. What type of resonance exists?
- h) Calculate the harmonic currents and voltages with filter.
- i) Is there any advantage of using the $L_f C_f$ filter (see Fig. P1.12.3b) as compared to that of Fig. P1.12.3a? What is the effect on filtering if a resistance R_{parallel} is connected in parallel with the inductor L_f (see Fig. P1.12.3c)?

Problem 1.13: Passive Filter Calculations as Applied to A Distribution Feeder with One User Including a Twelve-Pulse Static Power Converter Load

Figure P1.13 shows the one-line diagram of a large industrial plant being serviced from a utility transmission voltage at 13.8 kV_{L-L}. The demand on the utility system is 50 MVA and 50% of its load is a 12-pulse static power converter load. For a system impedance $Z_{\text{sys}} = 2.3\%$ referred to a 10 MVA base and a short-circuit current to load current ratio of $R_{sc} = 8.7$, design a passive RLC filter at the point of common coupling (PCC) such that the injected current harmonics and the resulting voltage harmonics at PCC are within the limits of IEEE-519 as proposed by the paper of Duffey and Stratford [64]: this paper shows that (without filter) the 11th, 13th, 23rd, 25th, 35th, 37th, 47th, and 49th current harmonics do not satisfy the limits of IEEE-519, and the 11th and 13th harmonic voltages violate the guidelines of IEEE-519 as well.

- a) For your design you may assume that an inductor with $L = 1$ mH and $R = 0.10 \Omega$ is available. Is this inductor suitable for such an RLC filter design?

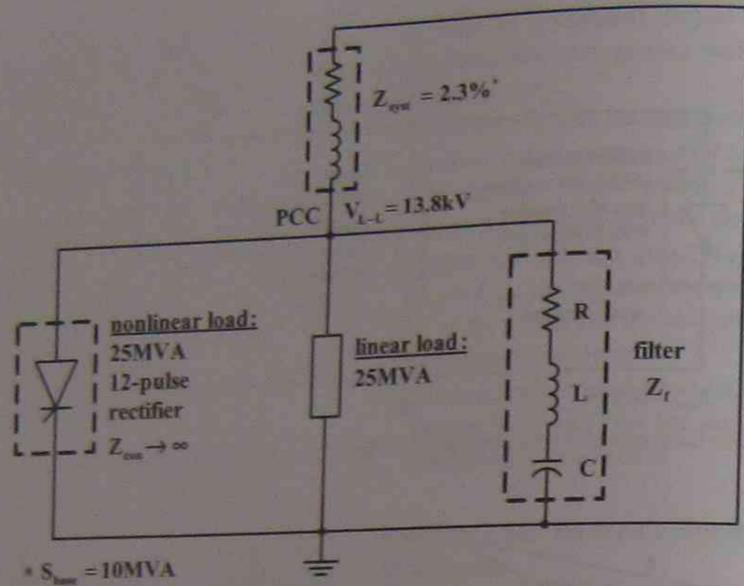


FIGURE P1.13 Distribution feeder with one user including linear and nonlinear (12-pulse converter) load.

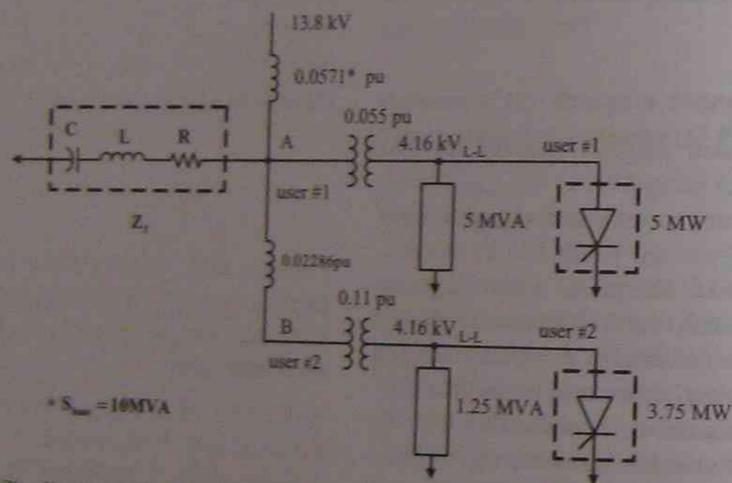


FIGURE P1.14 Utility distribution feeder with two users.

Compute the fundamental (60 Hz) current through the RLC filter and compare this current with the total load current.
 b) Calculate the harmonic currents and the harmonic voltages (5th to 19th) at PCC after the filter has been installed.

Problem 1.14: Passive Filter Calculations as Applied to a Distribution Feeder with Two Users Each with a Six-Pulse Static Power Converter Load

Figure P1.14 shows a utility distribution feeder that has two users along the radial feeder. Each user sees a different value of short-circuit or system size.

- For each user compute from the plant specifications apparent short-circuit power S_{sc} , short-circuit phase current I_{scph} , apparent load power S_L , load phase current I_{Lph} , load phase current of static power converter I_{Lpsc} , and short-circuit ratio R_{sc} .
- Determine the harmonic currents (in amperes and %) injected into the system due to the static power converter loads (up to 19th harmonic).
- Compute the harmonic voltages (in volts and %) induced at the 13.8 kV bus due to the harmonic currents transmitted (up to 19th harmonic).
- Design an RLC filter tuned to the frequency of the current with the largest harmonic amplitude. You may assume that an inductor with $R = 0.10 \Omega$ and $L = 1 \text{ mH}$ is available. The filter is

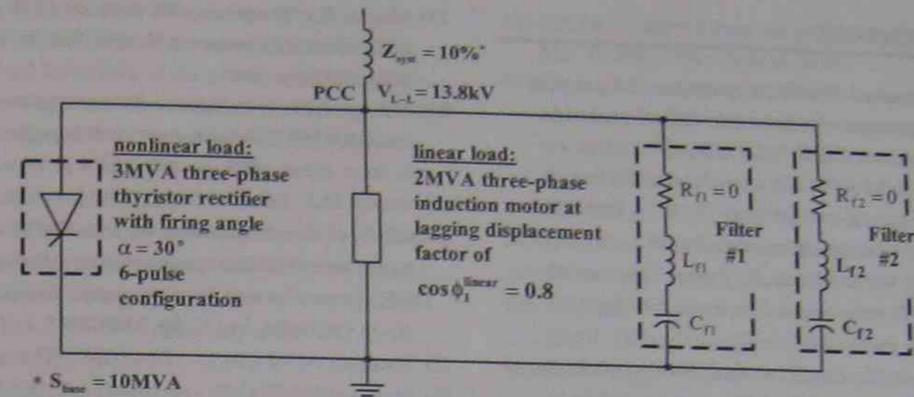


FIGURE P1.15 Distribution feeder with nonlinear and linear loads and two LC series filters.

- to be installed at the 13.8 kV bus next to user #1.
- Recompute the current harmonics (up to 19th harmonic) transmitted into the system and their associated harmonic voltages at the 13.8 kV bus after the filter has been installed.
- Are the harmonic currents and voltages at user #1 and user #2 within the limits of IEEE-519 as recommended by the paper of Duffey and Stratford [64]?

Problem 1.15: Design Two Series LC Filters So That the Total Displacement Power Factor $\cos \Phi_1^{\text{total}}$ Will Be 0.9 lagging $\leq \cos \Phi_1^{\text{total}} \leq 1.0$, and the Recommendations According To IEEE-519 Are Satisfied

Figure P1.15 shows the one-line diagram of an industrial plant being serviced from a utility transmission voltage at 13.8 kV_{L-L}. The total power demand on the utility system is 5 MVA: 3 MVA is a six-pulse static power converter load (three-phase rectifier with firing angle $\alpha = 30^\circ$, note $\cos \Phi_1^{\text{nonlinear}} = 0.955 \cos \alpha$ lagging), while the remaining 2 MVA is a linear (induction motor) load at $\cos \Phi_1^{\text{linear}} = 0.8$ lagging (inductive) displacement power factor. The system impedance is $Z_{\text{sys}} = 10\%$ referred to a $S_{\text{base}} = 10 \text{ MVA}$ base.

- Before any filter installation, the displacement power factor is $\cos \Phi_1^{\text{total}} = 0.826$ or $\Phi_1^{\text{total}} = 34.2^\circ$ lagging. The nonlinear load

- current is $I_{\text{nonlinear_load}} = 125.5 \text{ A}$, the linear load current is $I_{\text{linear_load}} = 83.66 \text{ A}$, and the total load current is $I_{\text{total_load}} = 209.1 \text{ A}$. Verify these data.
- Calculate short-circuit apparent power S_{sc} at the point of common coupling (PCC).
- Find short-circuit current $I_{sc\text{phase}}$ and the short-circuit current ratio R_{sc} .
- If no filter is employed the harmonic currents I_h exceed IEEE-519 limits. Calculate the harmonic currents and voltages at PCC without filter.
- To comply with IEEE-519 it is recommended to install two filters: one tuned at the 5th and the other one tuned at the 11th harmonic. Design two passive, series LC filters at PCC such that the fundamental (60 Hz) current through each of the filters is $I_{F1} = I_{F2} = I_F = j50 \text{ A}$. In your filter design calculation you may neglect the influence of the ohmic resistances of the two filters ($R_{F1} = R_{F2} = R_F = 0$). As a function of the filter impedance of filter #1, $Z_{F1,h}$, and the filter impedance of filter #2, $Z_{F2,h}$, you may calculate an equivalent filter impedance $Z_{\text{Equivalent},h}$ which can be used for the calculation of the parameter $\rho_{\text{vph},h}$. Make sure there are no parallel resonances.
- Calculate the harmonic currents $I_{\text{vph}5}$ through $I_{\text{vph}19}$ after the two filters have been installed. Are the harmonic currents after these two filters have been installed below the recommended IEEE-519 limits?
- Calculate the harmonic voltages V_h at PCC. Do they meet IEEE-519 recommendations?

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