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8:00 PM

Expt  
+  
G012

ELECTRICAL SYSTEM (POWER)

ELECTRICAL SYSTEM (ELECTRONICS)

+ G029

EQUIVALENT

POWER SUPPLY

ANALOG ELECTRONICS  
AMPLIFIER

DIGITAL ELECTRONICS

H011

POWER SOURCE

H013

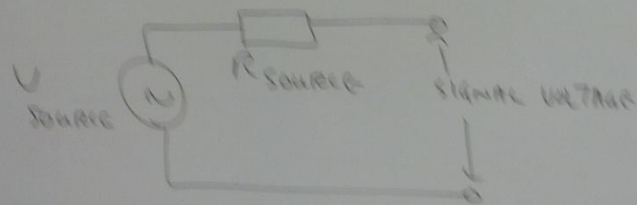
H012

(+) MECHANICAL  
PRINCIPLE

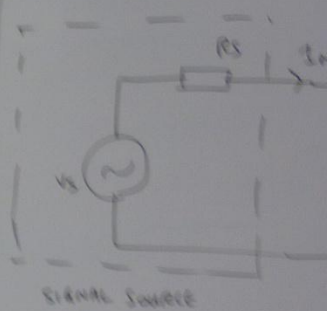
INPUT SIGNAL

OUTPUT SIGNAL

THE INPUT SIGNAL IS AMPLIFIED BY AMPLIFIER



EQUIVALENT CIRCUIT OF SIGNAL SOURCE



$V_S$  = SOURCE VOLTAGE

$R_S$  = SOURCE RESISTANCE

$R_{in}$  = INPUT RESISTANCE

$A_V$  = VOLTAGE GAIN

$V_{in}$  = INPUT VOLTAGE

$V_{out}$  = OUTPUT VOLTAGE

$R_{load}$  = LOAD RESISTANCE

System (Power)

System (Electronics)

ICS  
ICER  
3

DIGITAL ELECTRONICS

HOI2



MECHANICAL  
PRINCIPLE

UT SIGNAL

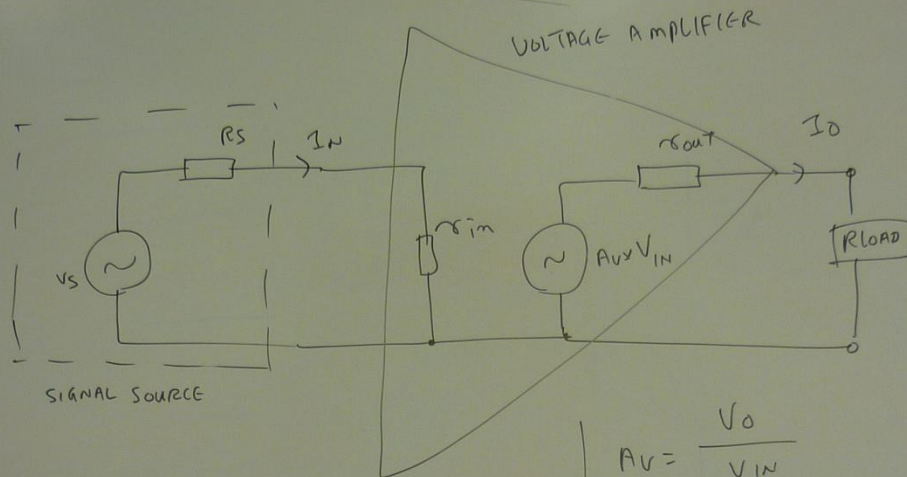
By AMPLIFIER

VOLTAGE

SIGNAL SOURCE

+E029

## EQUIVALENT CIRCUIT OF AMPLIFIER



$V_S$  = SOURCE VOLTAGE

$R_S$  = SOURCE RESISTANCE

$r_{im}$  = INPUT RESISTANCE

$A_V$  = VOLTAGE GAIN

$V_{IN}$  = INPUT VOLTAGE

$r_{out}$  = OUTPUT RESISTANCE

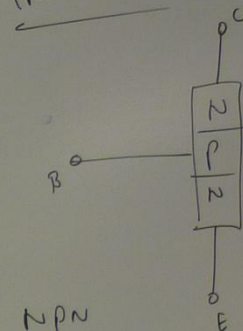
$R_{LOAD}$  = LOAD RESISTANCE

$$A_V = \frac{V_O}{V_{IN}}$$

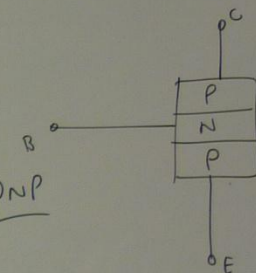
$$A_I = \frac{I_O}{I_{IN}} \quad (\text{CURRENT GAIN})$$

$$A_P = \frac{P_O}{P_{IN}} \quad (\text{POWER GAIN})$$

## TRANSISTOR

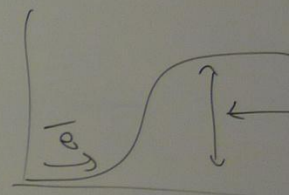


NPN



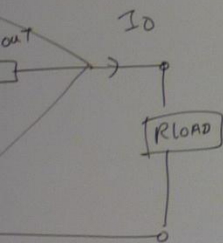
PNP

CONSTRUCTION





AMPLIFIER



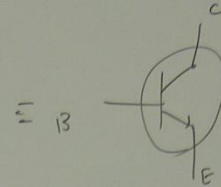
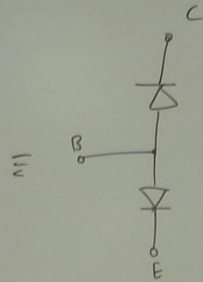
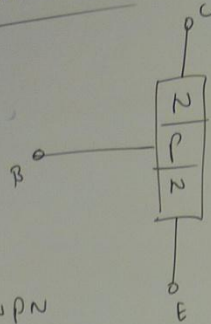
$$\frac{V_O}{V_{IN}}$$

$$= \frac{I_O}{I_{IN}} \quad (\text{CURRENT GAIN})$$

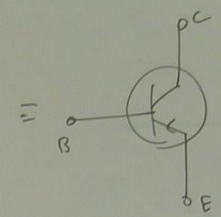
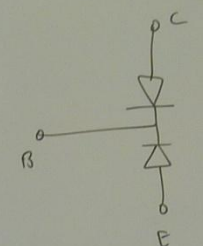
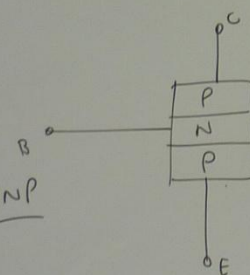
$$= \frac{P_O}{P_{IN}} \quad (\text{POWER GAIN})$$

## TRANSISTOR

NPN



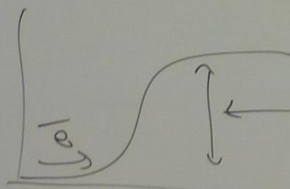
PNP



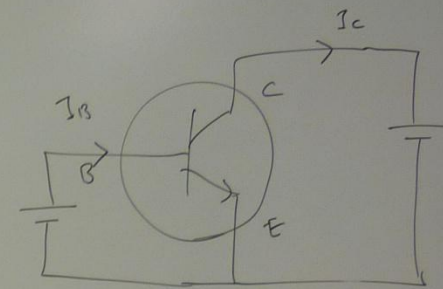
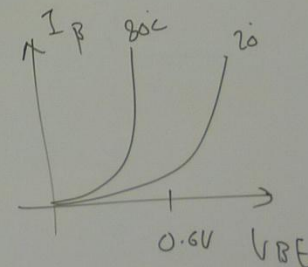
CONSTRUCTION

DIODE EQUIVALENCE

SYMBOLIC SYMBOL



$V_{BE}$  IS APPLIED TO REDUCE THE POTENTIAL BARRIER



$$\beta = \frac{I_C}{I_B}$$

$I_C$  = COLLECTOR CURRENT

$I_B$  = BASE CURRENT

$\beta$  = CURRENT GAIN  
( $h_{fe}$ )

$$V_{CE} =$$

THE

TI

E

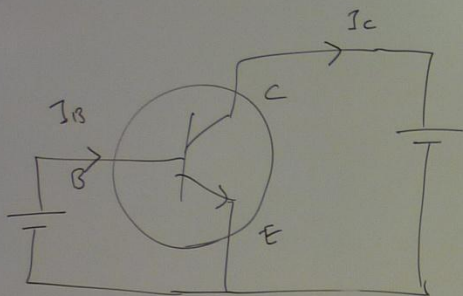
POWER

THE

THE

USE

- E



$$\beta = \frac{I_C}{I_B}$$

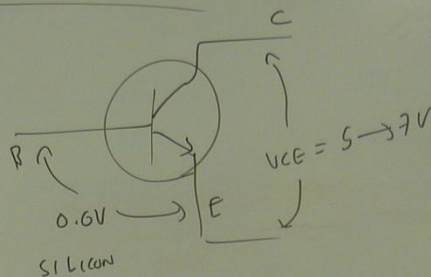
$I_C$  = collector current

$I_B$  = base current

$\beta$  = current gain  
( $h_{fe}$ )

$V_{BE}$

$V_{CE}$  = collector voltage



THE CASE OUTLINE

TYPE OF PACKAGE USED TO CONTAIN THE TRANSISTOR ELEMENT.

POWER RATING

THE POWER RATING OF A TRANSISTOR DETERMINES THE MAXIMUM COLLECTOR CURRENT THAT CAN BE USED FOR THAT TRANSISTOR FOR A GIVEN COLLECTOR-EMITTER VOLTAGE.

300mW  $\rightarrow$  SEVERAL HUNDRED WATTS.

AMPLIFICATION

AN IDEAL AMPLIFIER WILL HAVE THE SAME SHAPE AS IN P

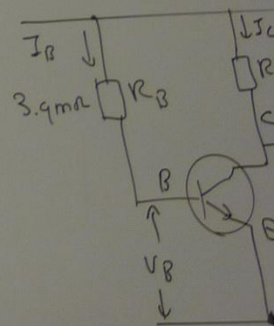
TRANSISTOR

WHEN USED AS AN AMPLIFIER, IT IS FORWARD BIASED.

A VOLTAGE ACROSS

THIS VOLTAGE NEEDS A SUPPLY AND VARIOUS

BASIC TRANSISTOR CIRCUITS





$V_{CE} = 5 \rightarrow 7V$

TO CONTAIN THE TRANSISTOR

TRANSISTOR DETERMINES

R CURRENT THAT CAN BE

STOR FOR A GIVEN COLLECTOR-

SEVERAL HUNDRED WATTS.

## AMPLIFICATION

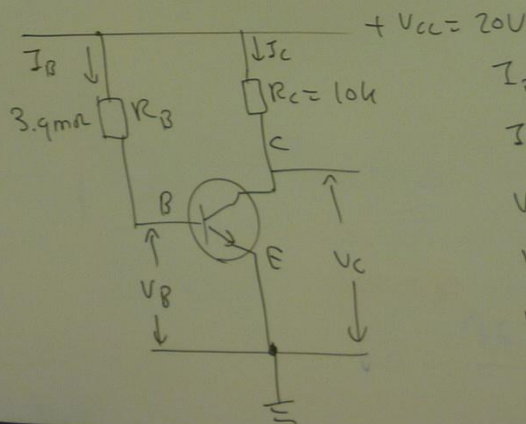
AN IDEAL AMPLIFIER WILL PRODUCE AN OUTPUT SIGNAL THAT HAS EXACTLY THE SAME SHAPE AS INPUT. EXCEPT IT WILL BE LARGER

## TRANSISTOR BIASING

WHEN USED AS AMPLIFIER, A TRANSISTOR NEEDS TO BE FORWARD BIASED. IT'S BASE-EMITTER JUNCTION MUST HAVE A VOLTAGE ACROSS IT OF 0.6V.

THIS VOLTAGE NEEDS TO BE SUPPLIED FROM DC POWER SUPPLY AND VARIOUS METHODS ARE USED TO ACHIEVE IT.

## BASIC TRANSISTOR - AMPLIFIER CIRCUIT



$I_B$  = BASE CURRENT

$I_C$  = COLLECTOR CURRENT

$V_B$  = BASE VOLTAGE

$V_C$  = COLLECTOR VOLTAGE

$V_{CC}$  = SUPPLY VOLTAGE

## DC CONDITION

WHEN AN AMPLIFIER IS USED AROUND THE CIRCUIT

Pb IN GIVEN DC

$$I_B = \frac{V_{CC} - V_B}{R_E}$$

$$I_C = \beta I_B$$

$$V_C = V_{CC} - I_C R_C$$

## FACTORS INFLUENCING

TEMPERATURE

THE OPERATION

Pb CALCULATE

IF THE VALUE

$$I_B = 5 \mu A$$

$$V_C = V_{CC} - I_C R_C$$

THE OUTPUT SIGNAL THAT HAS EXACTLY  
IT WILL BE LARGER

SIGNAL

THE RESISTOR NEEDS TO BE  
E-EMITTER JUNCTION MUST HAVE  
0.6V.

SUPPLIED FROM DC POWER  
IS ARE USED TO ACHIEVE IT  
ER CIRCUIT

$V_C = 20V$

$I_B =$  BASE CURRENT

$I_C =$  COLLECTOR CURRENT

$V_B =$  BASE VOLTAGE

$V_C =$  COLLECTOR VOLTAGE

$V_{CC} =$  SUPPLY VOLTAGE

### DC CONDITION

WHEN AN AMPLIFIER HAS NO INPUT SIGNAL, THE DC VOLTAGE  
AROUND THE CIRCUIT IS REFERRED AS DC QUIESCENT VOLTAGE

Pb IN GIVEN DIAGRAM, CALCULATE  $I_B$ ,  $I_C$  AND  
 $V_C$ .  $\beta = 200$

$$I_B = \frac{V_{CC} - V_{BE}}{R_E} = \frac{20 - 0.6}{3.4 \text{ M}\Omega} = \frac{19.4}{3.4 \times 10^6} = 5 \mu\text{A}$$

$$I_C = \beta I_B = 200 \times 5 \mu\text{A} = 1000 \mu\text{A} = 1 \text{ mA}$$

$$V_C = V_{CC} - I_C R_C = 20 - 1 \times 10^{-3} \times 10 \times 10^3 = 10V$$

### FACTORS INFLUENCING THE PERFORMANCE OF TRANSISTOR

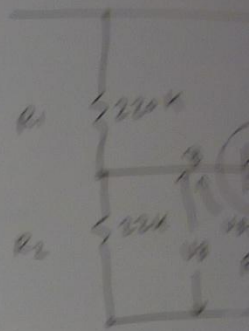
TEMPERATURE CHANGE AND CHANGE IN  $\beta$  EFFECTS  
THE OPERATION OF ELECTRONIC CIRCUIT.

Pb CALCULATE THE VALUE OF  $V_C$  FOR GIVEN CIRCUIT  
IF THE VALUE OF  $\beta$  INCREASES FROM 200 TO 300.

$$I_B = 5 \mu\text{A} \quad \beta = 300 \rightarrow I_C = \beta I_B = 300 \times 5 \mu\text{A}$$

$$V_C = V_{CC} - I_C R_C = 20 - 1.5 \times 10^{-3} \times 10 \times 10^3 = 5V$$

### POTENTIAL DIVIDER



$$V_B = \frac{V_{CC} \times R_3}{R_2 + R_3}$$

$$V_B = V_B -$$

$$I_E = I_C$$

$$V_C = V_{CC}$$



CONDITION

AN AMPLIFIER HAS NO INPUT SIGNAL, THE DC VOLTAGE AND THE CURRENT IS REFERRED AS DC QUIESCENT VOLTAGE.

IN GIVEN DIAGRAM, CALCULATE  $I_B$ ,  $I_C$  AND  $V_C$ .

$$I_B = \frac{V_{CC} - V_{BE}}{R_E} = \frac{20 - 0.6}{3.9 \times 10^3} = \frac{19.4}{3.9 \times 10^3} = 5 \mu A$$

$$I_C = \beta I_B = 200 \times 5 \mu A = 1000 \mu A = 1 mA$$

$$V_C = V_{CC} - I_C R_C = 20 - 1 \times 10^{-3} \times 10 \times 10^3 = 10V$$

FACTORS INFLUENCING THE PERFORMANCE OF TRANSISTOR

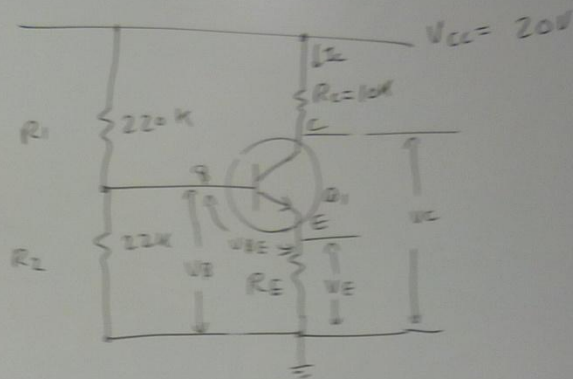
TEMPERATURE CHANGE AND CHANGE IN  $\beta$  EFFECTS THE OPERATION OF ELECTRONICS CIRCUIT.

CALCULATE THE VALUE OF  $V_C$  FOR GIVEN CIRCUIT IF THE VALUE OF  $\beta$  INCREASES FROM 200 TO 300.

$$I_B = 5 \mu A \quad \beta = 300 \rightarrow I_C = \beta I_B = 300 \times 5 \mu A$$

$$V_C = V_{CC} - I_C R_C = 20 - 1.5 \times 10^{-3} \times 10 \times 10^3 = 1.5V$$

## POTENTIAL DIVIDER BIASING



$$V_B = \frac{V_{CC} \times R_2}{R_1 + R_2}$$

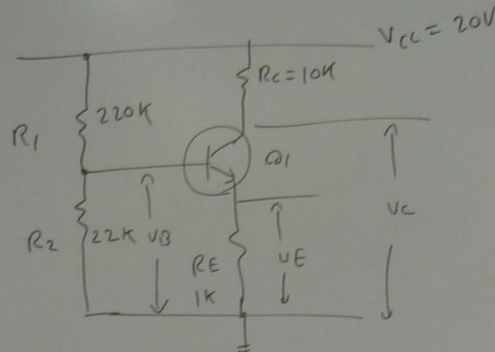
$$V_E = V_B - V_{BE}$$

$$I_E = I_C = \frac{V_E}{R_E}$$

$$V_C = V_{CC} - I_C R_C$$

pb

CALCULATE  $V_B$ ,  $V_E$ ,  $I_E$  AND  $V_C$  FOR THE GIVEN CIRCUIT.



$$V_B = \frac{V_{CC} R_2}{R_1 + R_2} = \frac{20 \times 22k}{(220 + 22)k} = 1.8V$$

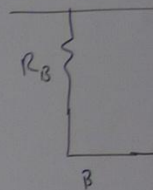
$$V_E = V_B - V_{BE} = 1.8 - 0.6 = 1.2V$$

$$I_E = I_C = \frac{V_E}{R_E} = \frac{1.2}{1k} = 1.2mA$$

$$V_C = V_{CC} - I_C R_C = 20 - 1.2 \times 10^{-3} \times 10 \times 10^3 = 8V$$

pb

FOR THE  
TO GIVE  
VOLTAGE.



$$V_C = V_{CC}$$

$$10 = 20$$

$$3.3 \times 10^3 I_C$$

$$I_C = \beta$$

$$R_B =$$



and  $V_{CC}$  for the given circuit.

$$\frac{2k}{2k} = 1.8V$$

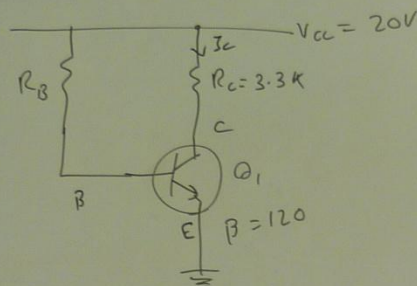
$$= 1.2V$$

$$= 1.2mA$$

$$= 1.2 \times 10^{-3} \times 10 \times 10^3$$

pb

FOR THE GIVEN CIRCUIT, DETERMINE THE VALUE OF  $R_B$  REQUIRED TO GIVE A VALUE OF COLLECTOR VOLTAGE EQUAL TO HALF THE SUPPLY VOLTAGE.



$$V_C = 10V$$

$$R_B = \frac{V_{CC} - V_{BE}}{I_B}$$

$$I_C = \beta I_B$$

$$V_C = V_{CC} - I_C R_C \rightarrow$$

$$10 = 20 - I_C \times 3.3 \times 10^3$$

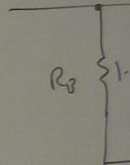
$$3.3 \times 10^3 I_C = 20 - 10 = 10 \Rightarrow I_C = \frac{10}{3.3 \times 10^3} = 3.3 \times 10^{-3} A$$

$$I_C = \beta I_B \Rightarrow I_B = \frac{I_C}{\beta} = \frac{3.3 \times 10^{-3}}{120} = 2.75 \times 10^{-6} A$$

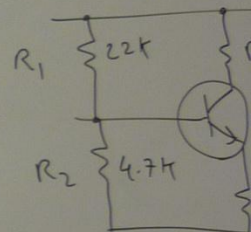
$$R_B = \frac{V_{CC} - V_{BE}}{I_B} = \frac{20 - 0.6}{2.75 \times 10^{-6}} = 7.09 \times 10^6 \Omega = 7.09 M\Omega$$

## EXERCISES

① FOR THE GIVEN



② CALCULATE THE B VOLTAGE  $V_C$  FOR



DETERMINE THE VALUE OF  $R_B$  REQUIRED  
 VOLTAGE EQUAL TO HALF THE SUPPLY

$$V_C = 10V$$

$$R_B = \frac{V_{CC} - V_{BE}}{I_B}$$

$$I_C = \beta I_B$$

$$\Rightarrow I_C = \frac{10}{3.3 \times 10^{-3}} = 3.3 \times 10^{-3} A$$

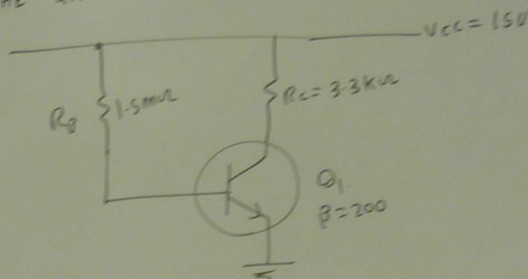
$$\frac{I_C}{\beta} = \frac{3.3 \times 10^{-3}}{120} = 2.75 \times 10^{-6} A$$

$$\frac{20 - 0.6}{2.75 \times 10^{-6}} = 7.09 \times 10^6 \Omega$$

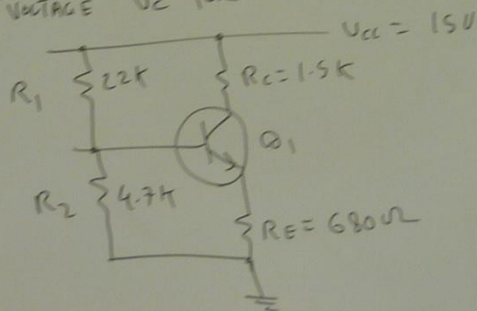
$$= 7.09 M\Omega$$

### EXERCISES

① FOR THE GIVEN CIRCUIT, CALCULATE (a)  $I_B$  (b)  $I_C$  (c)  $V_C$



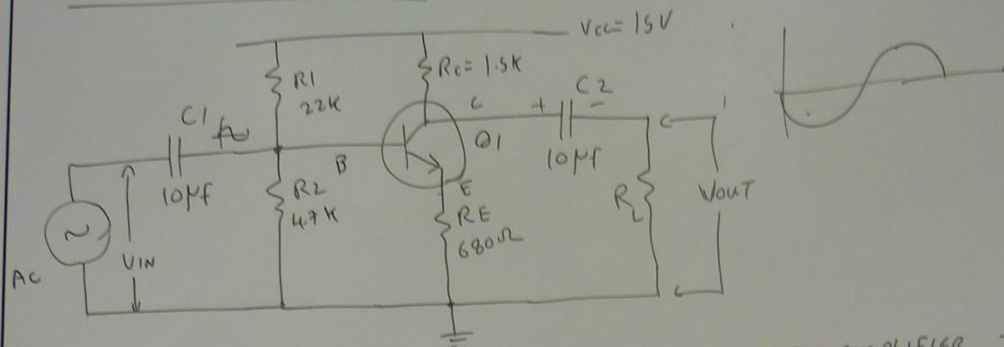
② CALCULATE THE BASE VOLTAGE  $V_B$ , EMITTER VOLTAGE  $V_E$  AND COLLECTOR VOLTAGE  $V_C$  FOR GIVEN CIRCUIT





# TRANSISTOR - AMPLIFIER (PART 1) AC CONDITIONS OF A TRANSISTOR AMPLIFIER

## COMMON EMITTER AMPLIFIER



THE ABOVE DIAGRAM SHOWS A COMMON EMITTER AMPLIFIER THAT USES POTENTIAL DIVIDER BIASING AND EMITTER STABILISING.

$C_1$  = INPUT COUPLING CAPACITOR,  $C_2$  = OUTPUT COUPLING CAPACITOR TO PREVENT DC VOLTAGE.

## AC CONDITIONS

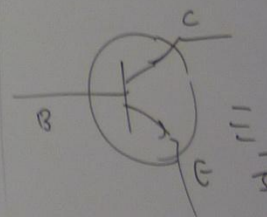
THE AC CHARACTERISTICS OF CE-TRANSISTOR AMPLIFIER

- VOLTAGE GAIN, INPUT RESISTANCE, OUTPUT RESISTANCE.

## CHARACTERISTICS

- THE INPUT
- THE VOLTAGE
- THE OUTPUT
- THE OUTPUT
- THE WAVE
- THE BASE.

## VOLTAGE GAIN



$$r_e = \frac{30 \text{ mV}}{I_c}$$

## CONDITIONS OF A TRANSISTOR AMPLIFIER



TRANSISTOR AMPLIFIER THAT USES  
BIASING.

OUTPUT COUPLING CAPACITOR

TRANSISTOR AMPLIFIER

INPUT RESISTANCE, OUTPUT RESISTANCE.

## CHARACTERISTICS OF CE AMPLIFIER

- THE INPUT SIGNAL HAS NO DC COMPONENT
- THE VOLTAGE AT BASE HAS BOTH DC & AC COMPONENT
- THE OUTPUT TERMINAL HAS NO DC COMPONENT
- THE OUTPUT SIGNAL IS 180° OUT OF PHASE WITH INPUT SIGNAL
- THE WAVE FORM AT EMITTER IS IDENTICAL TO THAT AT THE BASE.

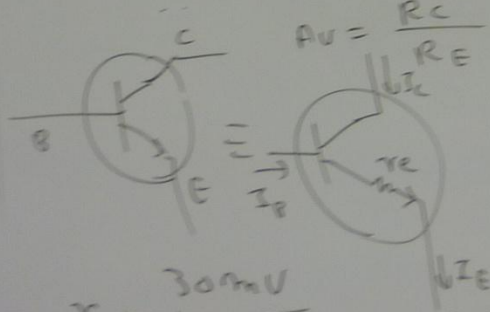
VOLTAGE GAIN  $A_V$

$$A_V = \frac{V_O}{V_{IN}}$$

$$A_V = \frac{R_C}{R_E + r_e}$$

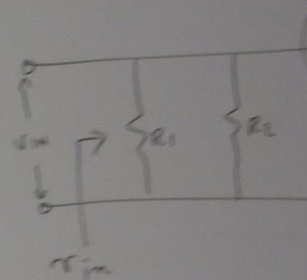
$$A_V = \frac{R_C}{R_E}$$

$$A_V = \frac{R_C \parallel R_L}{R_E + r_e}$$



$$r_e = \frac{30 \text{ mV}}{I_C (\text{mA})}$$

## INPUT AND OUTPUT

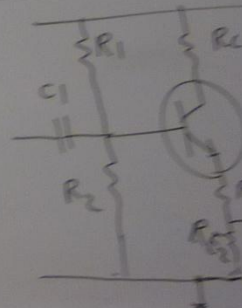


$$r_{in} = R_1 \parallel R_2$$

## OUTPUT RESISTANCE

$$r_o = R_C$$

## ADDING THE Emitter





# AMPLIFIER

NO DC COMPONENT

BOTH DC & AC COMPONENT

NO DC COMPONENT

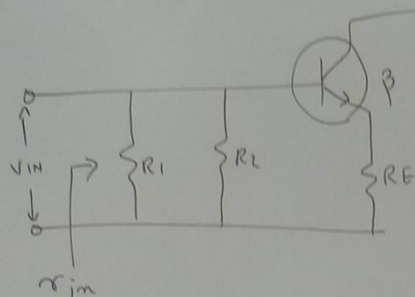
180° OUT OF PHASE WITH INPUT SIGNAL

WAVEFORM IS IDENTICAL TO THAT AT

$$A_v = \frac{R_C}{R_E + r_e}$$

$$A_v = \frac{R_C \parallel R_L}{R_E + r_e}$$

## INPUT AND OUTPUT RESISTANCE OF CE AMPLIFIER

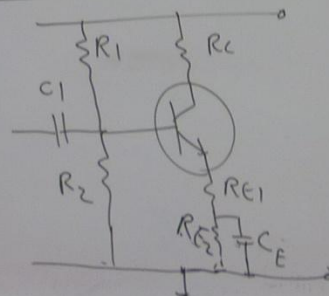


$$r_{in} = R_1 \parallel R_2 \parallel \beta(r_e + R_E)$$

## OUTPUT RESISTANCE

$$r_o = R_C$$

## ADDING THE EMITTER BYPASS CAPACITOR



$C_E$  = EMITTER BYPASS CAPACITOR

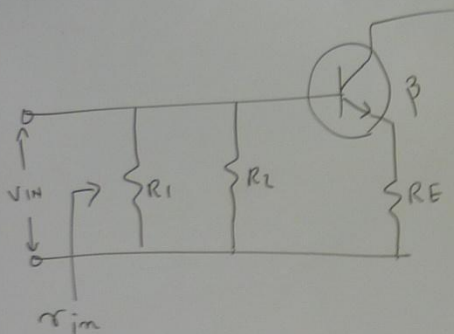
TO PROVIDE DC STABILITY

AC COMPONENT  
PHASE WITH INPUT SIGNAL  
IDENTICAL TO THAT AT

$$\frac{R_C}{R_E + r_e}$$

$$\frac{R_C \parallel R_L}{R_E + r_e}$$

## INPUT AND OUTPUT RESISTANCE OF CE AMPLIFIER

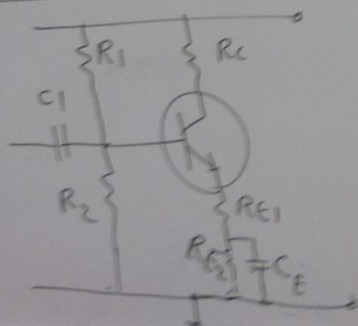


$$r_{in} = R_1 \parallel R_2 \parallel \beta(r_e + R_E)$$

## OUTPUT RESISTANCE

$$r_o = R_C$$

## ADDING THE EMITTER BYPASS CAPACITOR



$C_E$  = EMITTER BYPASS CAPACITOR

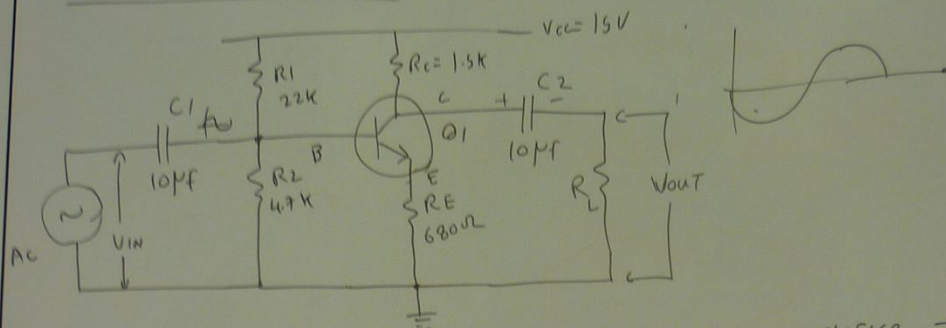
TO PROVIDE DC STABILITY



# TRANSISTOR - AMPLIFIER (PART 1)

AC CONDITIONS OF A TRANSISTOR AMPLIFIER

## COMMON EMITTER AMPLIFIER



THE ABOVE DIAGRAM SHOWS A COMMON EMITTER AMPLIFIER THAT USES POTENTIAL DIVIDER BIASING AND EMITTER STABILISING.

$C_1$  = INPUT COUPLING CAPACITOR,  $C_2$  = OUTPUT COUPLING CAPACITOR  
TO PREVENT DC VOLTAGE.

## AC CONDITIONS

THE AC CHARACTERISTICS OF CE-TRANSISTOR AMPLIFIER

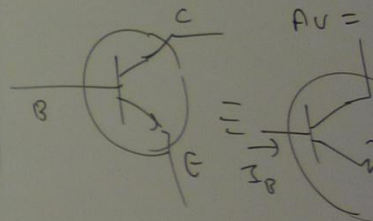
- VOLTAGE GAIN, INPUT RESISTANCE, OUTPUT RESISTANCE.

## CHARACTERISTICS OF

- THE INPUT SIGNAL
- THE VOLTAGE AT B
- THE OUTPUT TERMIN
- THE OUTPUT SIGNAL
- THE WAVE FORM A
- THE BASE.

VOLTAGE GAIN  $A_V$

$A_V =$



$$r_e = \frac{30 \text{ mV}}{I_c (\text{mA})}$$

## CHARACTERISTICS OF A TRANSISTOR AMPLIFIER



AMPLIFIER THAT USES  
INPUT COUPLING CAPACITOR

TRANSISTOR AMPLIFIER

OUTPUT RESISTANCE.

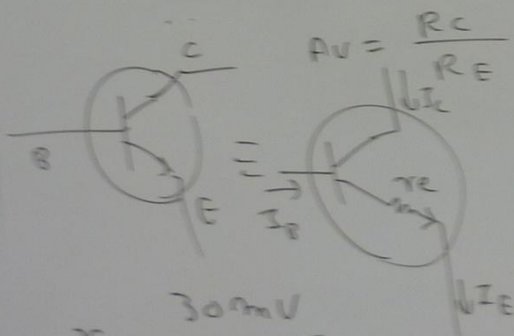
## CHARACTERISTICS OF CE AMPLIFIER

- THE INPUT SIGNAL HAS NO DC COMPONENT
- THE VOLTAGE AT BASE HAS BOTH DC & AC COMPONENT
- THE OUTPUT TERMINAL HAS NO DC COMPONENT
- THE OUTPUT SIGNAL IS 180° OUT OF PHASE WITH INPUT SIGNAL
- THE WAVE FORM AT EMITTER IS IDENTICAL TO THAT AT THE BASE.

VOLTAGE GAIN  $A_V$

$$A_V = \frac{V_o}{V_{in}}$$

$$A_V = \frac{R_C}{R_E}$$

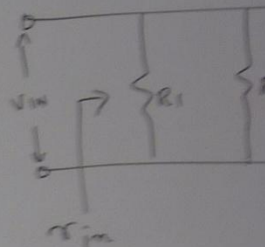


$$r_e = \frac{30 \text{ mV}}{I_C (\text{mA})}$$

$$A_V = \frac{R_C}{R_E + r_e}$$

$$A_V = \frac{R_C \parallel R_L}{R_E + r_e}$$

INPUT AND OUTPUT

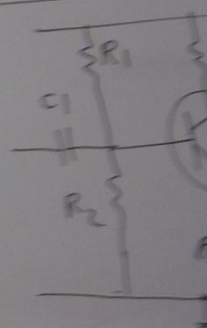


$$r_{in} = R_1 \parallel r_{in}$$

OUTPUT RESISTANCE

$$r_o = R_C$$

ADDING THE E





component

DC & AC component

component

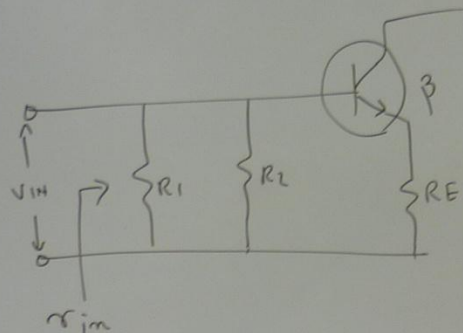
of phase with input signal

is identical to that at

$$A_v = \frac{R_C}{R_E + r_e}$$

$$A_v = \frac{R_C \parallel R_L}{R_E + r_e}$$

## INPUT AND OUTPUT RESISTANCE OF CE AMPLIFIER

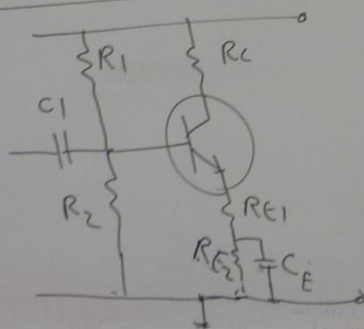


$$r_{in} = R_1 \parallel R_2 \parallel \beta(r_e + R_E)$$

## OUTPUT RESISTANCE

$$r_o = R_C$$

## ADDING THE EMITTER BYPASS CAPACITOR



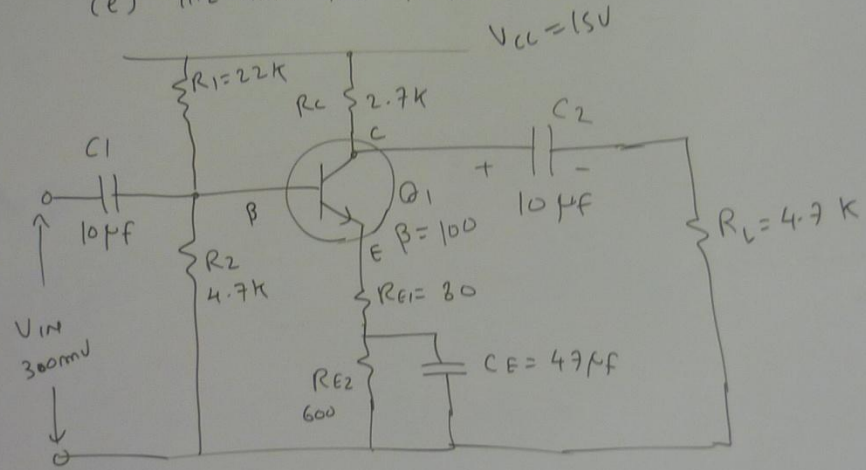
$C_E$  = EMITTER BYPASS CAPACITOR

TO PROVIDE DC STABILITY

Pb

FOR THE GIVEN CIRCUIT, CALCULATE THE FOLLOWINGS.

- THE DC VOLTAGE PRESENT AT BASE, EMITTER AND COLLECTOR OF  $Q_1$
- THE GAIN OF THE CIRCUIT
- THE INPUT RESISTANCE
- THE OUTPUT RESISTANCE
- THE GAIN AND INPUT RESISTANCE IF  $C_E$  IS DISCONNECTED.



$$(a) \quad V_B = \frac{V_{CC} R_2}{R_1 + R_2} = \frac{15 \times 4.7}{22 + 4.7} = 2.6 \text{ V}$$

$$V_E = V_B - V_{BE} = 2.6 - 0.6 = 2 \text{ V}$$

$$V_C = V_{CC} - I_C R_C, \quad I_C = I_E = \frac{V_E}{R_E}$$

$$I_C = I_E = \frac{2}{80 + 600}$$

$$V_C = V_{CC} - I_C R_C = 15 - 3 \times 10^{-3} \times 2.7 \times 10^3$$

$$r_e = \frac{30 \text{ mV}}{I_C (\text{mA})}$$

$$(b) \quad A_v = \frac{R_C}{r_e}$$

$$= \frac{2.7 \times 10^3}{30 \times 10^{-3}}$$

$$= 90$$

$$= 90$$

$$= 90$$

$$= 90$$

$$= 90$$



$$I_c = I_E = \frac{2}{80+600} = 3 \text{ mA}$$

$$V_c = V_{cc} - I_c R_c = 15 - 3 \times 10^{-3} \times 2.7 \times 10^3 = 6.9 \text{ V}$$

$$r_e = \frac{30 \text{ mV}}{I_c (\text{mA})} = \frac{30 \text{ mV}}{3 \text{ mA}} = 10 \Omega$$

$$(b) A_v = \frac{R_c \parallel R_L}{r_e + R_{E1}} = \frac{2.7 \text{ k} \parallel 4.7 \text{ k}}{10 + 80} = \frac{2.7 \times 4.7}{2.7 + 4.7} = \frac{12.715}{90}$$

$$= \frac{1.715 \text{ k}\Omega}{90} = 19$$

$$(c) r_{im} = R_1 \parallel R_2 \parallel \beta(r_e + R_{E1})$$

$$\frac{1}{r_{im}} = \frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{\beta(r_e + R_{E1})}$$

$$r_{im} = \frac{1}{\frac{1}{22 \times 10^3} + \frac{1}{4.7 \times 10^3} + \frac{1}{100(10 + 80)}} = 2710 \Omega = 2.71 \text{ k}\Omega$$

$$(d) R_o = R_c = 2.7 \text{ k}\Omega$$

$$(e) r_{in} = \frac{1}{\frac{1}{22 \times 10^3} + \frac{1}{4.7 \times 10^3} + \frac{1}{100(10 + R_{E1} + R_{E2})}}$$

*C<sub>E</sub> REMOVED*

$r_{im}$   
WITHOUT  
 $C_E$

$$(c) r_{in} = R_1 \parallel R_2 \parallel \beta(r_e + R_{E1})$$

$$\frac{1}{r_{in}} = \frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{\beta(r_e + R_{E1})}$$

$$r_{in} = \frac{1}{\frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{\beta(r_e + R_{E1})}}$$

$$= \frac{1}{\frac{1}{22 \times 10^3} + \frac{1}{4.7 \times 10^3} + \frac{1}{100(10 + 80)}}$$

$$= 2710 \Omega = 2.71 \text{ k}\Omega$$

$$(d) R_o = R_c = 2.7 \text{ k}\Omega$$

$$(e) r_{in} =$$

$C_E$  REMOVED

$$\frac{1}{22 \times 10^3} + \frac{1}{4.7 \times 10^3} + \frac{1}{100(10 + R_{E1} + R_{E2})}$$

$$r_{in} \text{ WITHOUT } C_E = \frac{1}{\frac{1}{22 \times 10^3} + \frac{1}{4.7 \times 10^3} + \frac{1}{100(10 + 80 + 600)}}$$

$$= 3.7 \text{ k}\Omega$$



$$r_{in} = R_1 \parallel R_2 \parallel \beta(r_e + R_{E1})$$

$$= \frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{\beta(r_e + R_{E1})}$$

$$= \frac{1}{22 \times 10^3} + \frac{1}{4.7 \times 10^3} + \frac{1}{100(10 + 80)}$$

$$= 2710 \Omega = 2.71 k\Omega$$

$$= R_c = 2.7 k$$

$$r_{in} = \frac{1}{\frac{1}{22 \times 10^3} + \frac{1}{4.7 \times 10^3} + \frac{1}{100(10 + R_{E1} + R_{E2})}}$$

$$r_{im} = \frac{1}{\frac{1}{22 \times 10^3} + \frac{1}{4.7 \times 10^3} + \frac{1}{100(10 + 80 + 600)}}$$

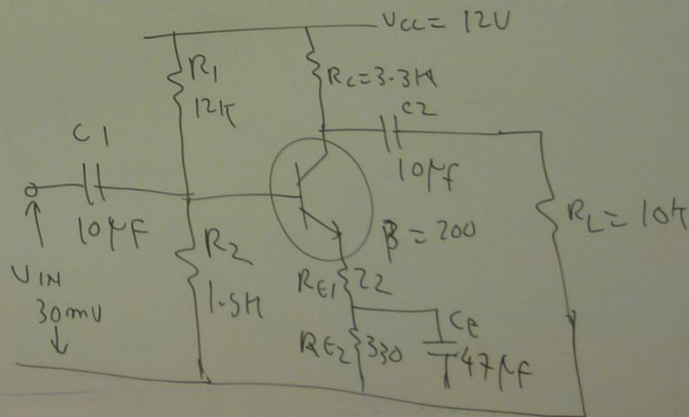
without CE

$$= 3.7 k\Omega$$

### EXERCISE

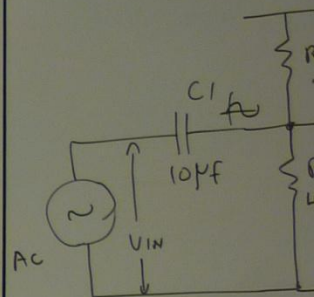
FOR THE GIVEN CIRCUIT, CALCULATE THE FOLLOWINGS.

- (a)  $V_B$  AND  $V_E$  (b)  $I_c$  (c)  $V_c$  (d)  $r_e$   
 (e) VOLTAGE GAIN  $A_v$  (f) INPUT RESISTANCE  $r_{in}$   
 (g) OUTPUT RESISTANCE  $r_o$  (h) OUTPUT VOLTAGE  $V_o$



### TRANSISTOR -

### COMMON EMITTER



THE ABOVE DIAGRAM IS A POTENTIAL DIVIDER

$C_1$  = INPUT CAPACITOR TO

AC CONDITIONS

THE AC CIRCUIT - VOLTAGE

$$\frac{1}{\beta(r_e + R_{E1})} + \frac{1}{R_2} + \frac{1}{\beta(r_e + R_{E1})}$$

$$\frac{1}{R_2} + \frac{1}{\beta(r_e + R_{E1})}$$

$$\frac{1}{4.7 \times 10^3} + \frac{1}{100(10 + 80)}$$

$$10 \Omega = 2.71 \text{ k}\Omega$$

$$2.7 \text{ k}$$

$$\frac{1}{22 \times 10^3} + \frac{1}{4.7 \times 10^3} + \frac{1}{100(10 + R_{E1} + R_{E2})}$$

$$r_{im} = \frac{1}{\frac{1}{22 \times 10^3} + \frac{1}{4.7 \times 10^3} + \frac{1}{100(10 + 80 + 600)}}$$

$$= 3.7 \text{ k}\Omega$$

### EXERCISE

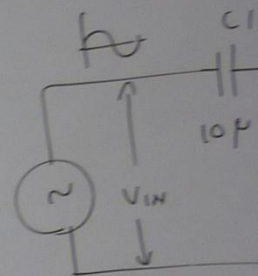
FOR THE GIVEN CIRCUIT, CALCULATE THE FOLLOWINGS.

- (a)  $V_B$  AND  $V_E$  (b)  $I_C$  (c)  $V_C$  (d)  $r_e$   
 (e) VOLTAGE GAIN  $A_V$  (f) INPUT RESISTANCE  $r_{in}$   
 (g) OUTPUT RESISTANCE  $r_o$  (h) OUTPUT VOLTAGE  $V_o$



CE-AMPLIFIER

CC-AMPLIFIER



$$V_B = \frac{V}{10}$$

$$V_E = V$$

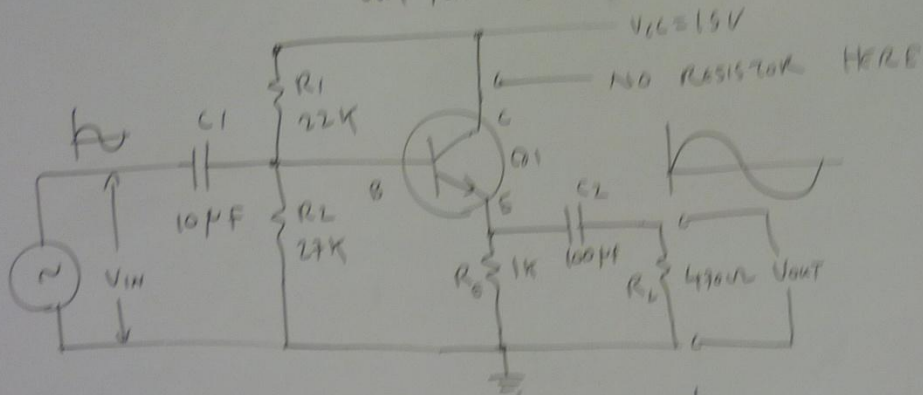
$$I_E = I$$



# TRANSISTOR AMPLIFIER (PART - 2) - Common Collector (C.C) Amplifier & Common Base (C.B) Amplifier

C.B - AMPLIFIER  $\rightarrow$  INPUT & OUTPUT SIGNALS ARE 180 OUT OF PHASE

C.C - AMPLIFIER  $\rightarrow$  INPUT & OUTPUT SIGNALS ARE IN PHASE  
OUTPUT TERMINAL HAS NO DC VOLTAGE



$$V_B = \frac{V_{CC} R_2}{R_1 + R_2}$$

$$V_E = V_B - V_{BE}$$

$$I_E \approx I_C = \frac{V_E}{R_E}$$

$$V_C \approx V_{CC}$$

$$r_e = \frac{30 \text{ mV}}{I_C}$$

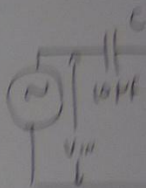
VOLTAGE GAIN

$$A_v = 1$$

$$r_{in} = R_1 || R_2 || [\beta (r_e + R_E || R_L)]$$

$$r_o = r_e$$

ph for  
(a)  $r_e$   
(d) OUT P



(a)  $r_e =$

$30 =$

$V_B =$

$V_E =$

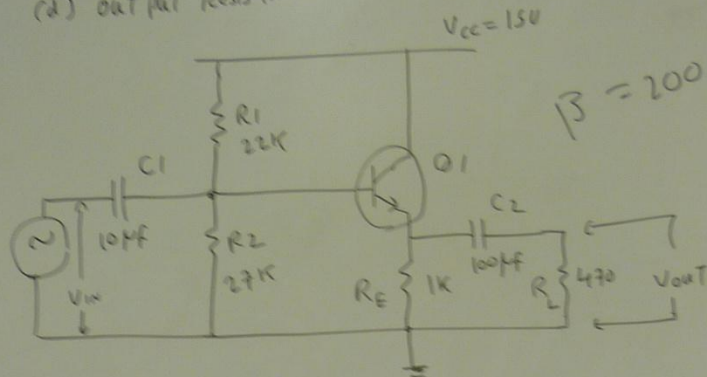
AMPLIFIER  
AMPLIFIER

180 OUT OF PHASE

PHASE

OUTAGE

PROBLEM FOR THE GIVEN CIRCUIT, CALCULATE  
(a)  $r_e$  (b) THE GAIN OF THE CIRCUIT (c) INPUT RESISTANCE  
(d) OUTPUT RESISTANCE



$$(a) r_e = \frac{30 \text{ mV}}{I_c}$$

$$I_c = I_E = \frac{V_E}{R_E}$$

$$V_E = V_B - V_{BE}$$

$$V_B = \frac{V_{CC} R_2}{R_1 + R_2}$$

$$r_{in} = \beta (r_e + R_E \parallel R_L)$$

$$V_B = \frac{15 \times 27}{22 + 27} = 8.3 \text{ V}$$

$$V_E = V_B - V_{BE} = 8.3 - 0.6 = 7.7 \text{ V}$$

$$I_E = I_C = \frac{V_E}{R_E} = \frac{7.7}{1 \text{ K}} = 7.7 \text{ mA}$$

$$r_e = \frac{30 \text{ mV}}{I_c} = \frac{30 \text{ mV}}{7.7 \text{ mA}} = 3.9 \Omega$$

$$(b) \text{ GAIN} = A_v =$$

$$(c) r_{in} = R_1 \parallel$$

$$r_{in} = \frac{1}{\frac{1}{R_1} +}$$

$$= \frac{1}{22 \times 10^3}$$

$$= 10$$

$$(d) r_o =$$



INPUT RESISTANCE

200

7

V<sub>OUT</sub>

27

27

8E

0.6 = 7.7V

E = 7.7

R<sub>E</sub> = 1k

mV

30 mV

7.7 mA

$$(b) \text{ GAIN} = A_v = 1$$

$$(c) r_{im} = R_1 \parallel R_2 \parallel \beta (r_e + R_E \parallel R_L)$$

$$r_{im} = \frac{1}{\frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{\beta (r_e + \frac{R_E R_L}{R_E + R_L})}}$$

$$= \frac{1}{\frac{1}{22 \times 10^3} + \frac{1}{27 \times 10^3} + \frac{1}{200 \left( 3.9 + \frac{1 \times 10^3 \times 470}{1 \times 10^3 + 470} \right)}}$$

$$= 10.2 \text{ k}\Omega$$

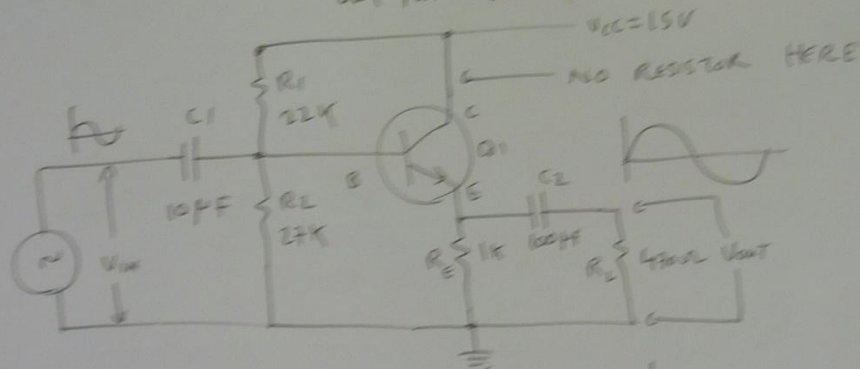
$$(d) r_o = r_e = 3.9 \Omega$$

# TRANSISTOR AMPLIFIER (PART - 2) - COMMON COLLECTOR (C.C) AMPLIFIER & COMMON BASE (C.B) AMPLIFIER

C.E. - AMPLIFIER  $\rightarrow$  INPUT & OUTPUT SIGNALS ARE 180 OUT OF PHASE

C.C. - AMPLIFIER  $\rightarrow$  INPUT & OUTPUT SIGNALS ARE IN PHASE

OUTPUT TERMINAL HAS NO DC VOLTAGE



$$V_B = \frac{V_{CC} R_2}{R_1 + R_2}$$

$$V_E = V_B - V_{BE}$$

$$I_E = I_C = \frac{V_E}{R_E}$$

$$V_{CE} = V_{CC}$$

$$r_{ce} = \frac{30mV}{I_C}$$

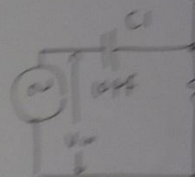
VOLTAGE GAIN

$$A_V = 1$$

$$r_{in} = R_1 || R_2 || [\beta (r_{ce} + R_E || R_L)]$$

$$r_o = r_{ce}$$

pg for the  
 (a) TE (b)  
 (d) OUTPUT



$$(a) r_{ce} = \frac{30mV}{I_C}$$

$$I_C = I_E$$

$$V_E = V_B$$

$$V_B = \frac{V_{CC}}{R_1 + R_2}$$



c) AMPLIFIER  
c. B) AMPLIFIER

180° OUT OF PHASE

IN PHASE

VOLTAGE

E

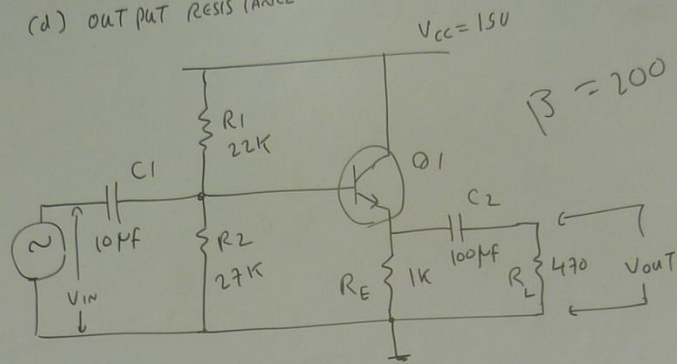
IN

1

$$\left| R_2 \right| \parallel \left[ \beta (r_e + R_E \parallel R_L) \right]$$

$r_e$

ph) FOR THE GIVEN CIRCUIT, CALCULATE  
(a)  $r_e$  (b) THE GAIN OF THE CIRCUIT (c) INPUT RESISTANCE  
(d) OUTPUT RESISTANCE



$$(a) r_e = \frac{30 \text{ mV}}{I_C}$$

$$I_C = I_E = \frac{V_E}{R_E}$$

$$V_E = V_B - V_{BE}$$

$$V_B = \frac{V_{CC} R_2}{R_1 + R_2}$$

$$V_B = \frac{15 \times 27}{22 + 27} = 8.3 \text{ V}$$

$$V_E = V_B - V_{BE} = 8.3 - 0.6 = 7.7 \text{ V}$$

$$I_E = I_C = \frac{V_E}{R_E} = \frac{7.7}{1 \text{ k}} = 7.7 \text{ mA}$$

$$r_e = \frac{30 \text{ mV}}{I_C} = \frac{30 \text{ mV}}{7.7 \text{ mA}} = 3.9 \Omega$$

$$(b) \text{ GAIN} = A_v = 1$$

$$(c) r_{in} = R_1 \parallel R_2$$

$$r_{in} = \frac{1}{\frac{1}{R_1} + \frac{1}{R_2}}$$

$$= \frac{1}{\frac{1}{22 \times 10^3}}$$

$$= 10$$

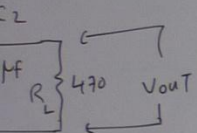
$$(d) r_o =$$

ULATE

CIRCUIT (C) INPUT RESISTANCE

= 15V

$$\beta = 200$$



$$= \frac{15 \times 27}{22 + 27} = 8.3V$$

$$= V_B - V_{BE} = 8.3 - 0.6 = 7.7V$$

$$= I_C = \frac{V_E}{R_E} = \frac{7.7}{1k} = 7.7mA$$

$$r_e = \frac{30mV}{I_C} = \frac{30mV}{7.7mA} = 3.9\Omega$$

$$(b) GAIN = A_V = 1$$

$$(c) r_{in} = R_1 \parallel R_2 \parallel \beta (r_e + R_E \parallel R_L)$$

$$r_{in} = \frac{1}{\frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{\beta \left( r_e + \frac{R_E R_L}{R_E + R_L} \right)}}$$

$$= \frac{1}{\frac{1}{22 \times 10^3} + \frac{1}{27 \times 10^3} + \frac{1}{200 \left( 3.9 + \frac{1 \times 10^3 \times 470}{1 \times 10^3 + 470} \right)}}$$

$$= 10.2k\Omega$$

$$(d) r_o = r_e = 3.9\Omega$$



## APPLICATION OF CE & CC AMPLIFIER

CE AMPLIFIER  $\longrightarrow$  AMPLIFICATION

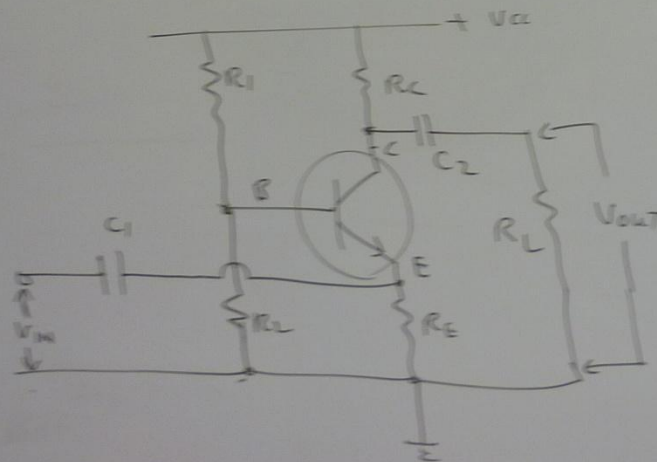
CC AMPLIFIER  $\longrightarrow$  BUFFER

BECAUSE  $A_V = 1$

## THE COMMON BASE (C.B) AMPLIFIER

- NO CURRENT GAIN
- ONLY VOLTAGE GAIN

THE INPUT SIGNAL IS  
APPLIED TO EMITTER  
AND OUTPUT IS TAKEN  
OUT FROM COLLECTOR



COMPARISON

CC AMPLIFIER

CONFIGURATION

CE

CC

CB

COMPARISON OF  $C_E$ ,  $C_C$  AND  
 $C_B$  AMPLIFIERS

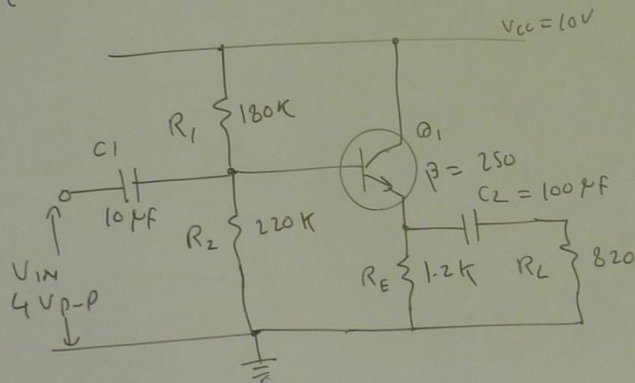
CON FIGURATION	IN PUT RESISTANCE	OUT PUT RESISTANCE	PHASE SHIFT	VOLTAGE GAIN ( $A_V$ )	CURRENT GAIN ( $A_I$ )	POWER GAIN $A_P$	SPECIAL FEATURES
$C_E$	MEDIUM	MEDIUM ( $-R_C$ )	$180$	HIGH	MEDIUM	HIGH	$R_E$ STABILIZER, AMPLIFICATION
$C_C$	HIGH	LOW ( $-r_{e_c}$ )	$0$	UNITY	HIGH	HIGH	BUFFER, $A_V=1$
$C_B$	LOW	MEDIUM ( $-R_C$ )	$0$	MEDIUM	UNITY	MEDIUM	NO $A_I$ ONLY $A_V$



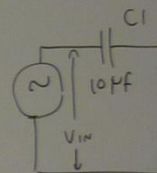
# EXERCISE

FOR THE GIVEN CIRCUIT, CALCULATE THE FOLLOWINGS

- |                        |                               |
|------------------------|-------------------------------|
| (a) $V_B$ AND $V_E$    | (e) INPUT RESISTANCE $r_{in}$ |
| (b) $I_C$              | (f) OUTPUT RESISTANCE $r_o$   |
| (c) $r_e$              | (g) OUTPUT VOLTAGE $V_o$      |
| (d) VOLTAGE GAIN $A_v$ |                               |



ph FOR TH  
(a)  $r_e$   
(d) OUTPUT



(a)  $r_e =$   
 $I_C =$   
 $V_E =$   
 $V_B =$