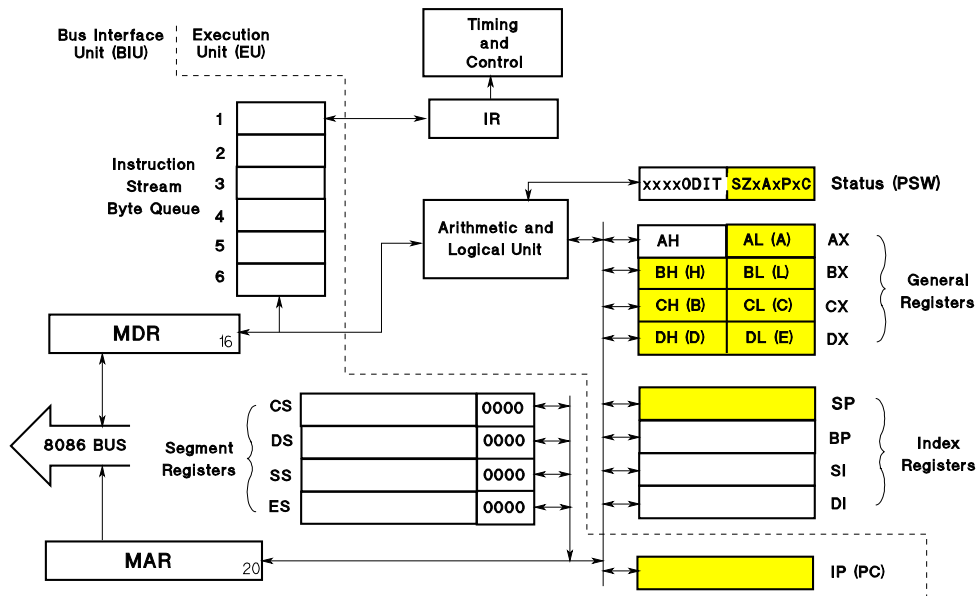


## Programmer's Model of the 8086 Microprocessor



## 8086 Hardware Relocation

*How to make 16 bits address a megabyte of memory*

|                           |                |     |
|---------------------------|----------------|-----|
| Offset (from instruction) | 16 bits        | 64K |
| Segment Register Value    | + 16 bits 0000 |     |
| Physical Address          | 20 bits        | 1Mb |

*The 8086 has a 16 bit "Logical Address Space,"  
and a 20 bit "Physical Address Space"*

M3S30017

EE for EE